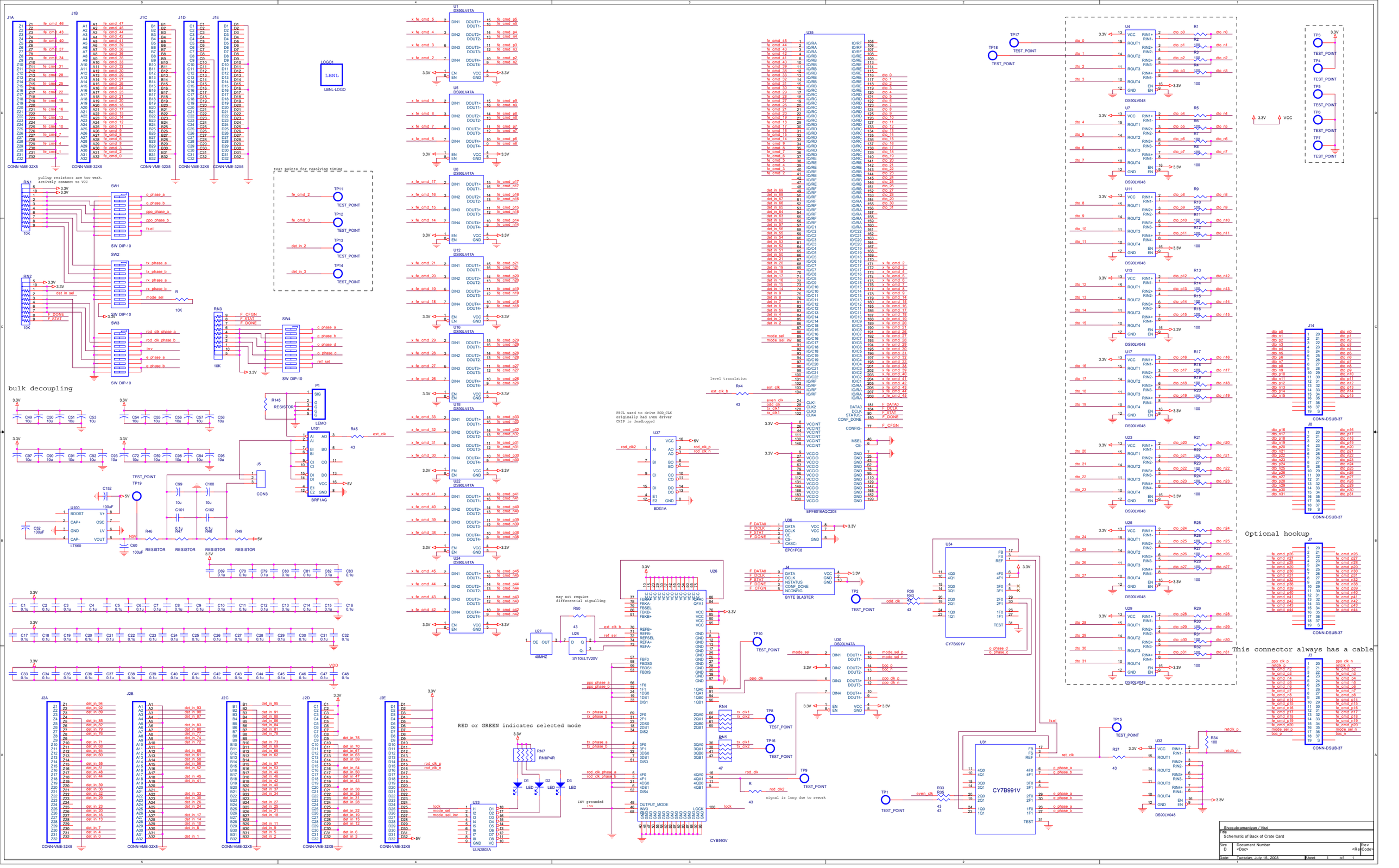
J1A
J1B
J1C
J1D
J1E
CONN-VME-32X5
LOGO1
LBNL
LBNL-LOGO
pullup resistors are too weak.
actively connect to VCC
RN1
RN2
RN3
SW1
SW2
SW3
SW4
SW DIP-10
10K
R
bulk decoupling
C49 10u
C50 10u
C51 10u
C53 10u
C54 10u
C55 10u
C56 10u
C57 10u
C58 10u
C97 10u
C90 10u
C91 10u
C92 10u
C93 10u
C72 10u
C59 10u
C98 10u
C94 10u
C95 10u
test points for resolving timing
TP11
TP12
TP13
TP14
TEST_POINT
LEMO
P1
R145
RESISTOR
U101
R45
ext_clk
J5
CON3
TP19
C152
100uF
U100
LT660
C52 100uF
C60 100uF
C99 10u
C100 10u
C101 0.1u
C102 0.1u
C47 0.1u
C48 0.1u
R46
R47
R48
R49
BRF1AG
C69 0.1u
C70 0.1u
C79 0.1u
C80 0.1u
C81 0.1u
C82 0.1u
C83 0.1u
C1 0.1u
C2 0.1u
C3 0.1u
C4 0.1u
C5 0.1u
C6 0.1u
C7 0.1u
C8 0.1u
C9 0.1u
C10 0.1u
C11 0.1u
C12 0.1u
C13 0.1u
C14 0.1u
C15 0.1u
C16 0.1u
C17 0.1u
C18 0.1u
C19 0.1u
C20 0.1u
C21 0.1u
C22 0.1u
C23 0.1u
C24 0.1u
C25 0.1u
C26 0.1u
C27 0.1u
C28 0.1u
C29 0.1u
C30 0.1u
C31 0.1u
C32 0.1u
C33 0.1u
C34 0.1u
C35 0.1u
C36 0.1u
C37 0.1u
C38 0.1u
C39 0.1u
C40 0.1u
C41 0.1u
C42 0.1u
C43 0.1u
C44 0.1u
C45 0.1u
C46 0.1u
VDD
J2A
J2B
J2C
J2D
J2E
RED or GREEN indicates selected mode
RN7
RN8P4R
D1 LED
D2 LED
D3 LED
U33
ULN2803A
U1
U5
U8
U12
U16
U18
U22
U24
DS90LV47A
U37
BDG1A
PECL used to drive ROD_CLK
originally had LVDS driver
CHIP is deadbugged
U35
EPF6016AQC208
U36
EPC1PC8
J4
BYTE BLASTER
U26
CYB993V
U27
40MHZ
U28
SY10ELTV20V
may not require
differential signalling
U30
U31
U34
CY7B991V
U4
U7
U11
U13
U17
U23
U25
U29
U32
DS90LV048
R1
R2
R3
R4
R5
R7
R8
R9
R10
R11
R13
R14
R15
R17
R19
R20
R21
R22
R23
R24
R25
R26
R27
R28
R29
R31
R32
R33
R34
R35
R36
R37
R43
R44
R46
R47
R48
R49
R50
level translation
TP17
TP18
TP1
TP2
TP3
TP4
TP5
TP6
TP7
TP8
TP9
TP10
TP15
TP16
J14
CONN-DSUB-37
J8
J7
J3
Optional hookup
This connector always has a cable
signal is long due to rework
Sivasubramaniyan / Virzi
Schematic of Back of Crate Card
Size D
Document Number
<Doc>
Rev
<RevCode>
Date: Tuesday, July 15, 2003
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