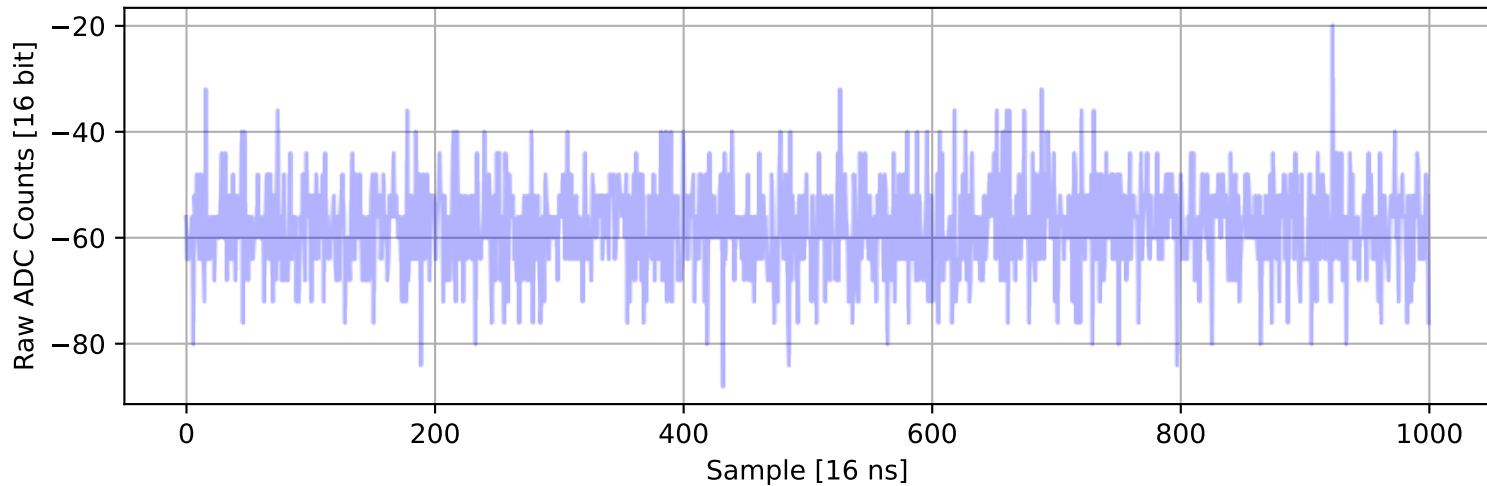
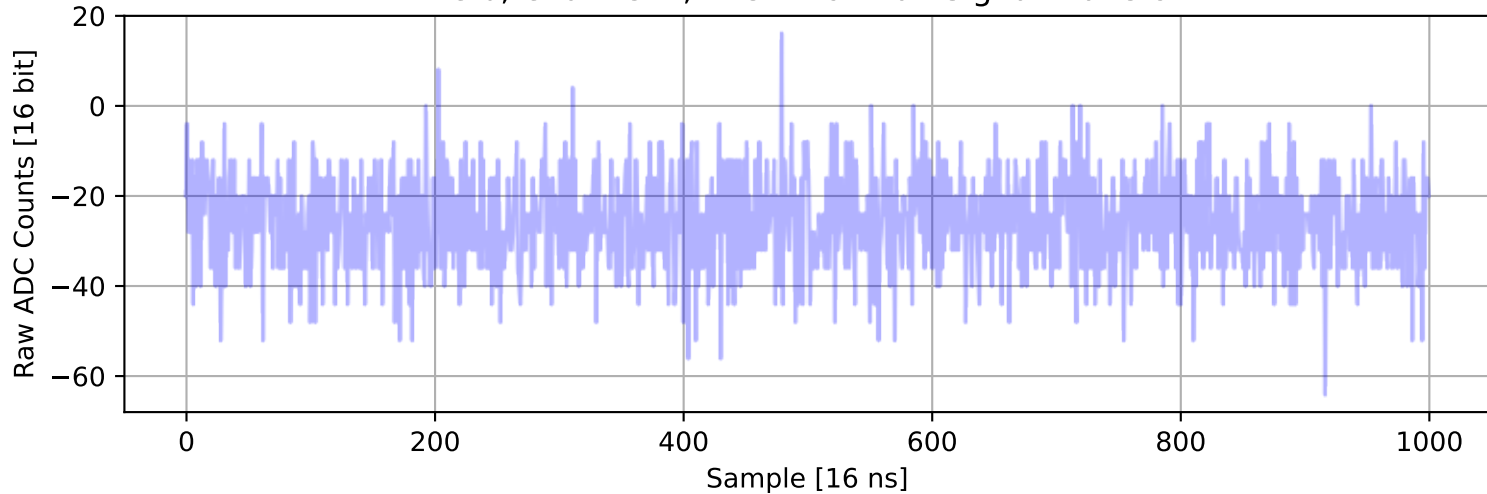


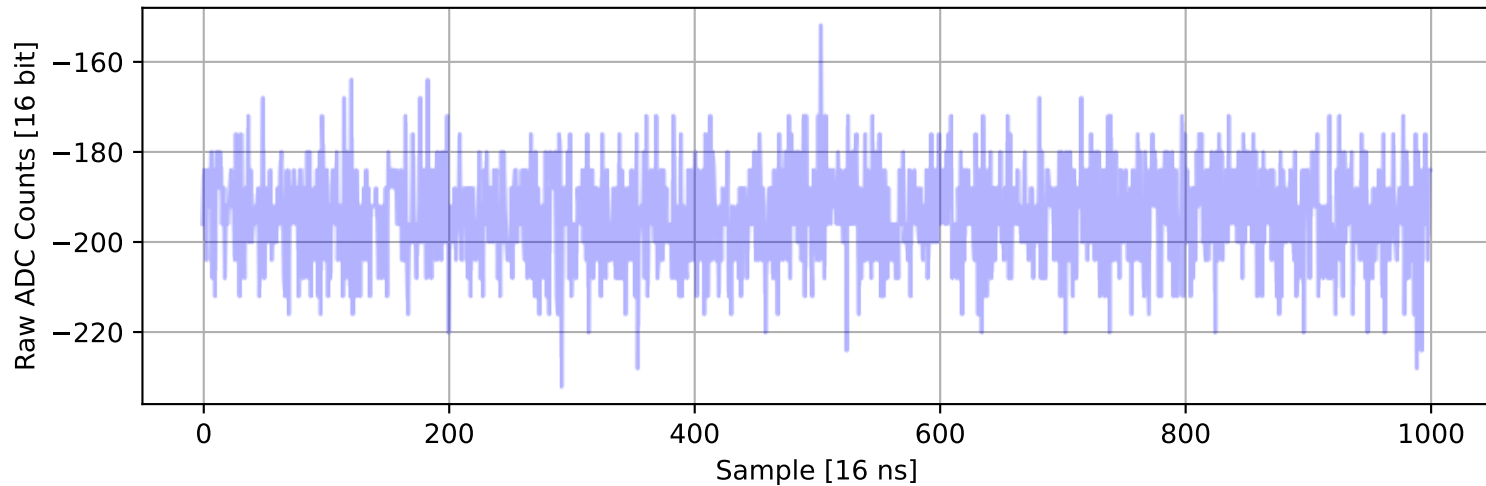
ADC 0, Channel 0, Event 14: Max Signal Waveform



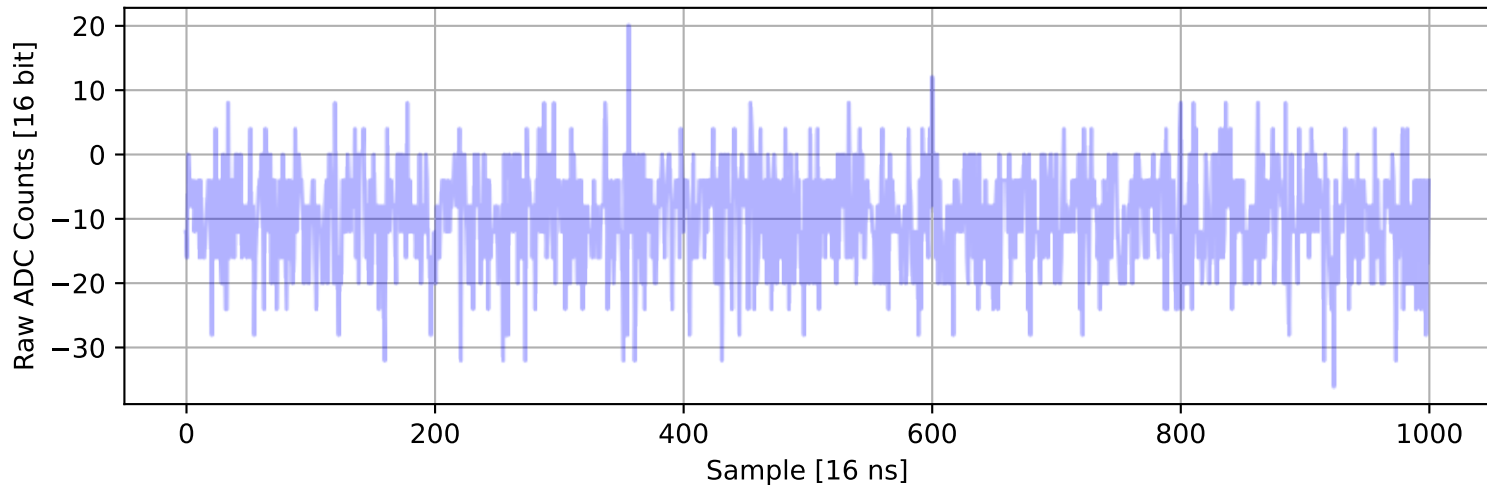
ADC 0, Channel 1, Event 19: Max Signal Waveform



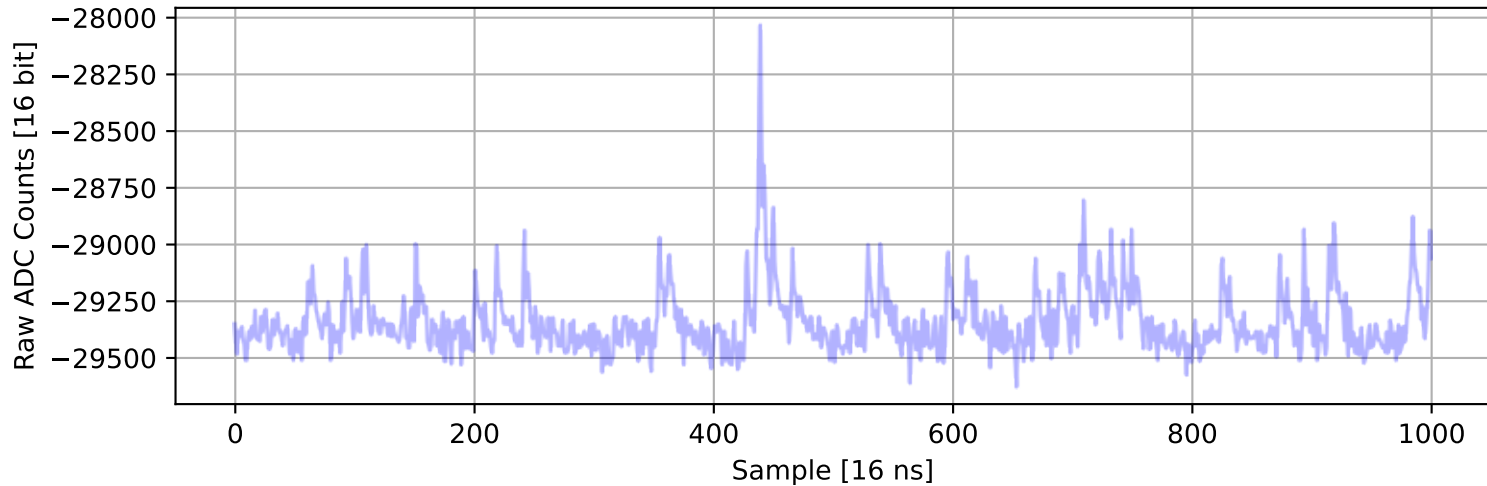
ADC 0, Channel 2, Event 31: Max Signal Waveform



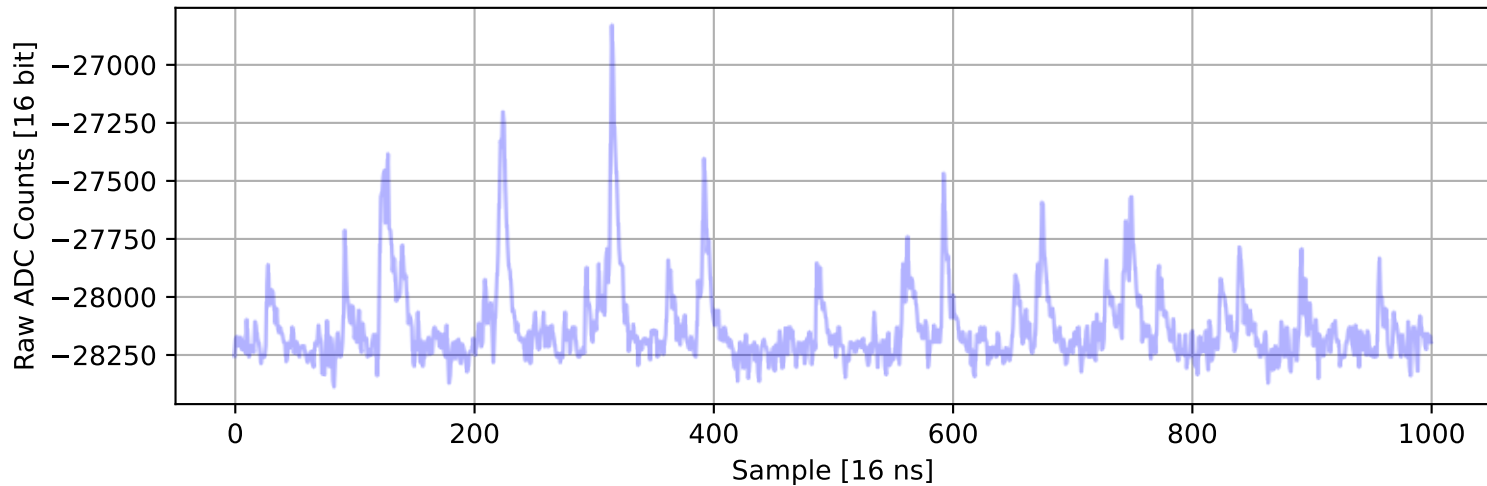
ADC 0, Channel 3, Event 4: Max Signal Waveform



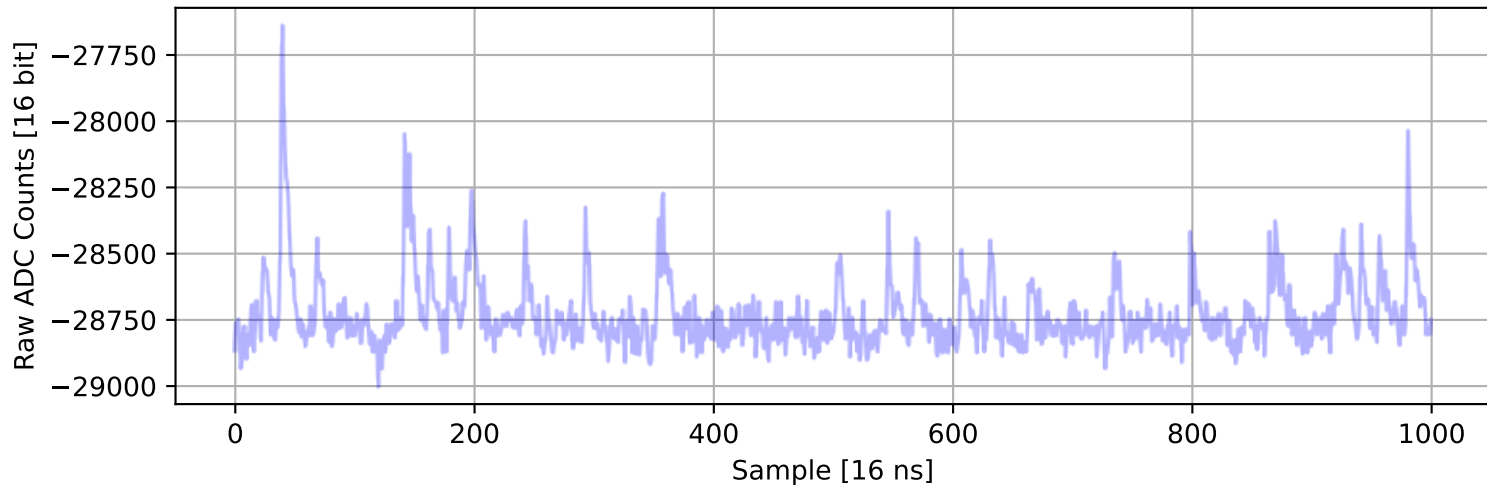
ADC 0, Channel 4, Event 21: Max Signal Waveform



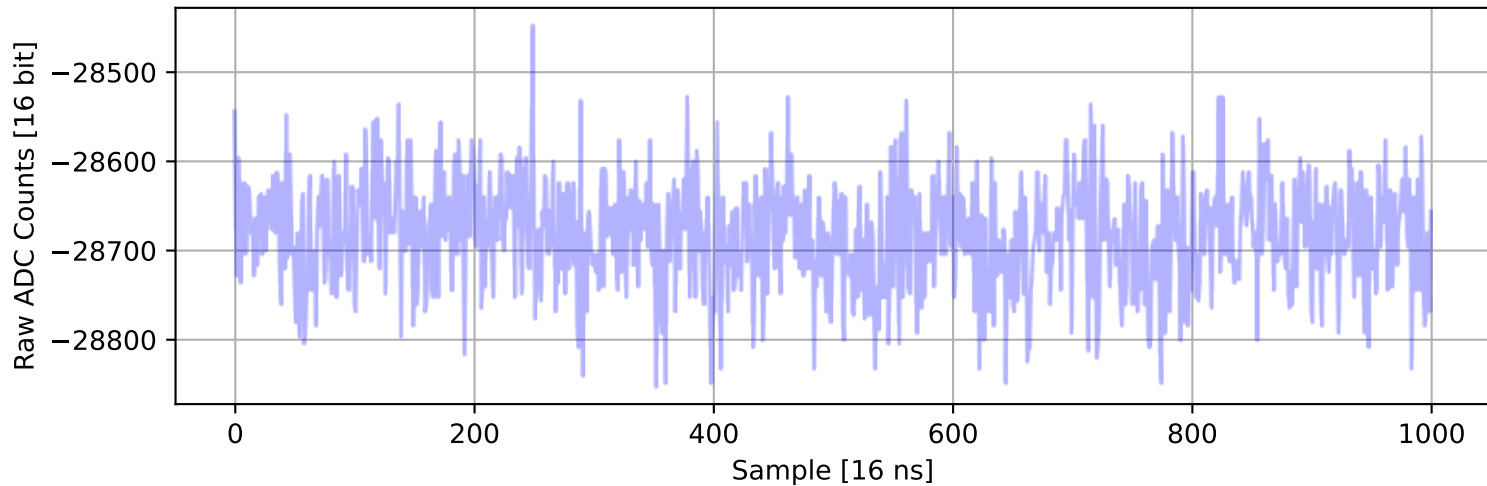
ADC 0, Channel 5, Event 26: Max Signal Waveform



ADC 0, Channel 6, Event 46: Max Signal Waveform

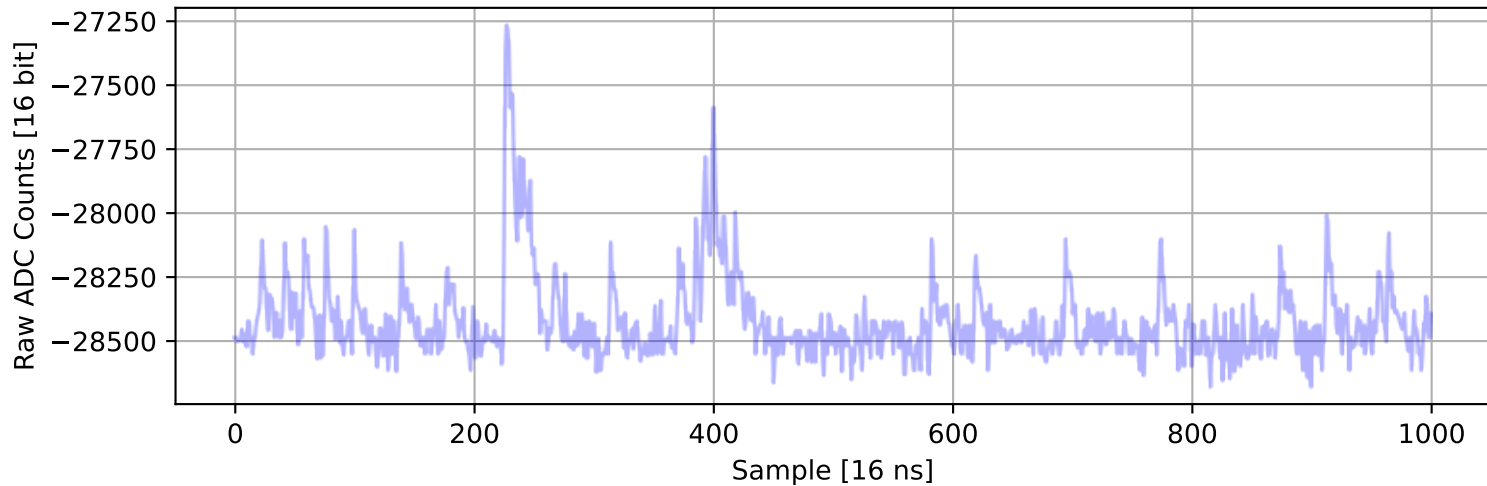


ADC 0, Channel 7, Event 5: Max Signal Waveform

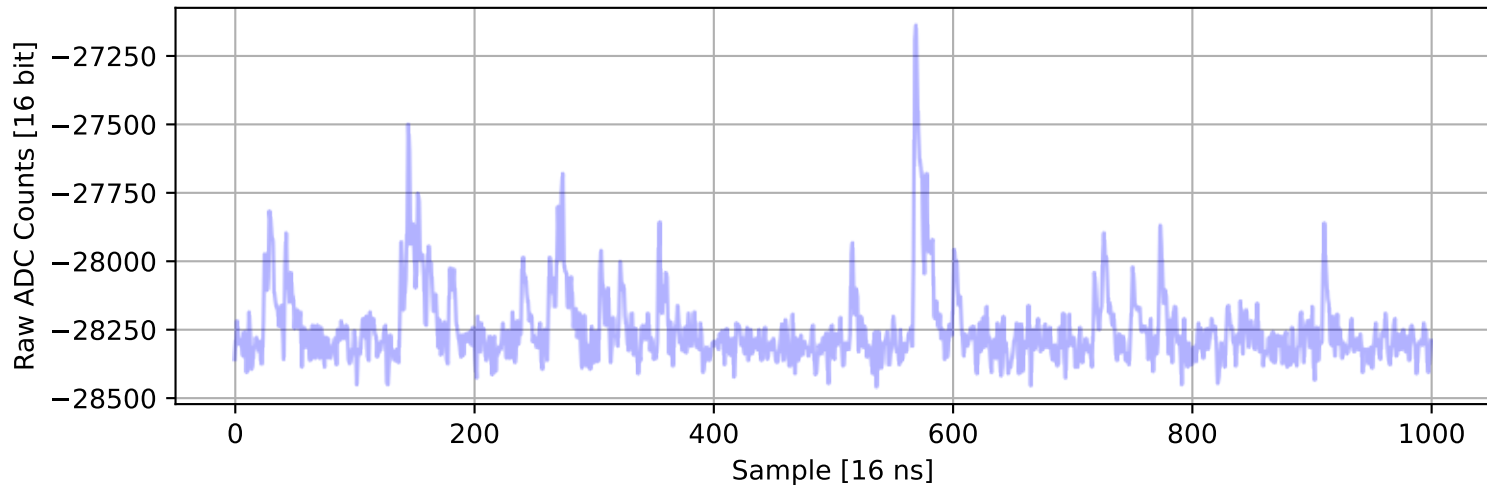




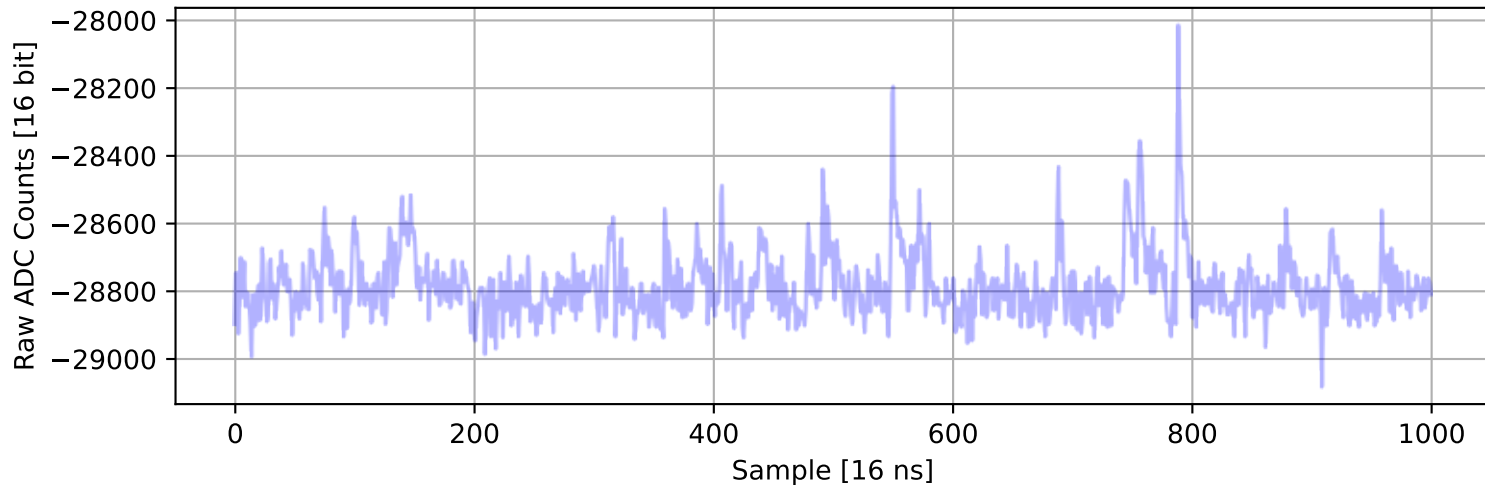
ADC 0, Channel 8, Event 22: Max Signal Waveform



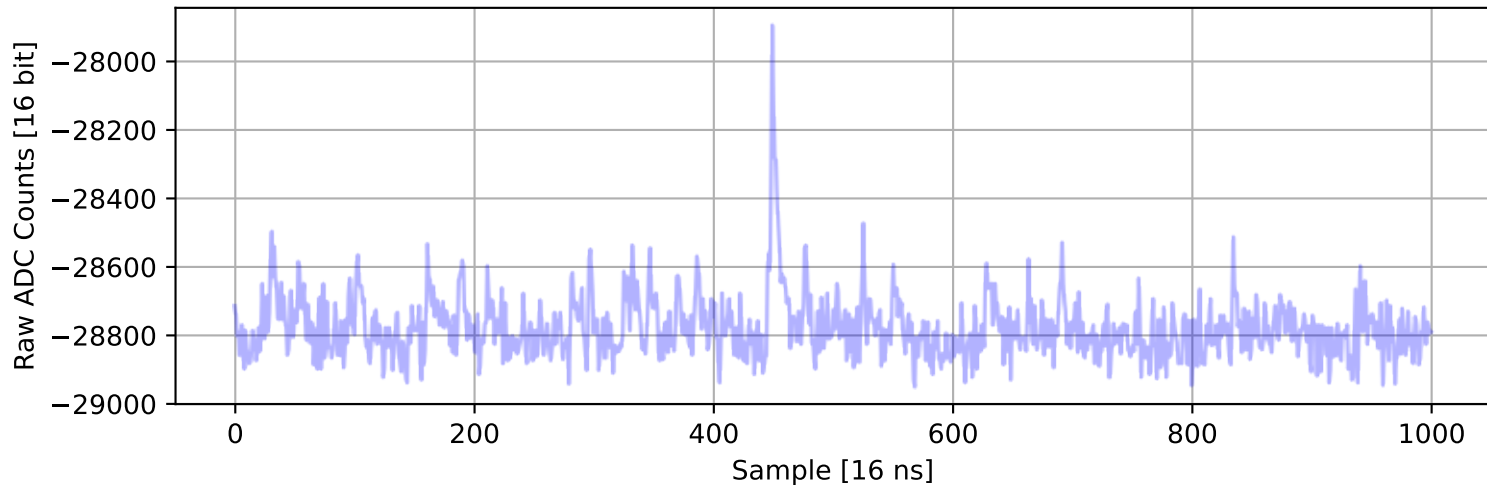
ADC 0, Channel 9, Event 26: Max Signal Waveform



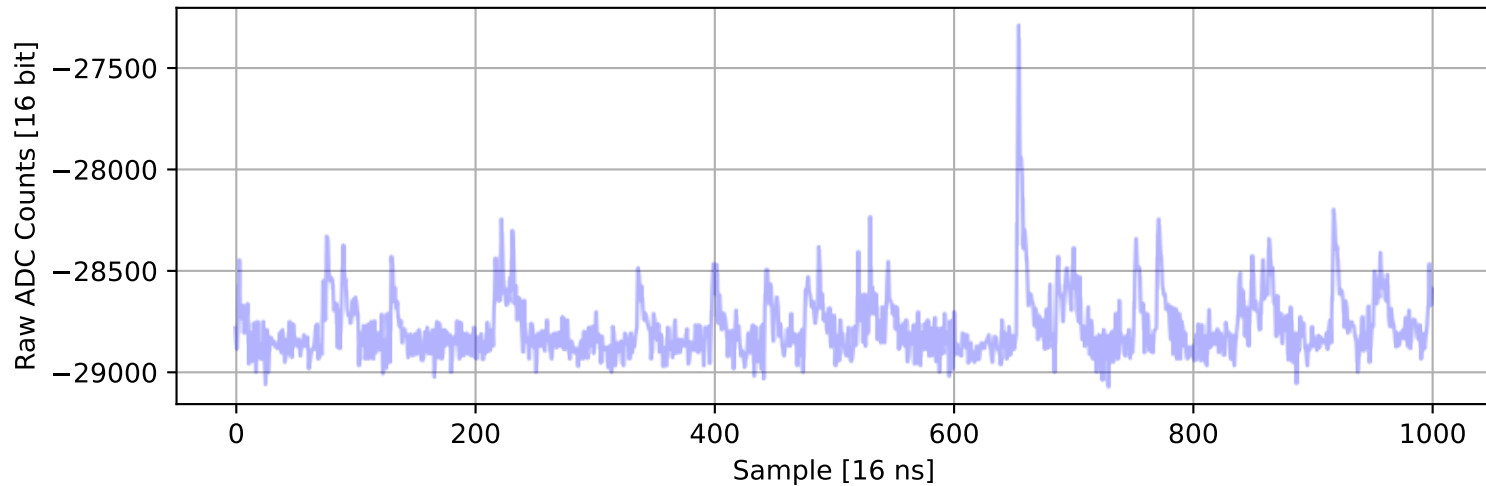
ADC 0, Channel 10, Event 7: Max Signal Waveform



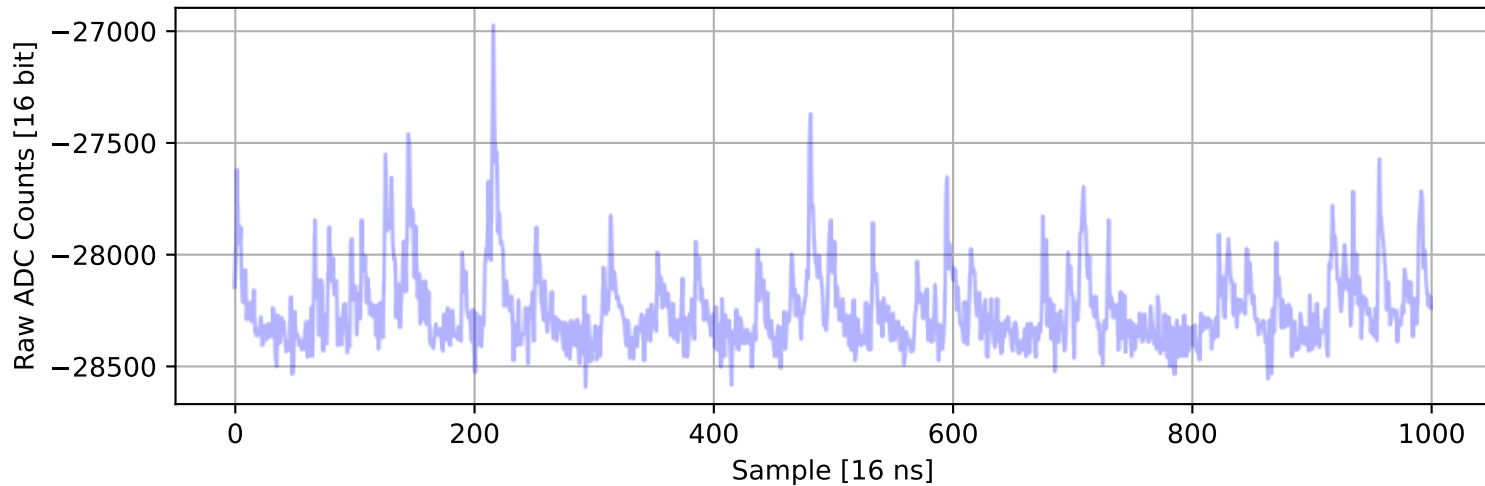
ADC 0, Channel 11, Event 35: Max Signal Waveform



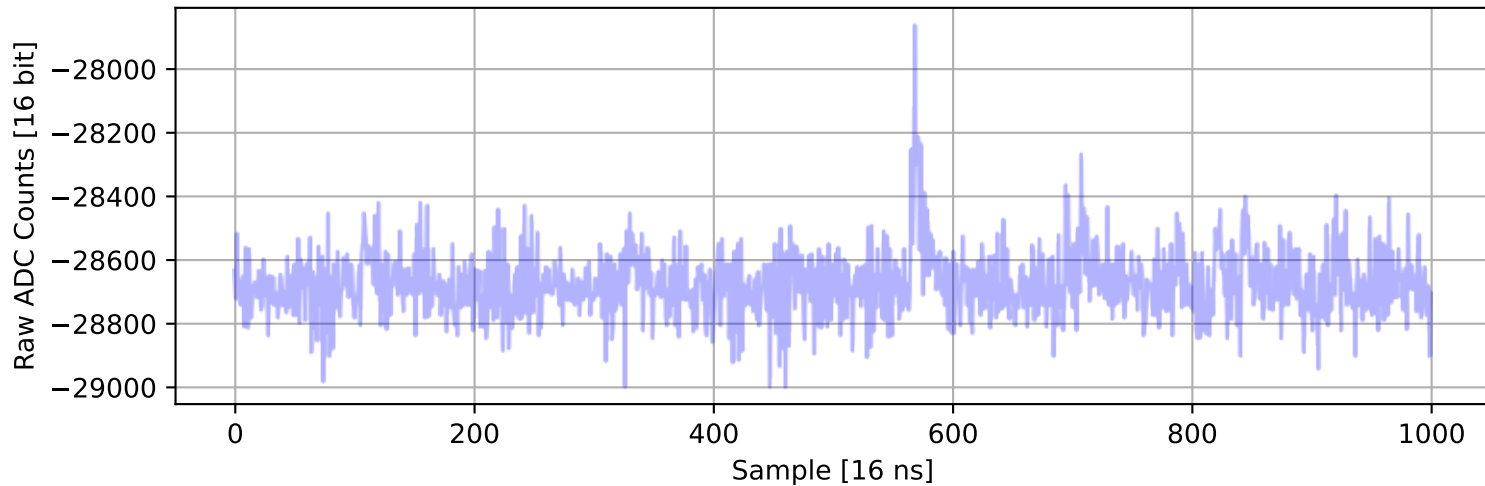
ADC 0, Channel 12, Event 15: Max Signal Waveform



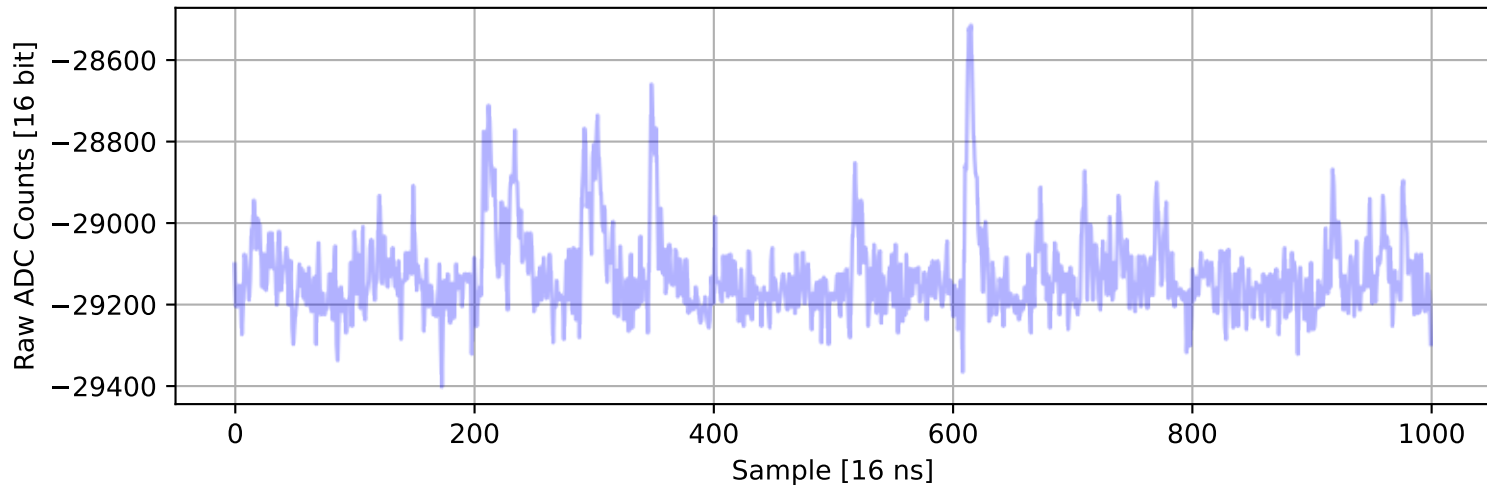
ADC 0, Channel 13, Event 62: Max Signal Waveform



ADC 0, Channel 14, Event 3: Max Signal Waveform

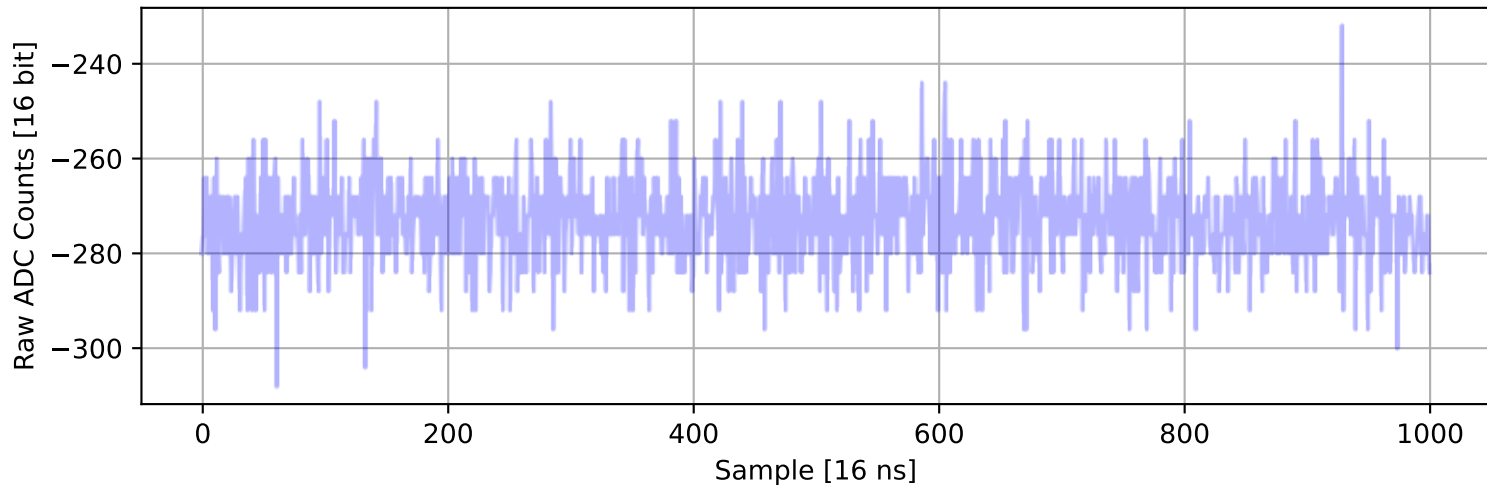


ADC 0, Channel 15, Event 0: Max Signal Waveform

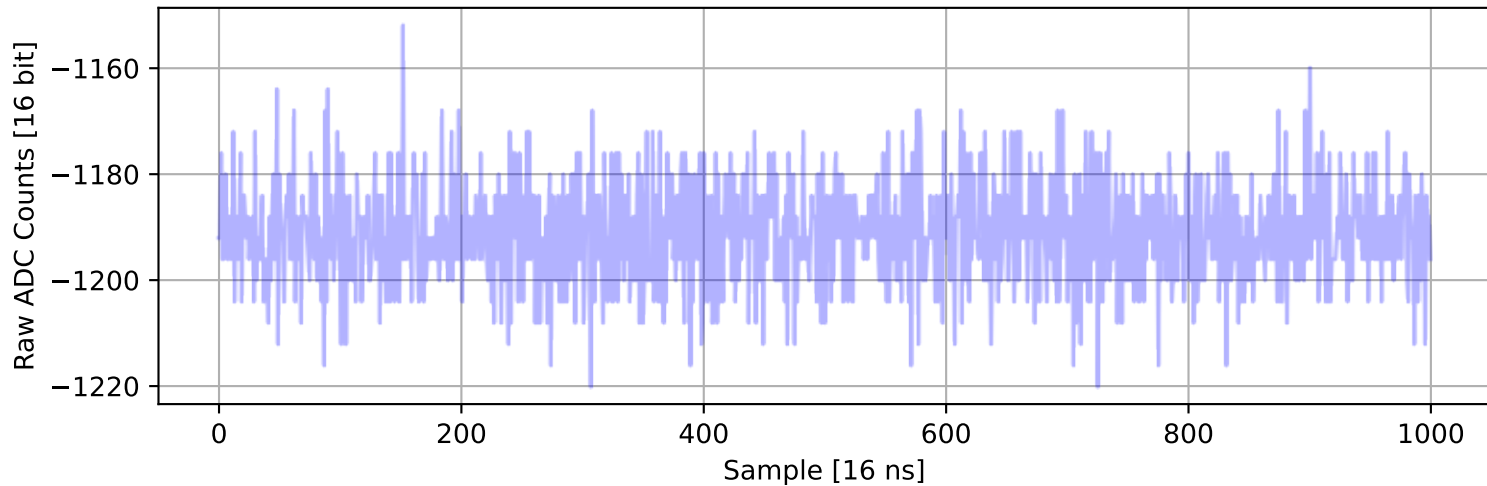




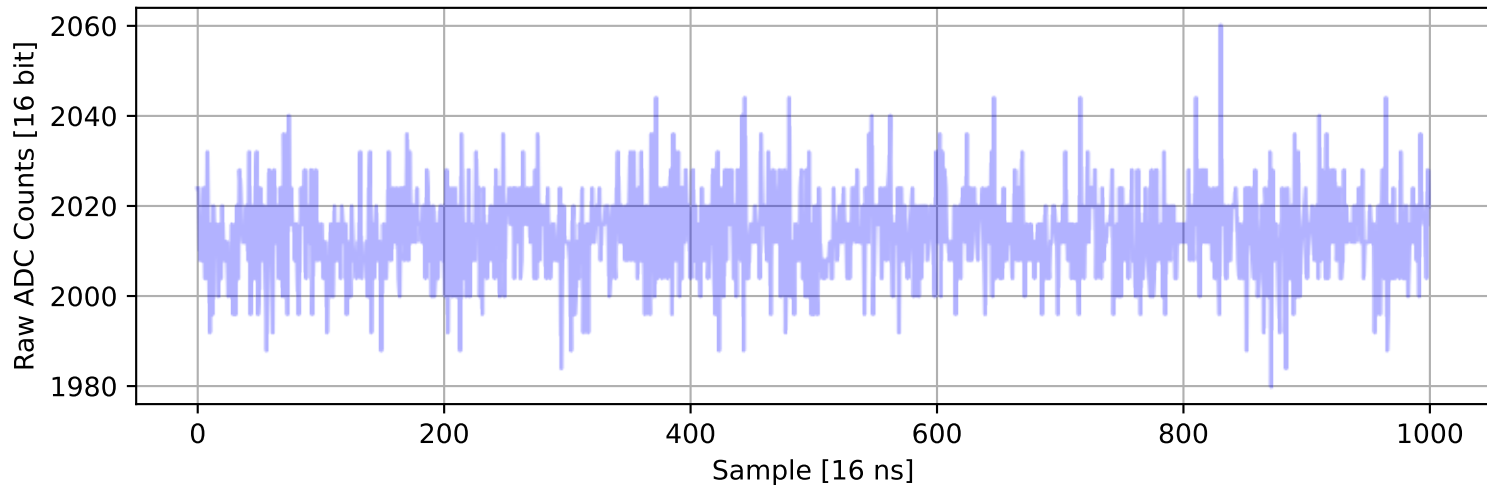
ADC 0, Channel 16, Event 14: Max Signal Waveform



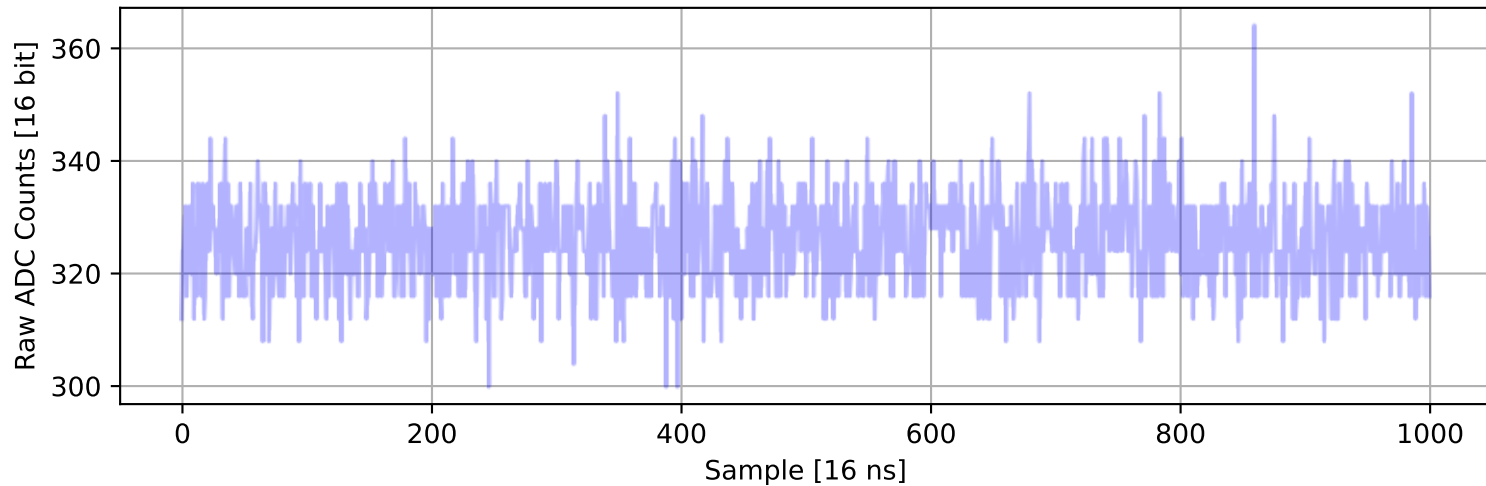
ADC 0, Channel 17, Event 44: Max Signal Waveform



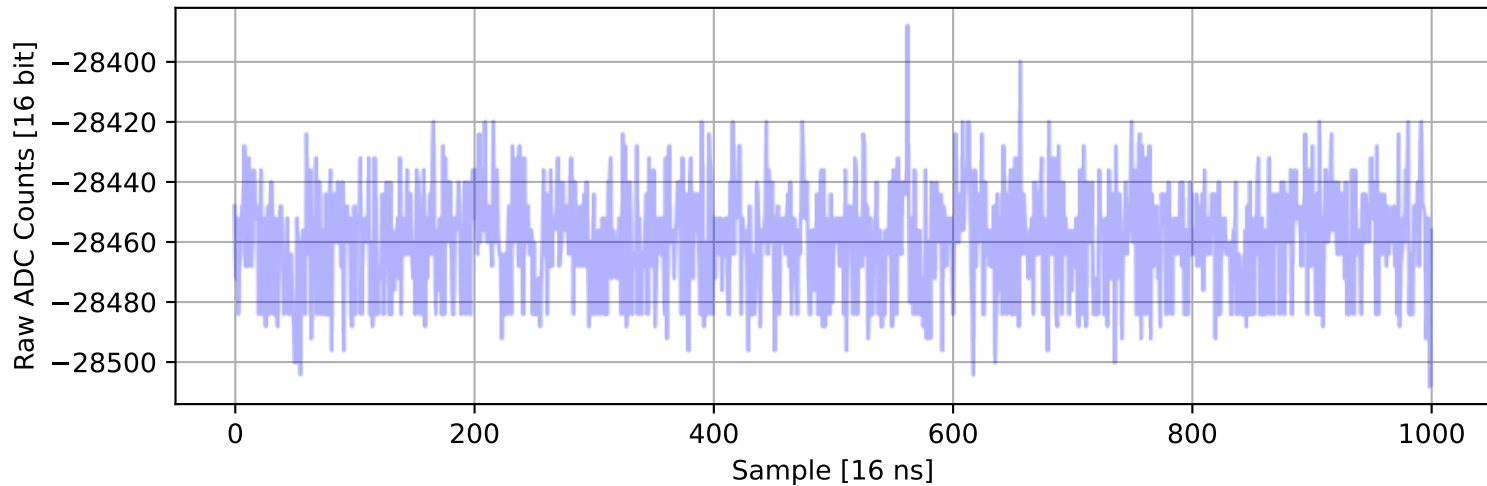
ADC 0, Channel 18, Event 21: Max Signal Waveform



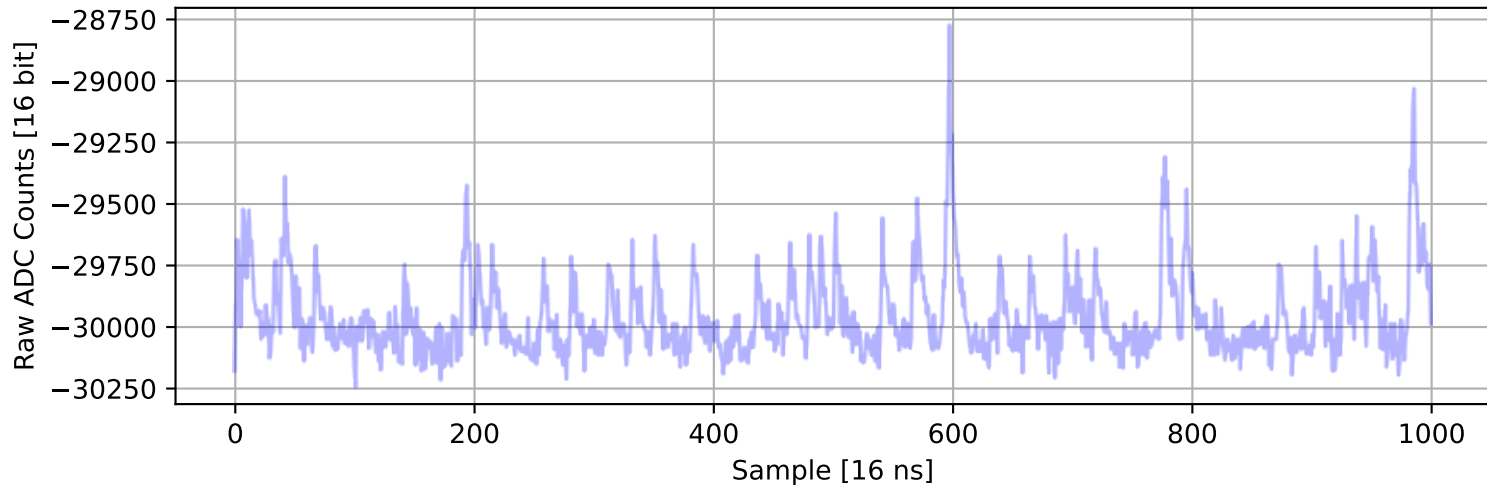
ADC 0, Channel 19, Event 75: Max Signal Waveform



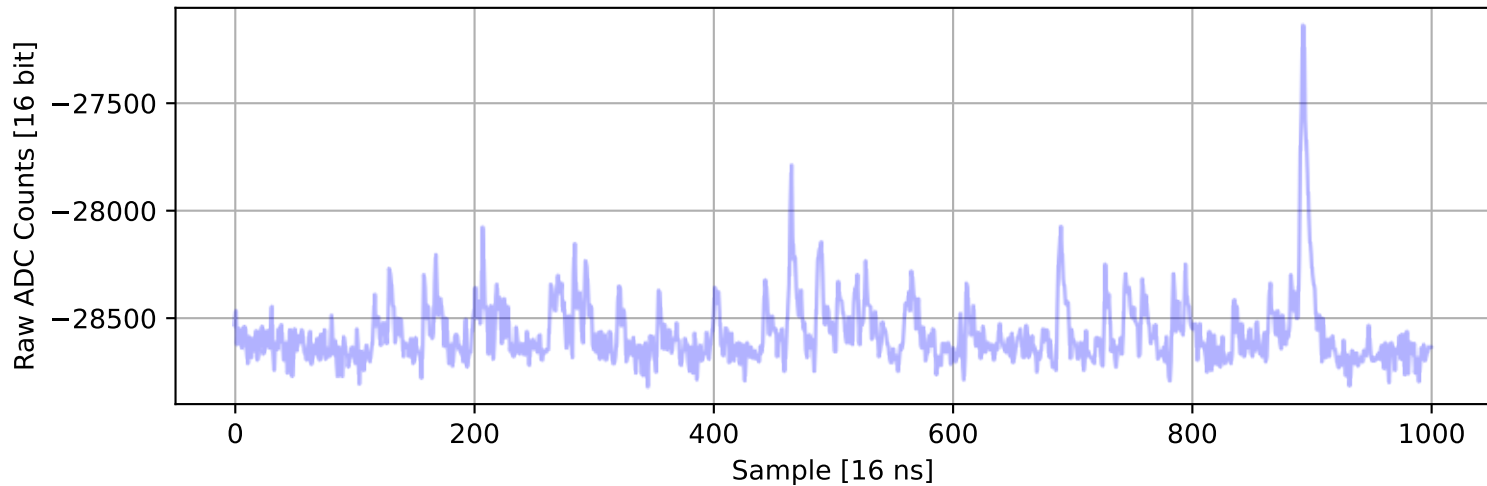
ADC 0, Channel 20, Event 72: Max Signal Waveform



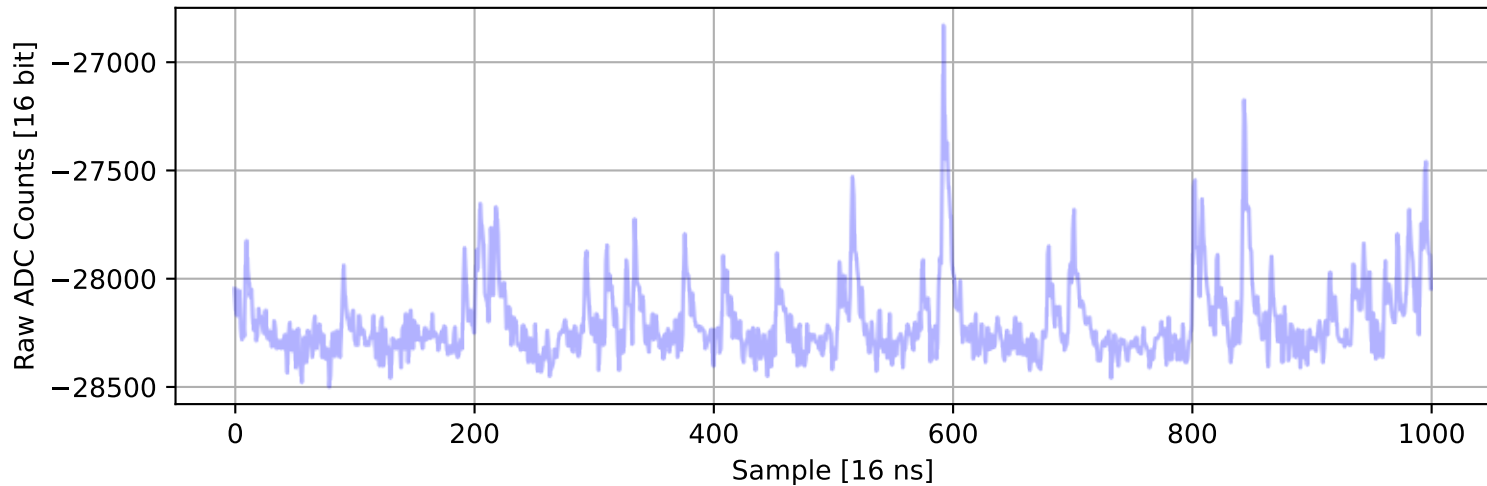
ADC 0, Channel 21, Event 38: Max Signal Waveform



ADC 0, Channel 22, Event 36: Max Signal Waveform

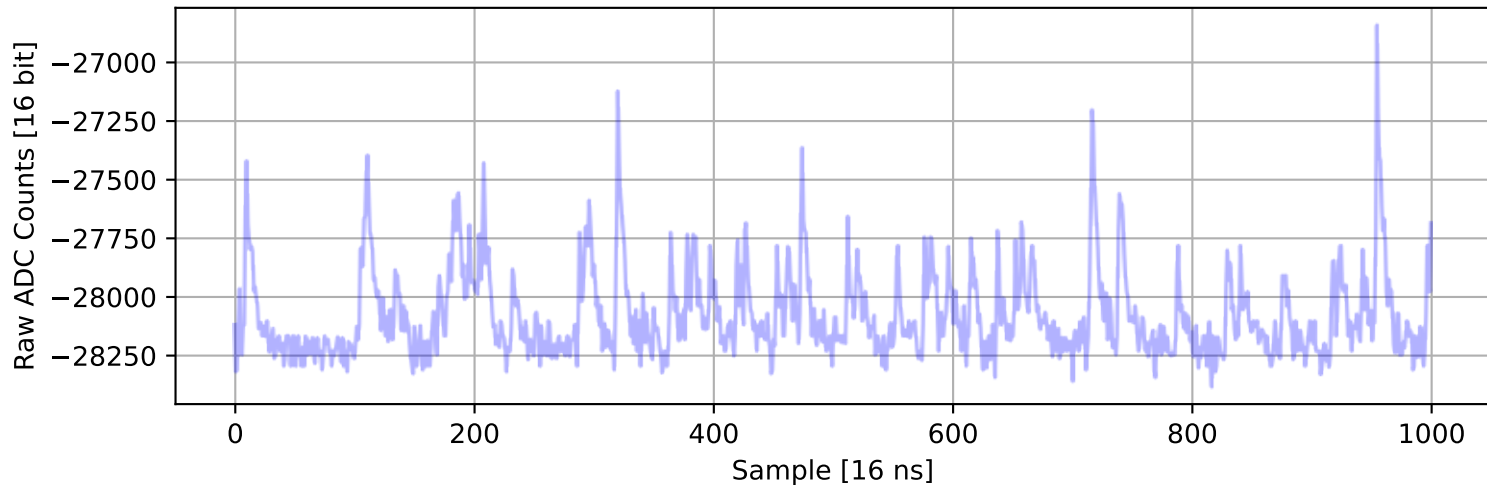


ADC 0, Channel 23, Event 9: Max Signal Waveform

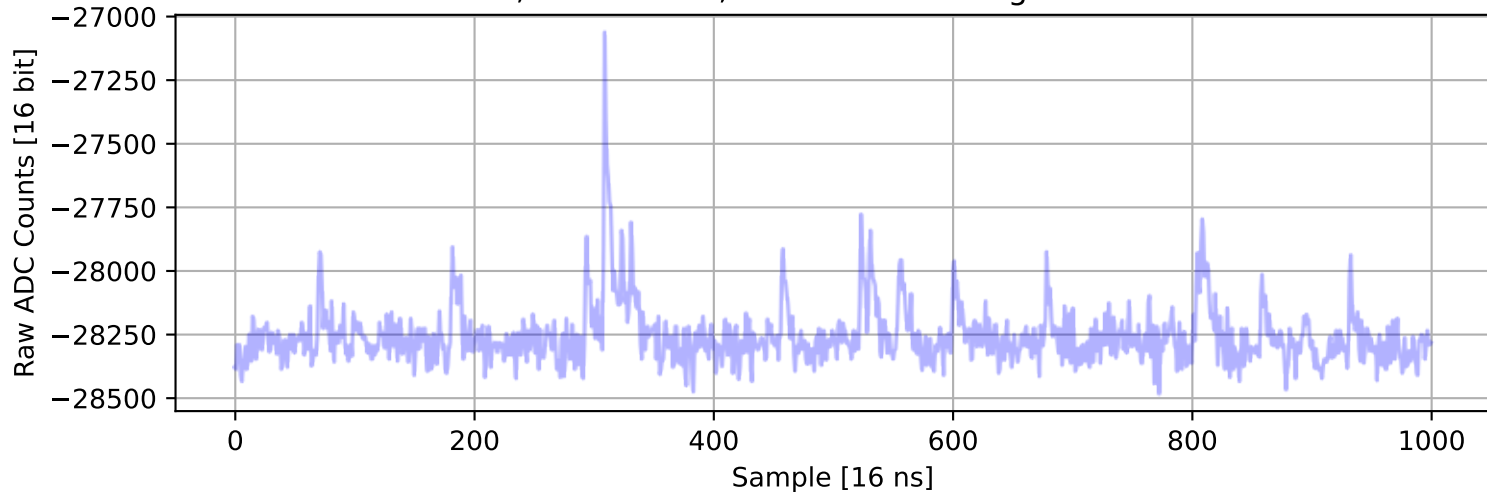




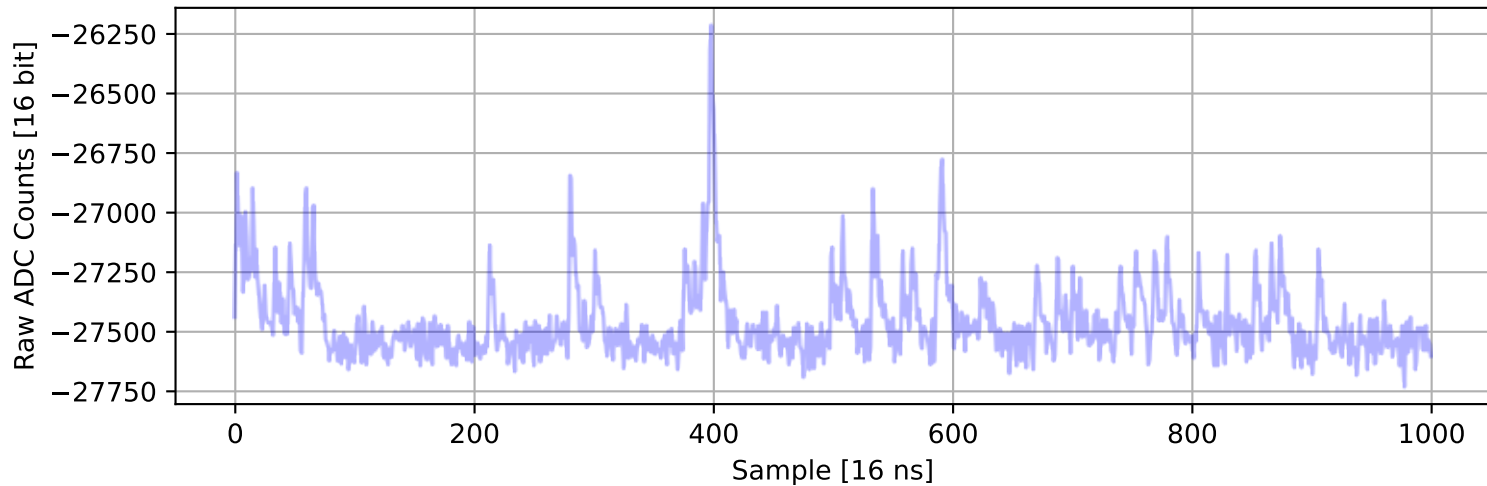
ADC 0, Channel 24, Event 32: Max Signal Waveform



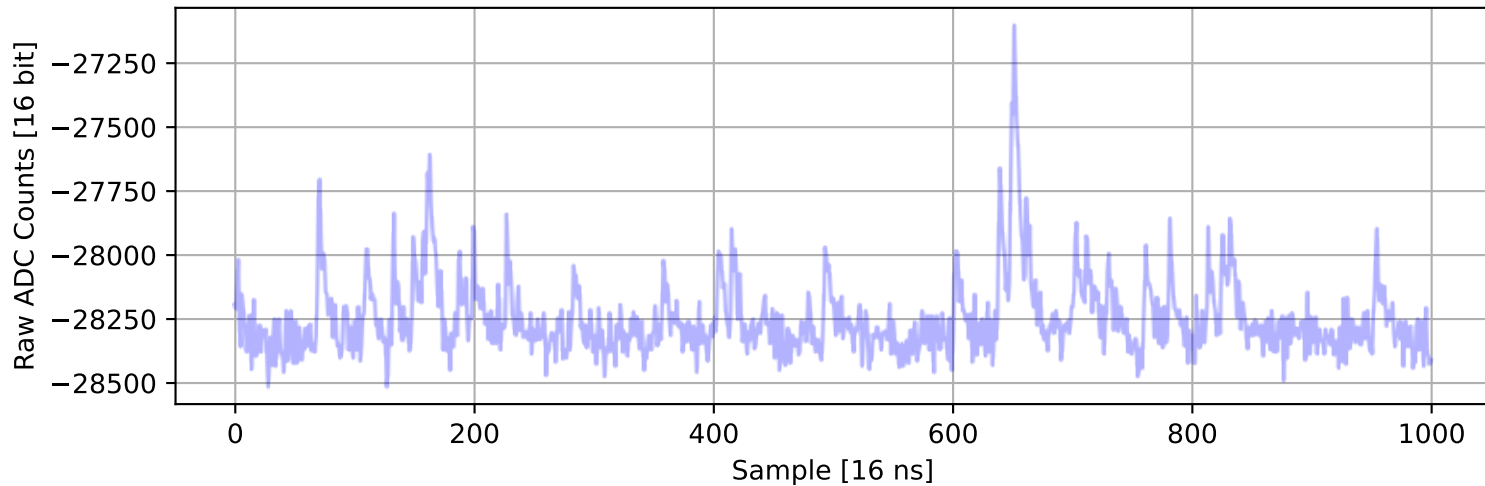
ADC 0, Channel 25, Event 83: Max Signal Waveform



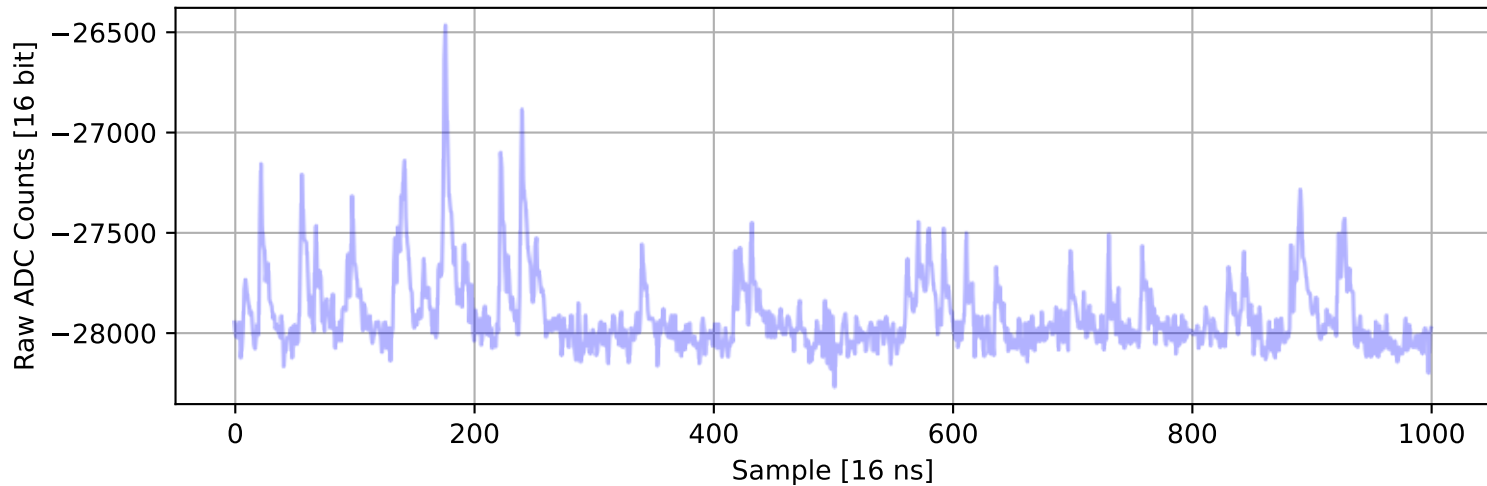
ADC 0, Channel 26, Event 24: Max Signal Waveform



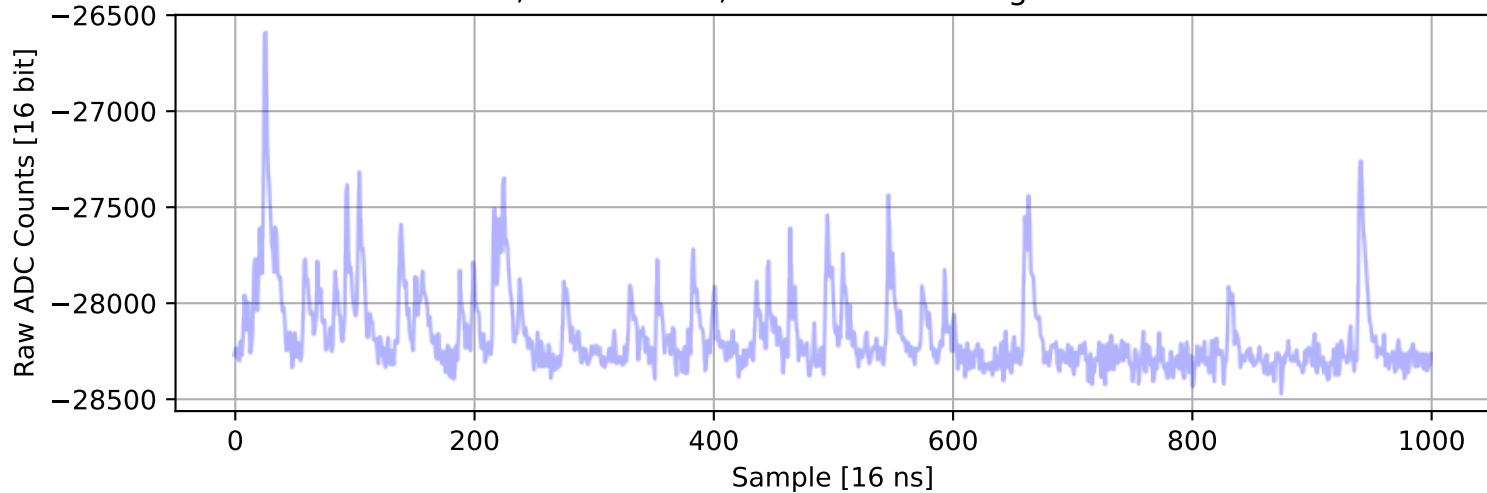
ADC 0, Channel 27, Event 53: Max Signal Waveform



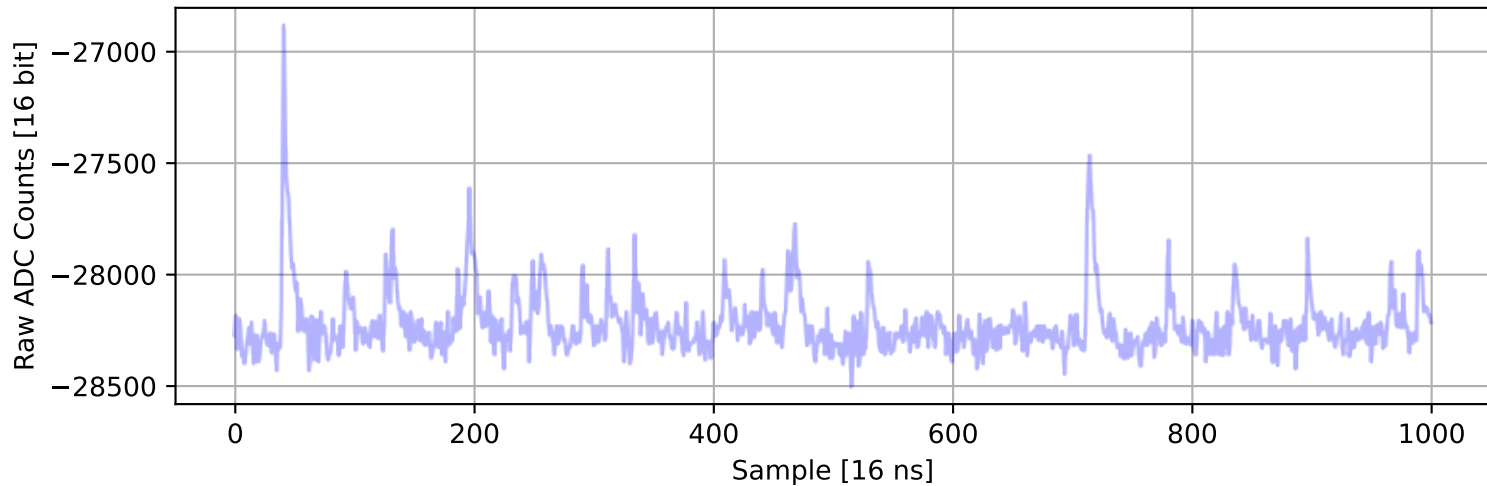
ADC 0, Channel 28, Event 56: Max Signal Waveform



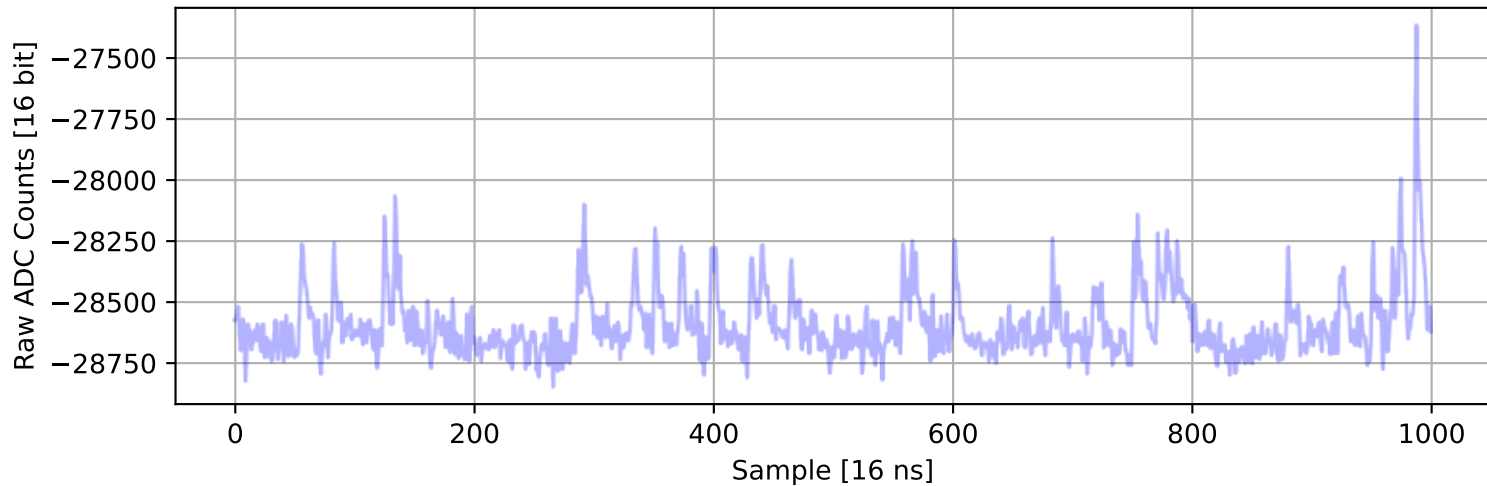
ADC 0, Channel 29, Event 45: Max Signal Waveform



ADC 0, Channel 30, Event 22: Max Signal Waveform

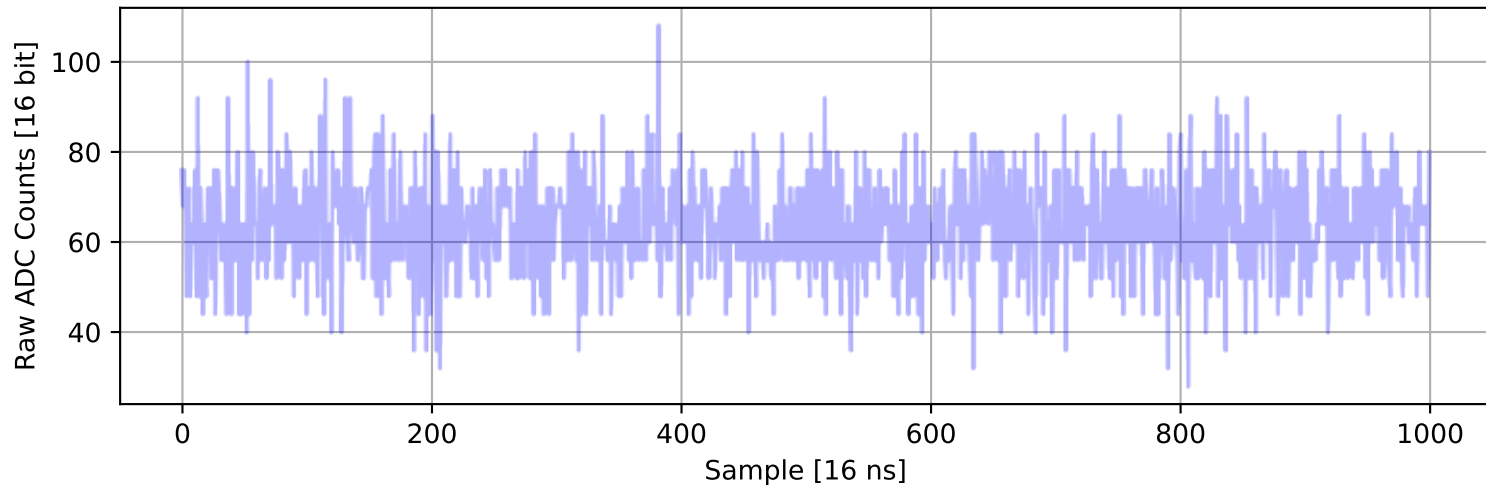


ADC 0, Channel 31, Event 19: Max Signal Waveform

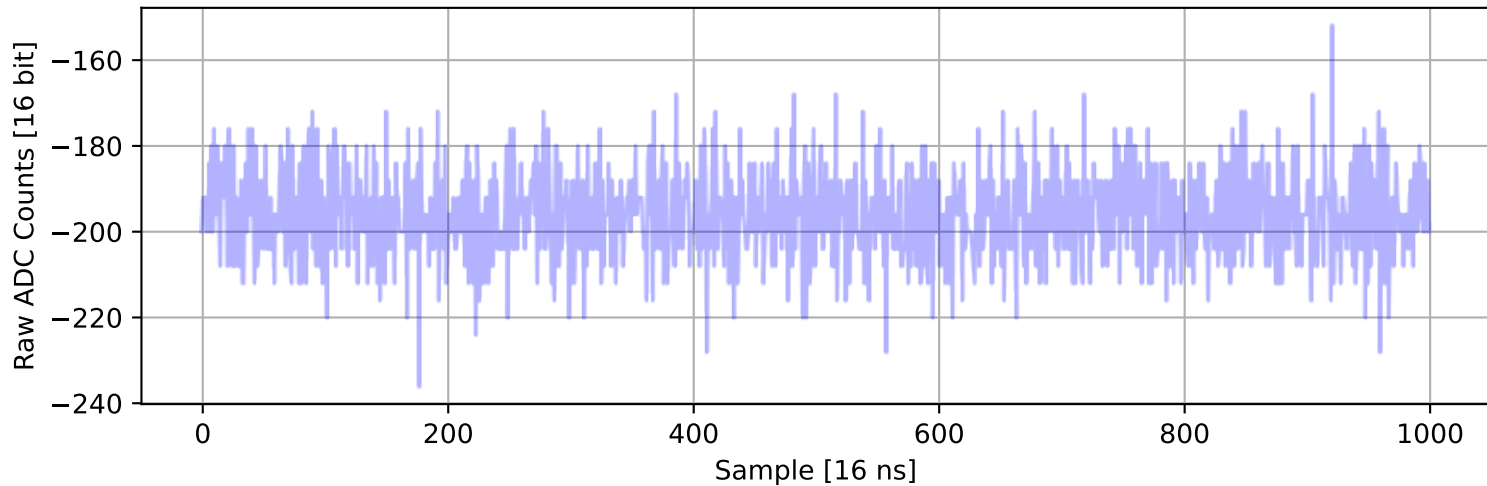




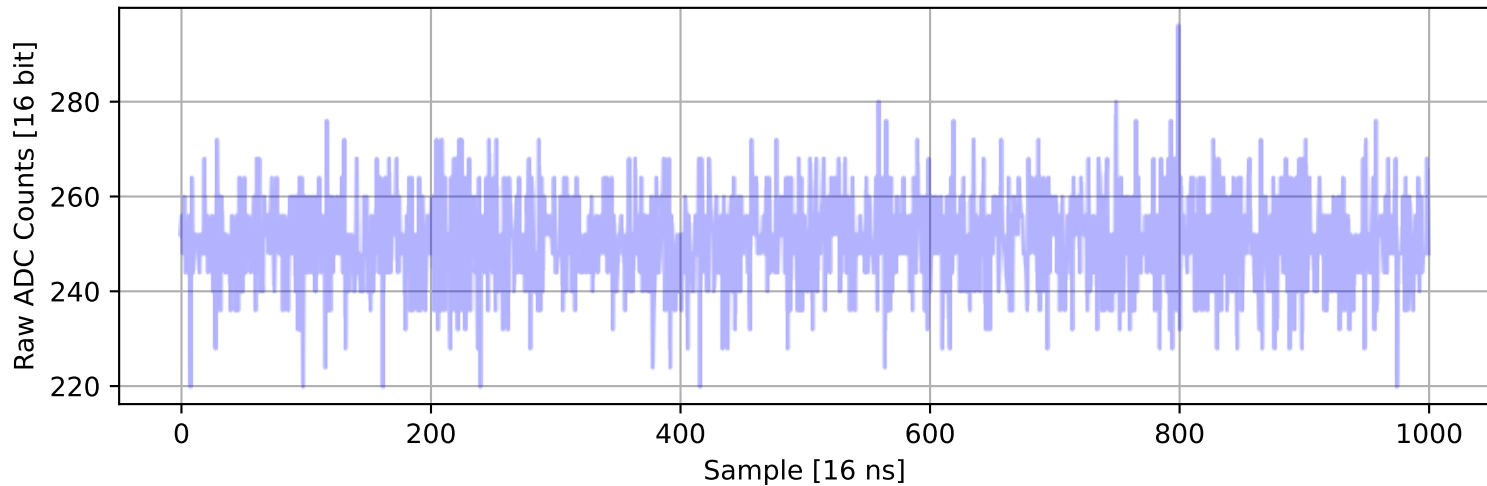
ADC 0, Channel 32, Event 11: Max Signal Waveform



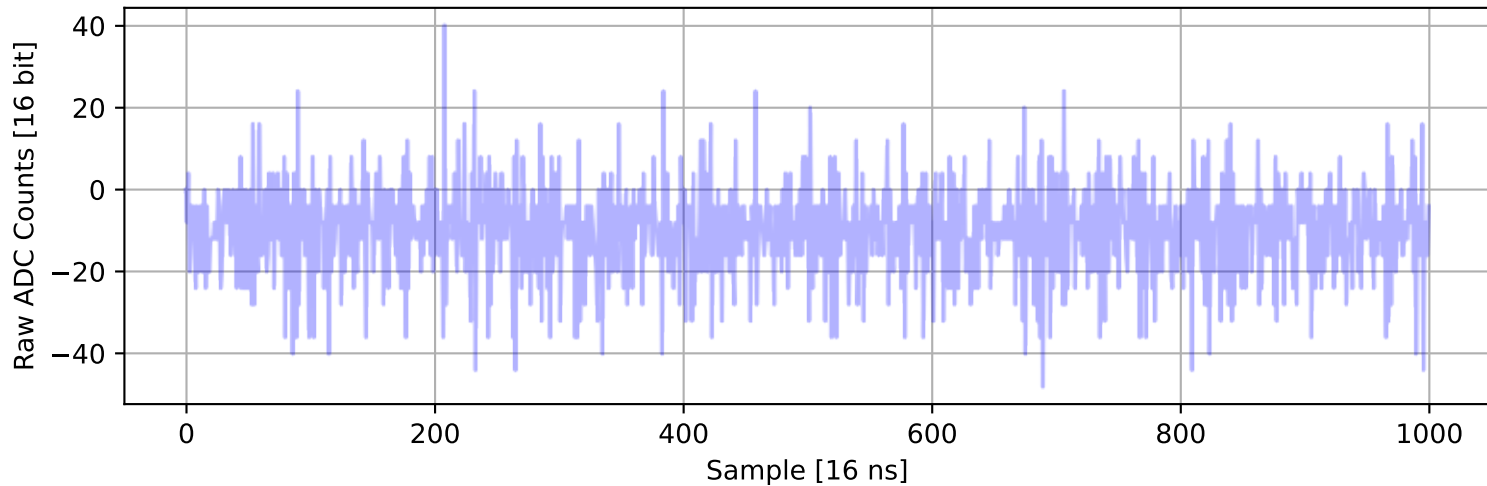
ADC 0, Channel 33, Event 15: Max Signal Waveform



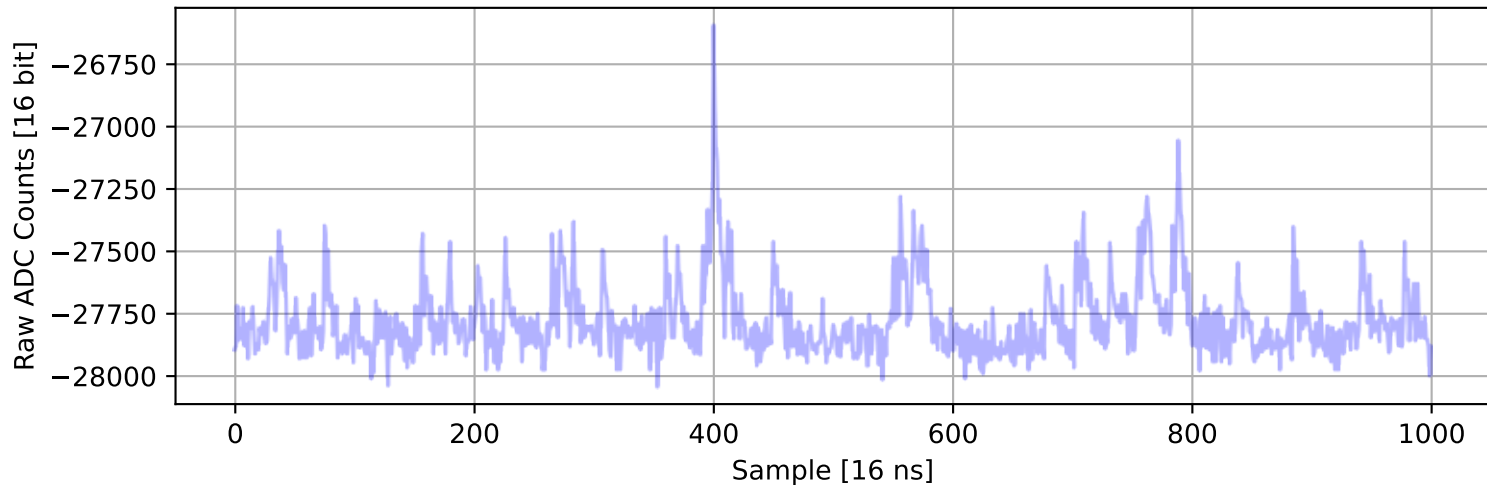
ADC 0, Channel 34, Event 30: Max Signal Waveform



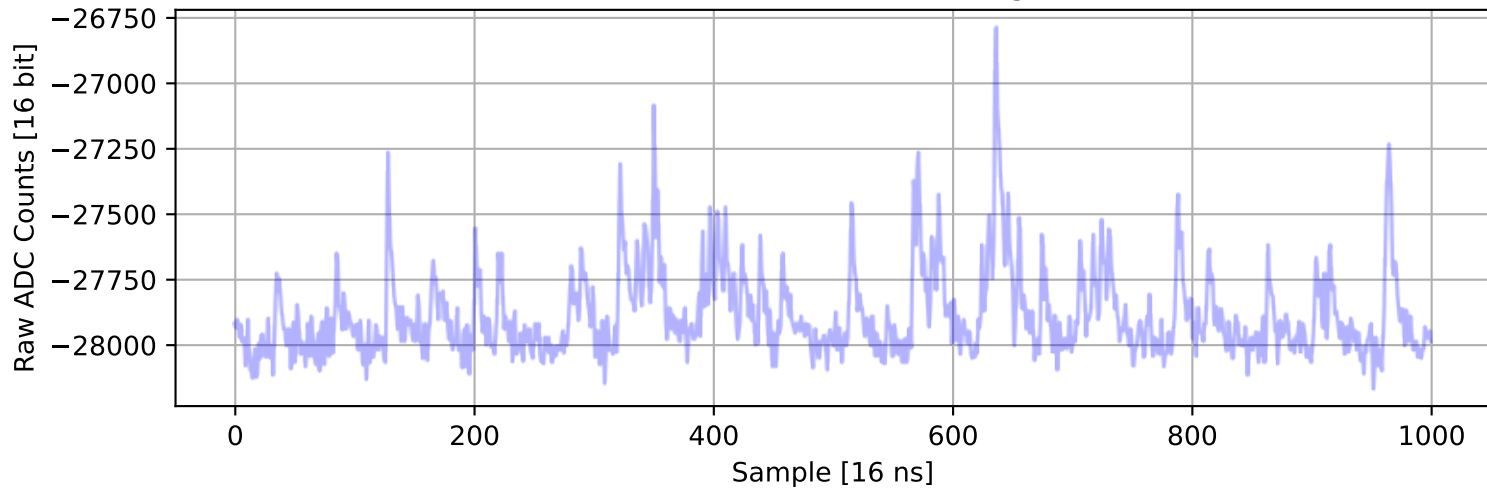
ADC 0, Channel 35, Event 4: Max Signal Waveform



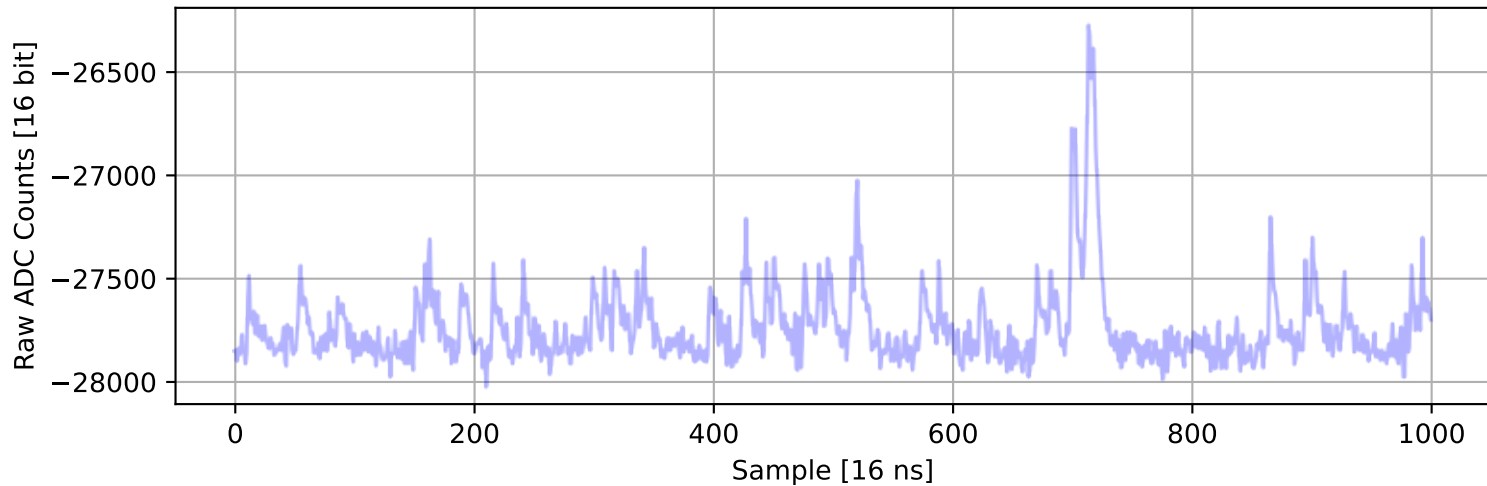
ADC 0, Channel 36, Event 52: Max Signal Waveform



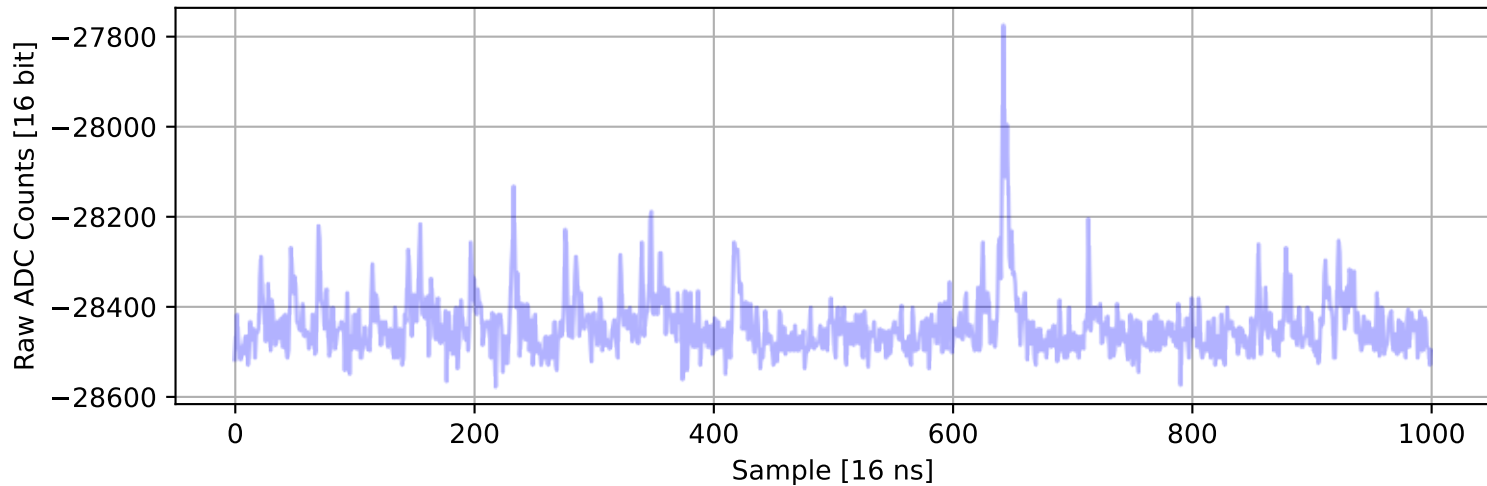
ADC 0, Channel 37, Event 27: Max Signal Waveform



ADC 0, Channel 38, Event 74: Max Signal Waveform

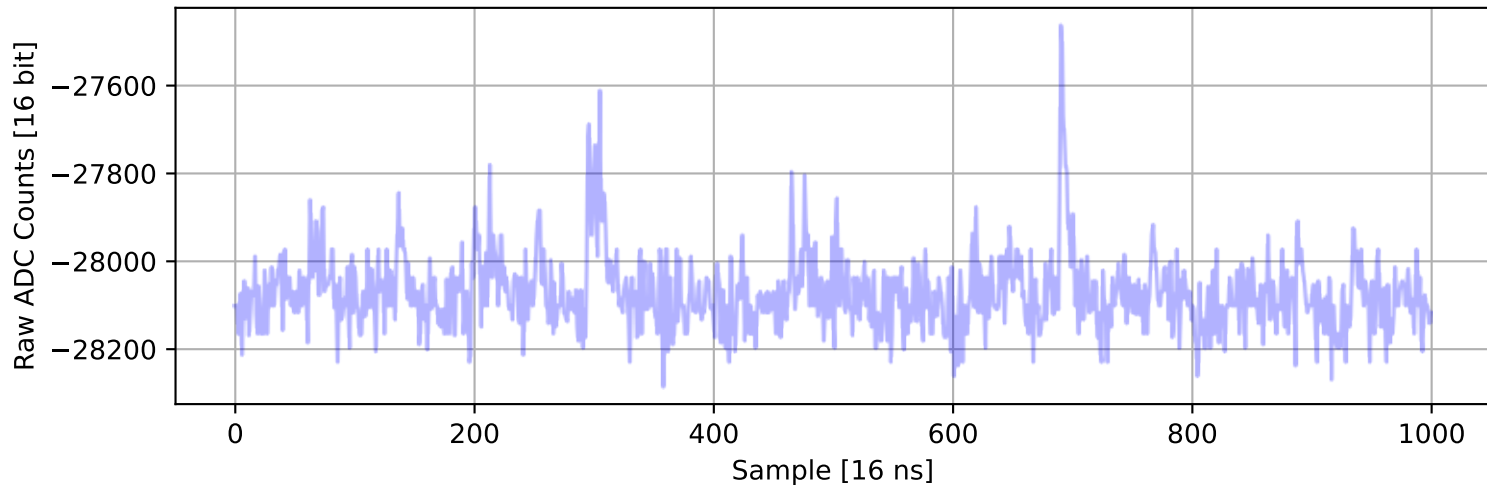


ADC 0, Channel 39, Event 54: Max Signal Waveform

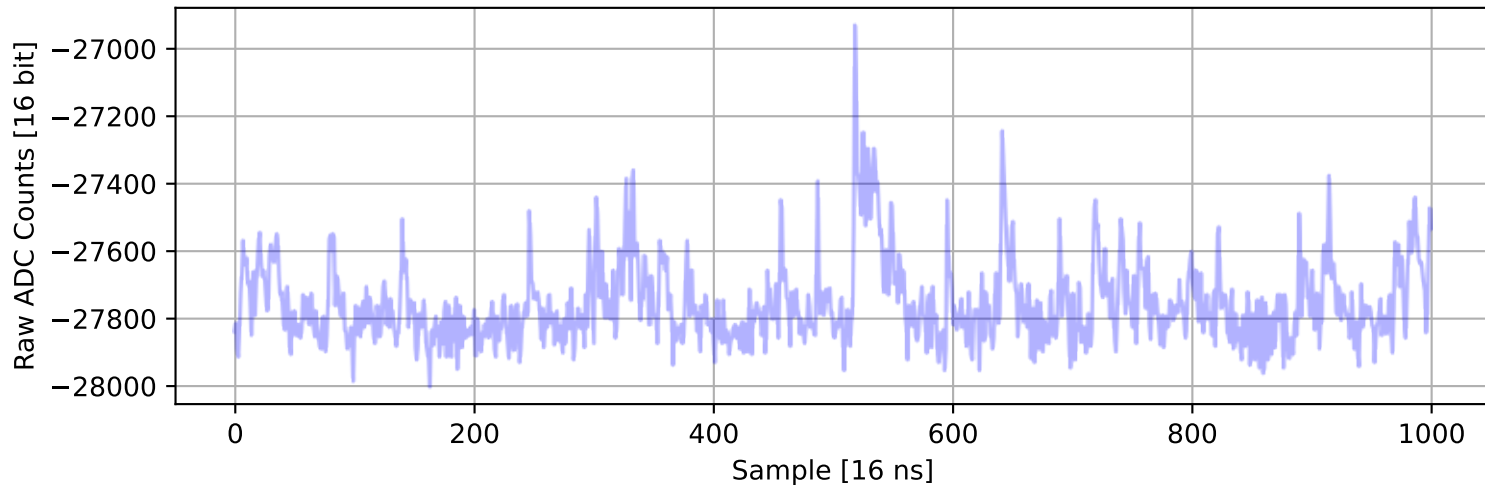




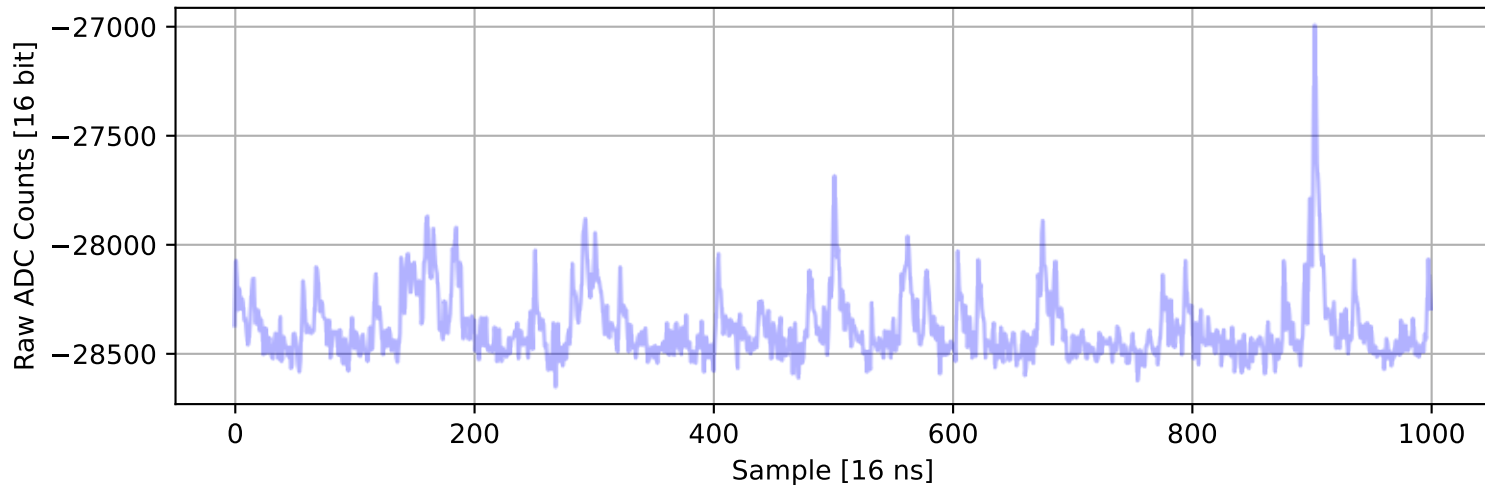
ADC 0, Channel 40, Event 18: Max Signal Waveform



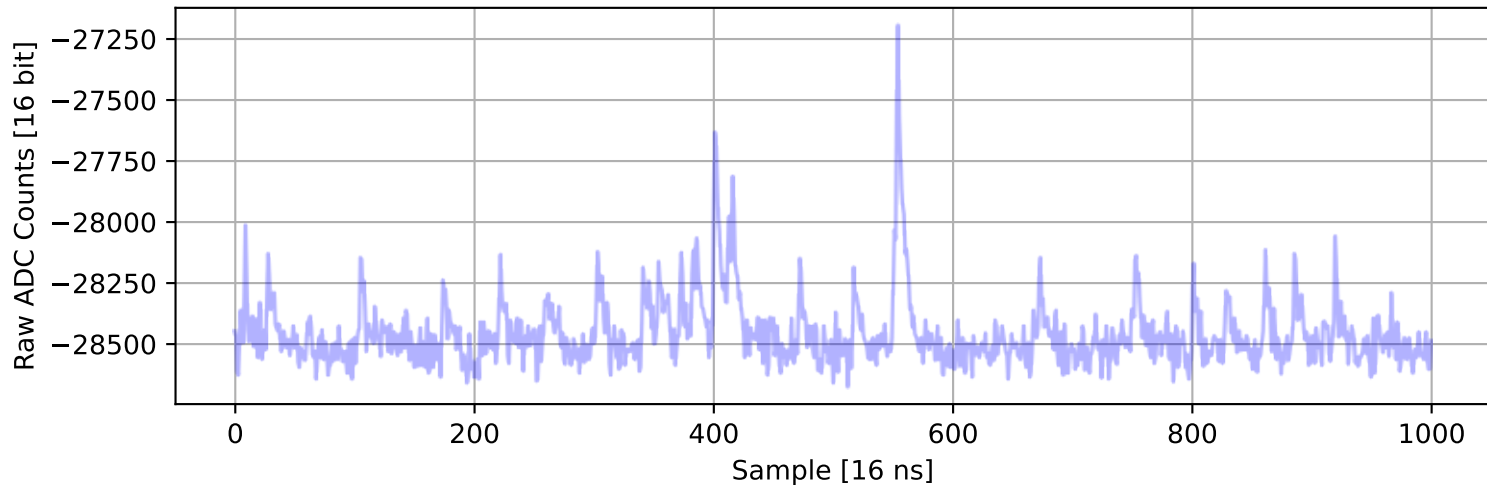
ADC 0, Channel 41, Event 58: Max Signal Waveform



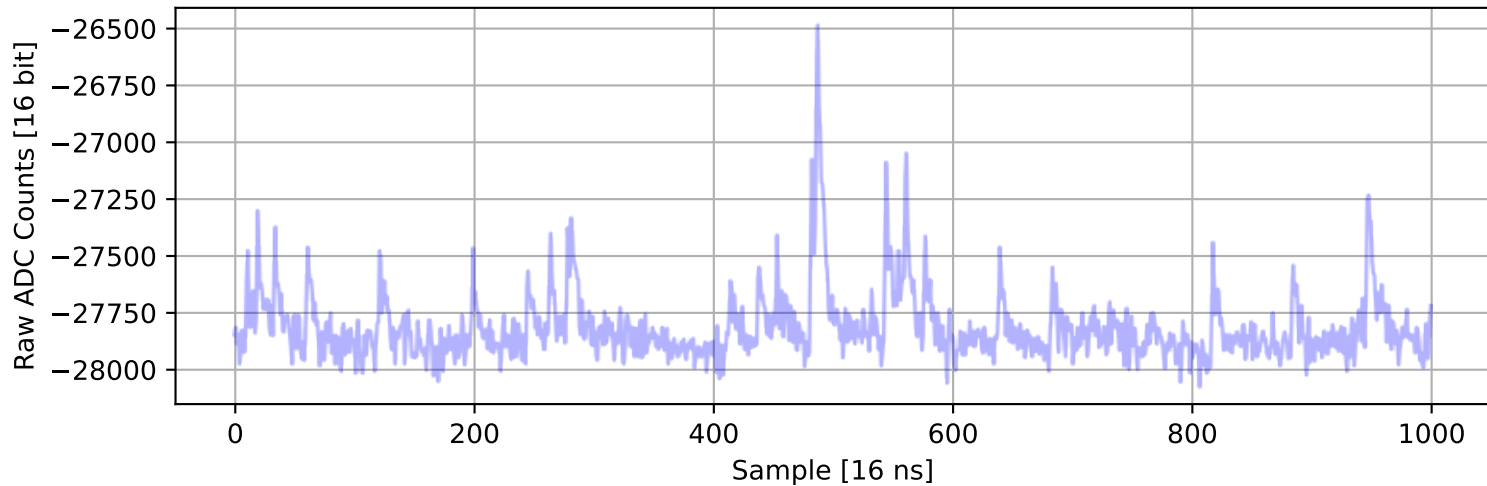
ADC 0, Channel 42, Event 16: Max Signal Waveform



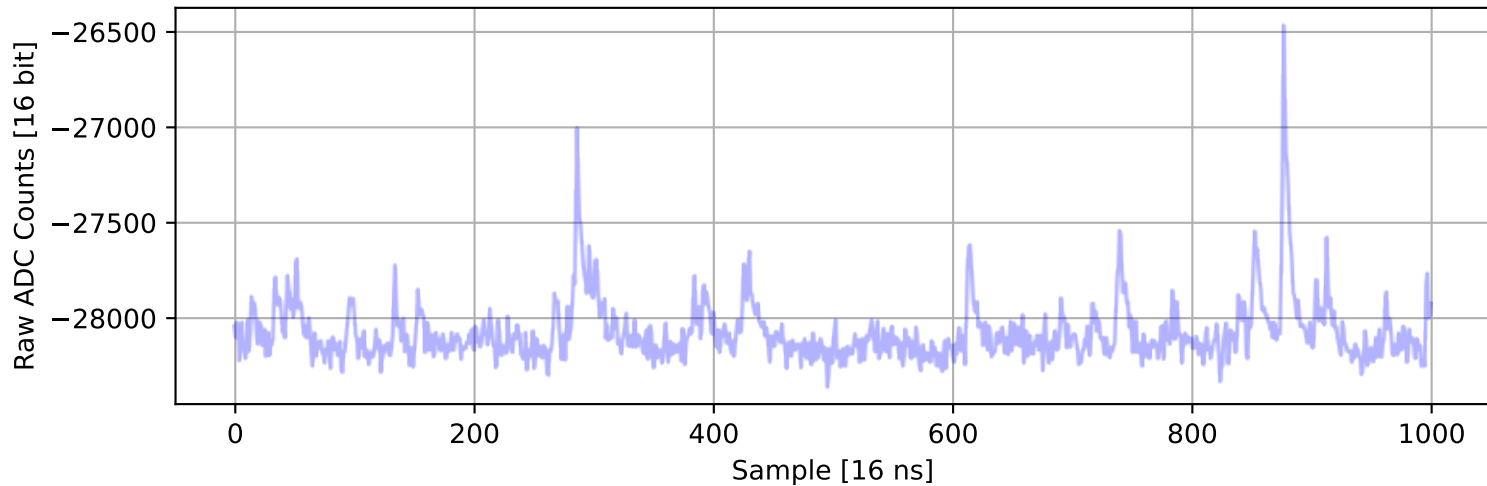
ADC 0, Channel 43, Event 25: Max Signal Waveform



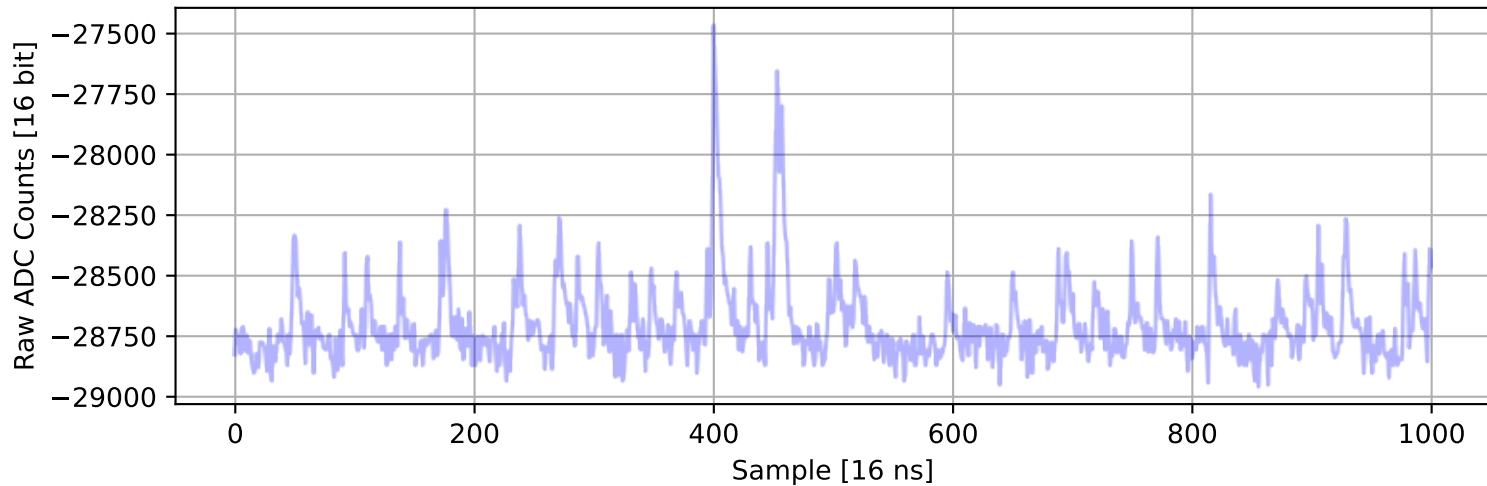
ADC 0, Channel 44, Event 77: Max Signal Waveform



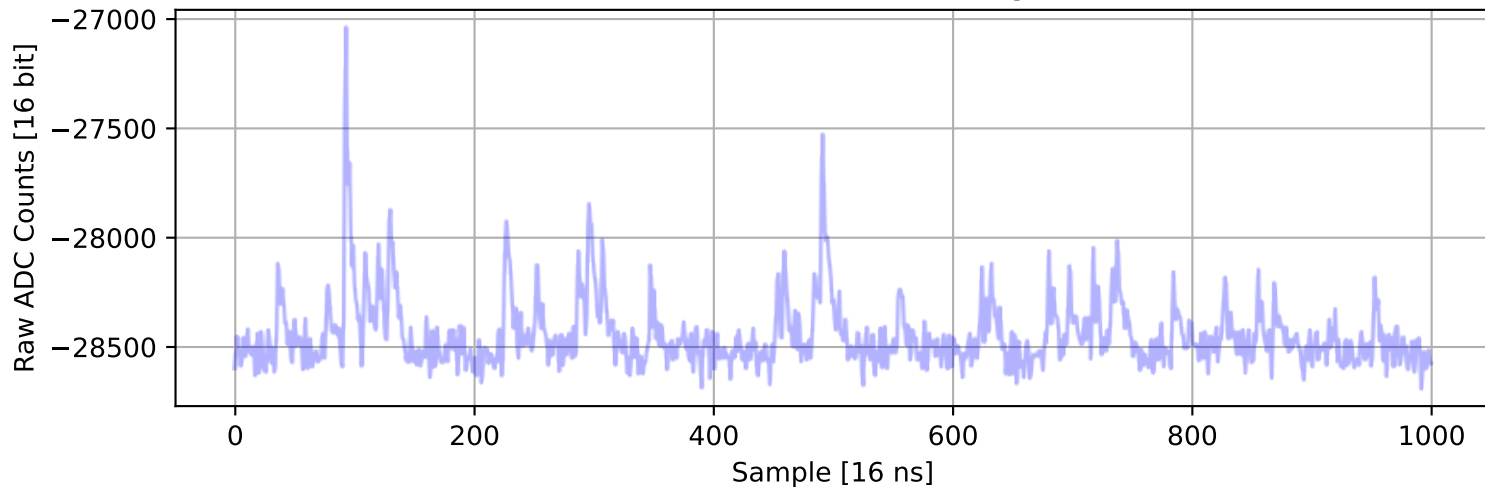
ADC 0, Channel 45, Event 57: Max Signal Waveform



ADC 0, Channel 46, Event 15: Max Signal Waveform

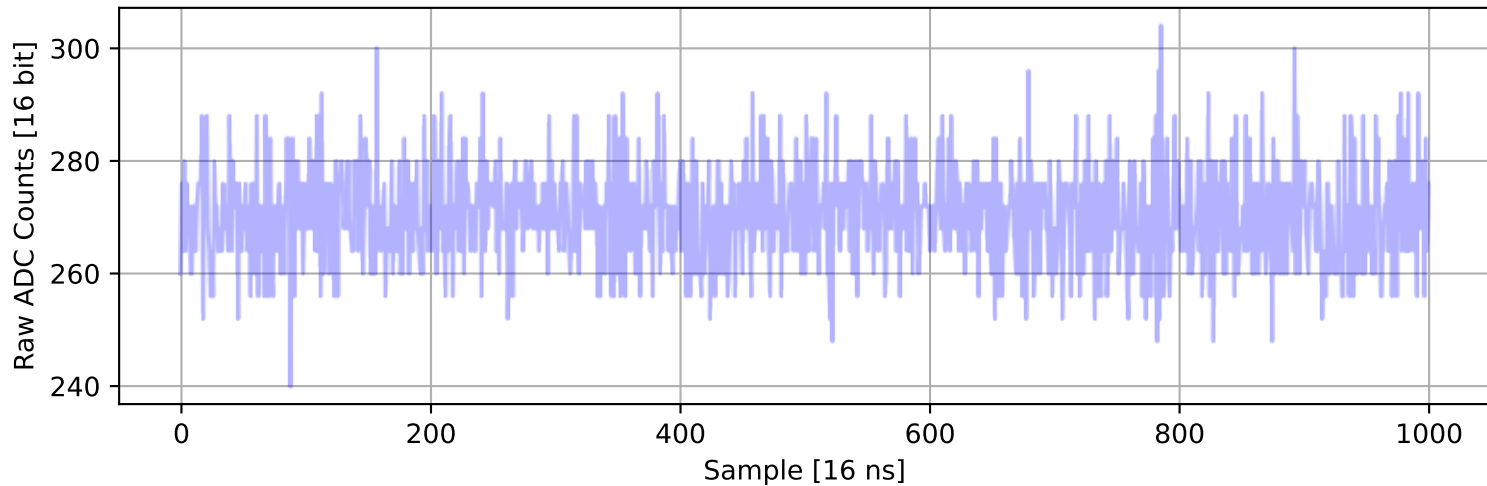


ADC 0, Channel 47, Event 48: Max Signal Waveform

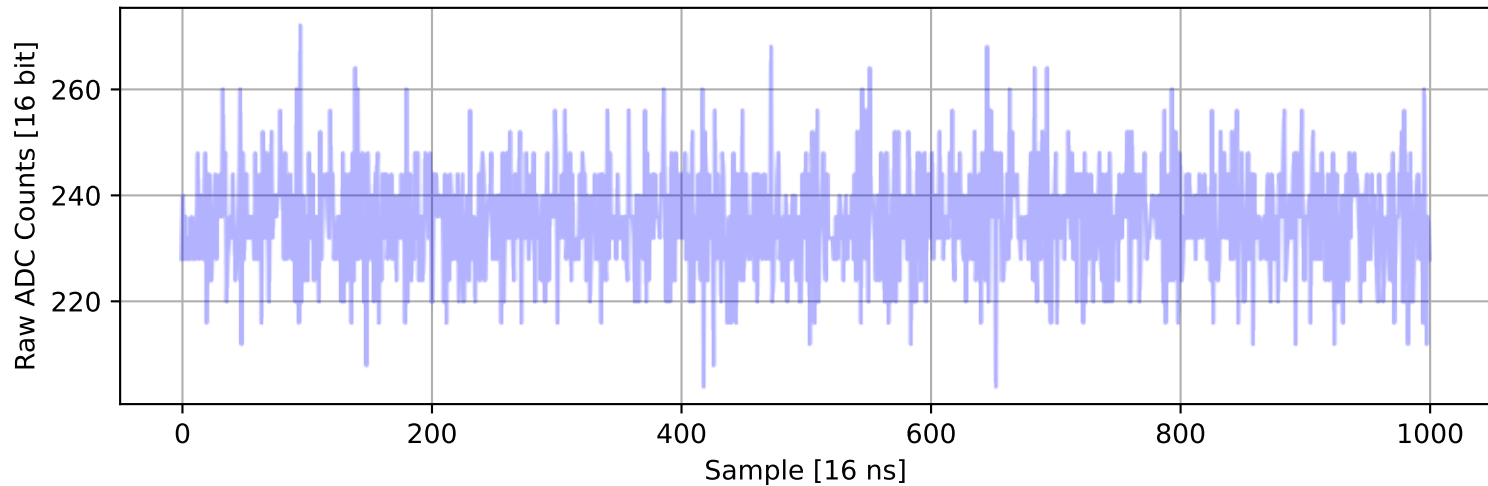




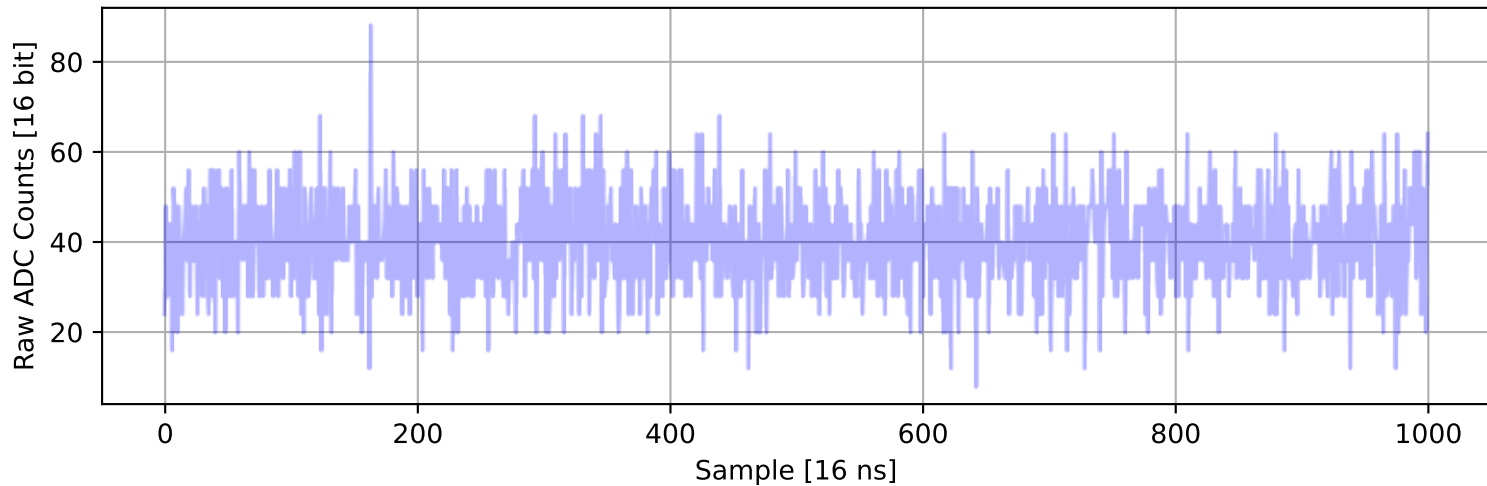
ADC 0, Channel 48, Event 19: Max Signal Waveform



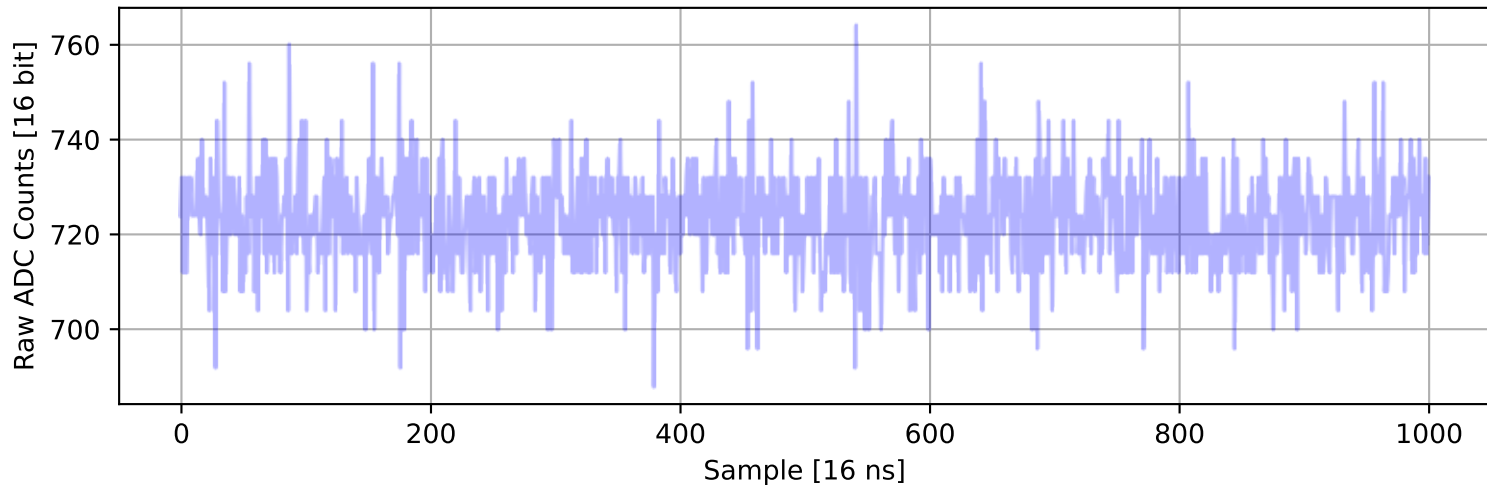
ADC 0, Channel 49, Event 2: Max Signal Waveform



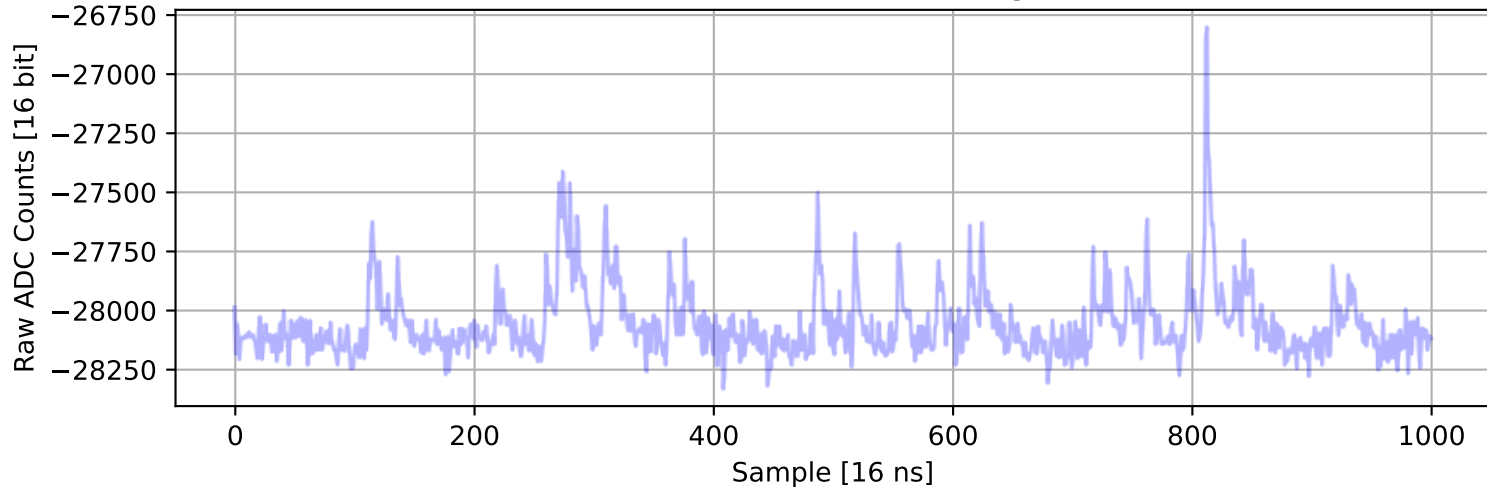
ADC 0, Channel 50, Event 11: Max Signal Waveform



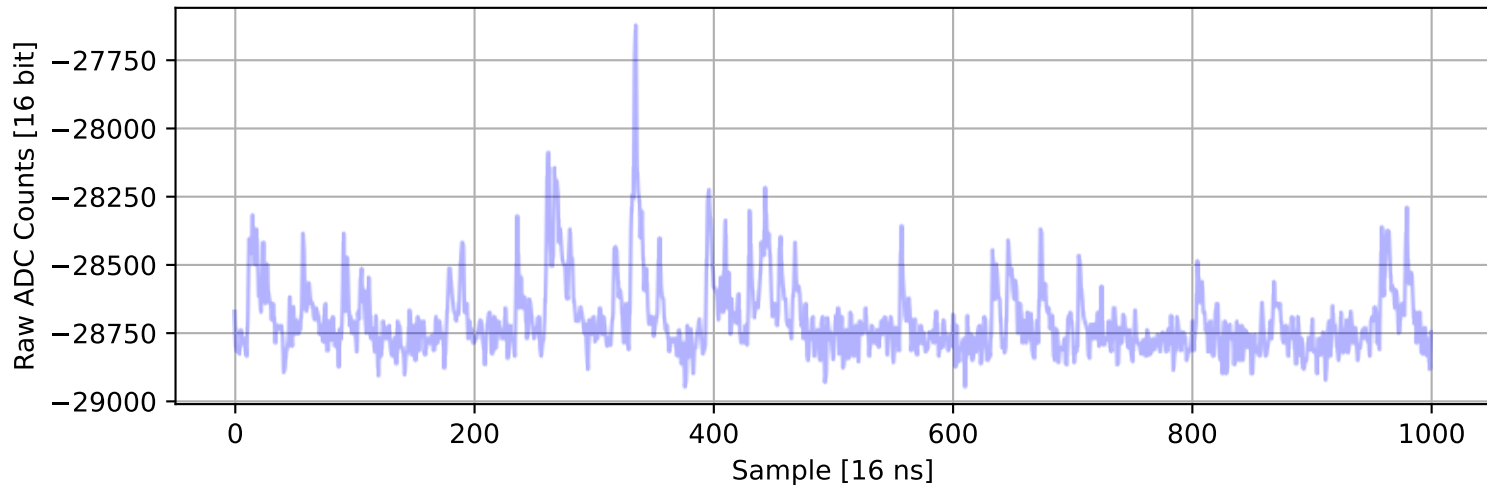
ADC 0, Channel 51, Event 64: Max Signal Waveform



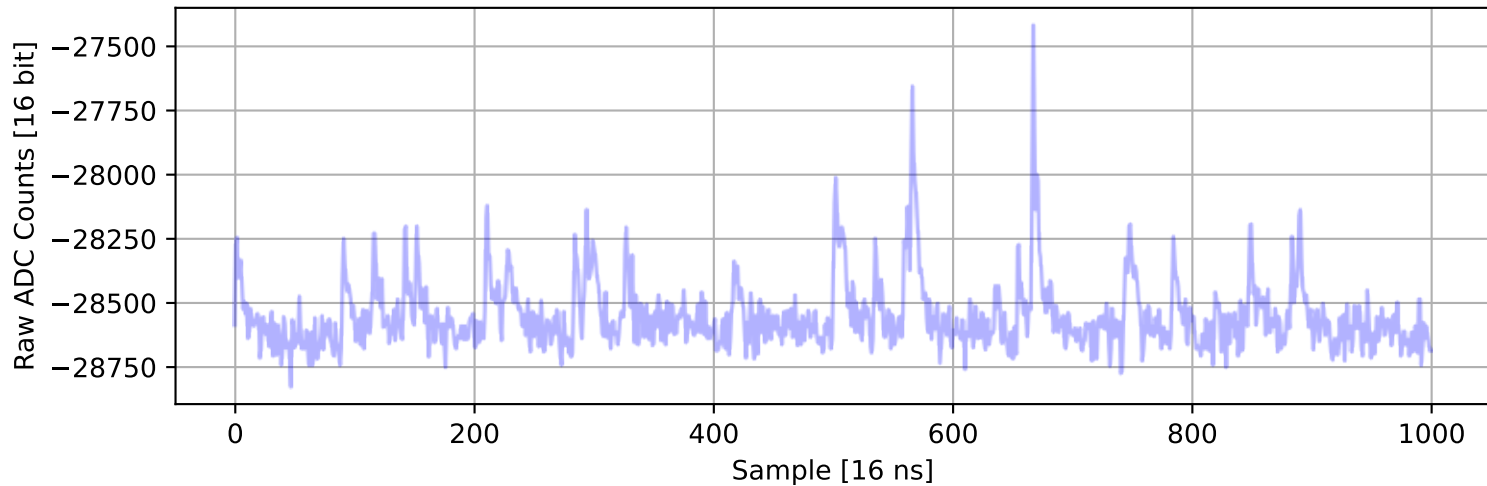
ADC 0, Channel 52, Event 43: Max Signal Waveform



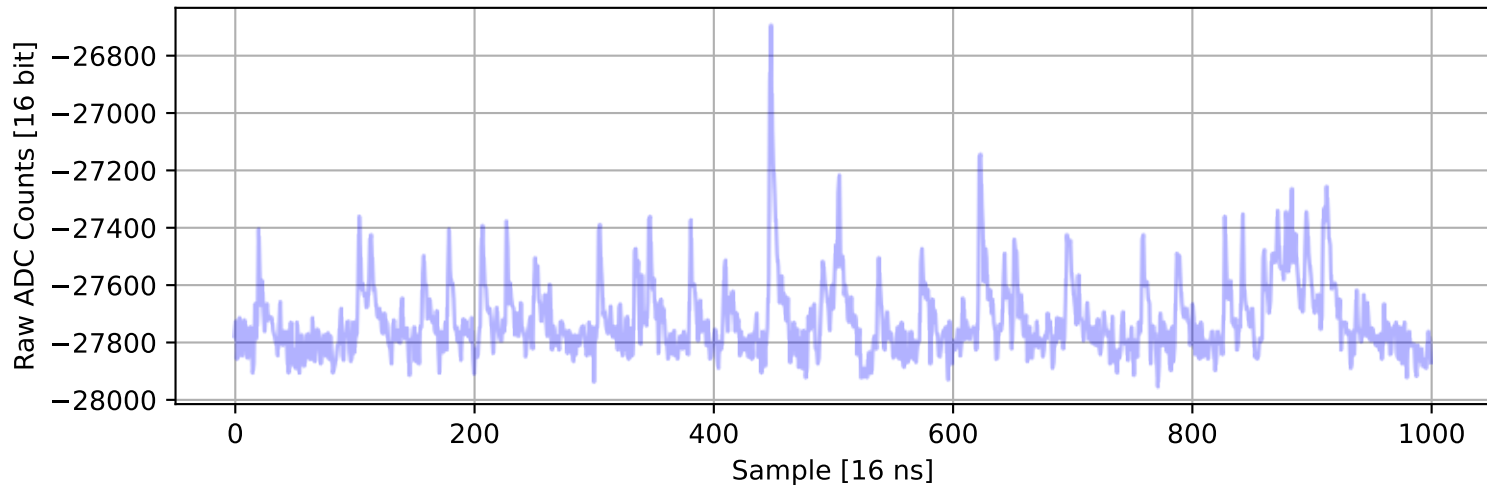
ADC 0, Channel 53, Event 44: Max Signal Waveform



ADC 0, Channel 54, Event 71: Max Signal Waveform

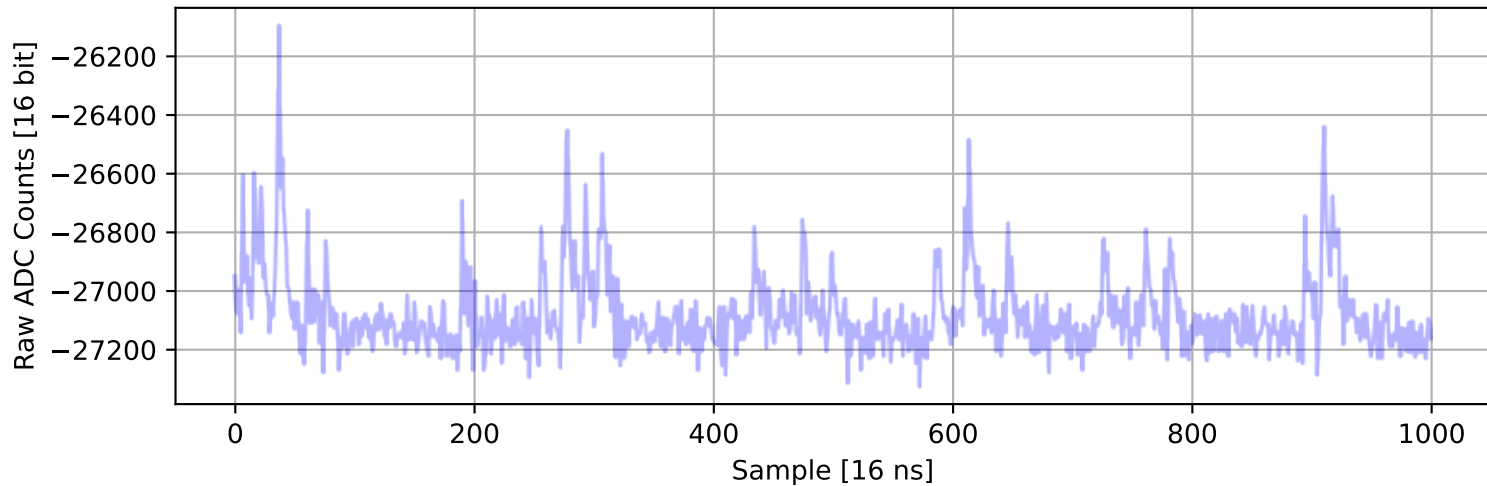


ADC 0, Channel 55, Event 8: Max Signal Waveform

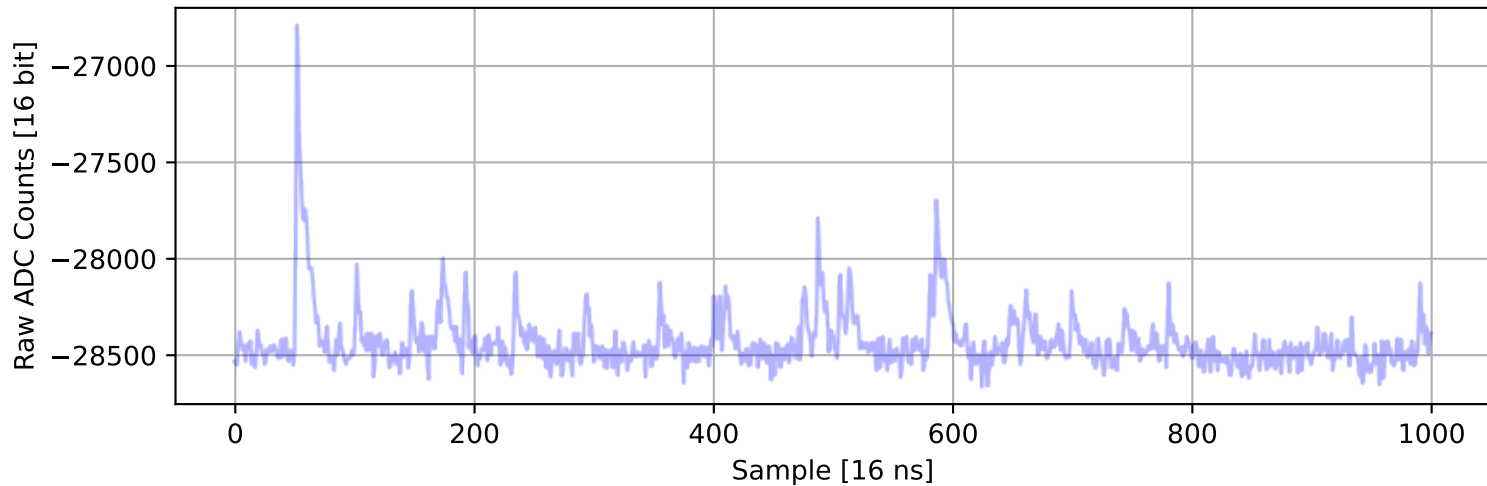




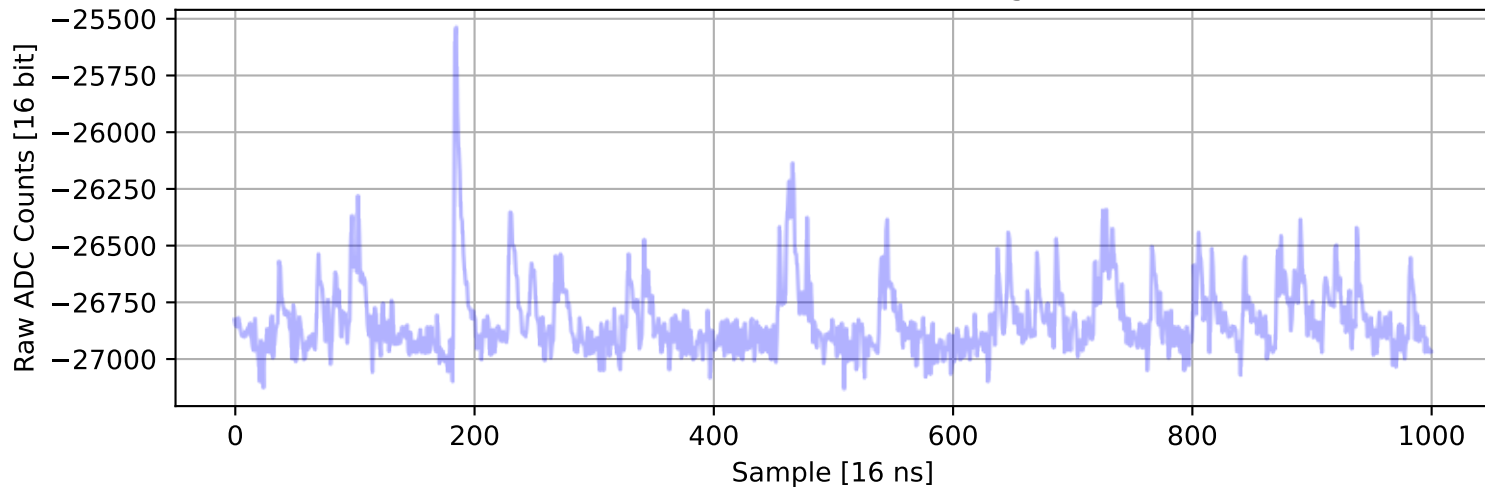
ADC 0, Channel 56, Event 71: Max Signal Waveform



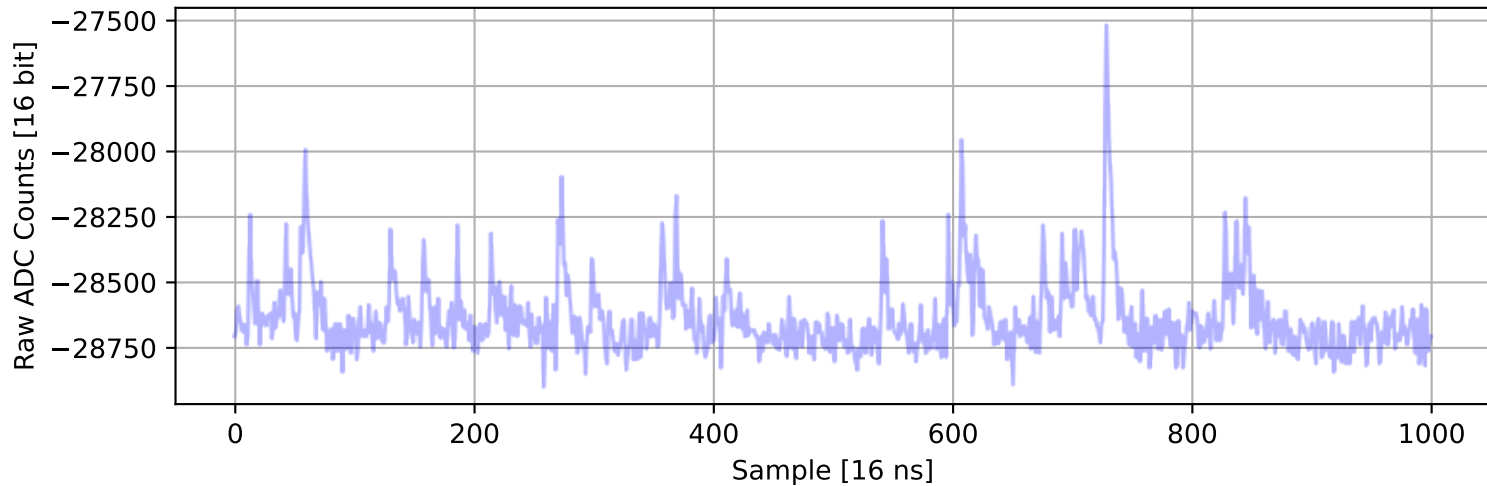
ADC 0, Channel 57, Event 34: Max Signal Waveform



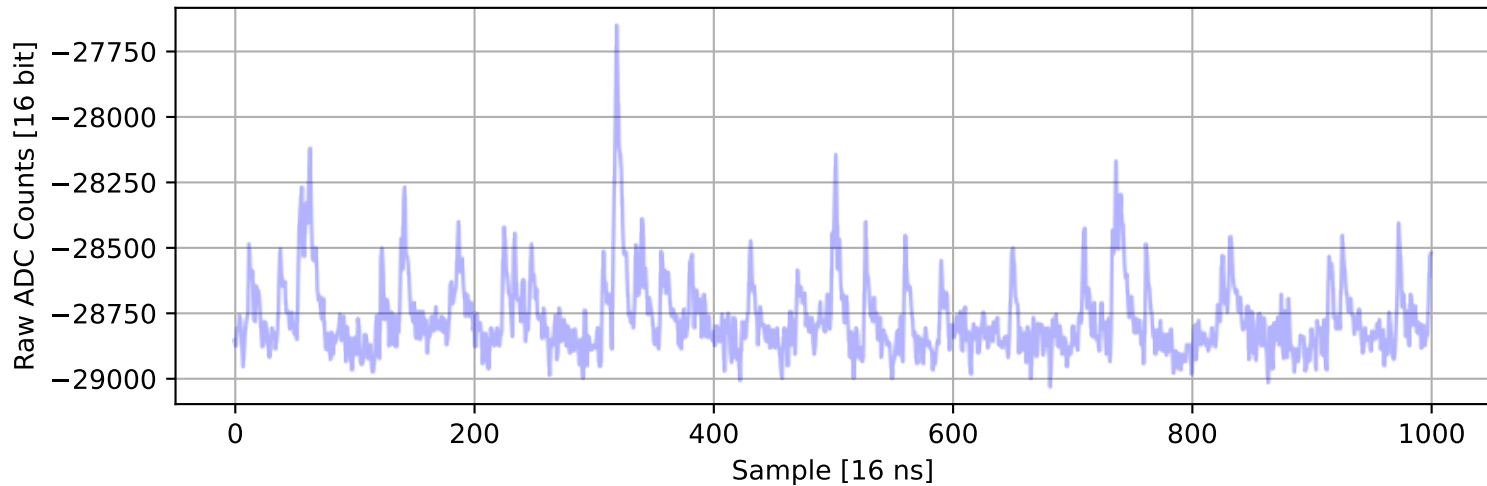
ADC 0, Channel 58, Event 11: Max Signal Waveform



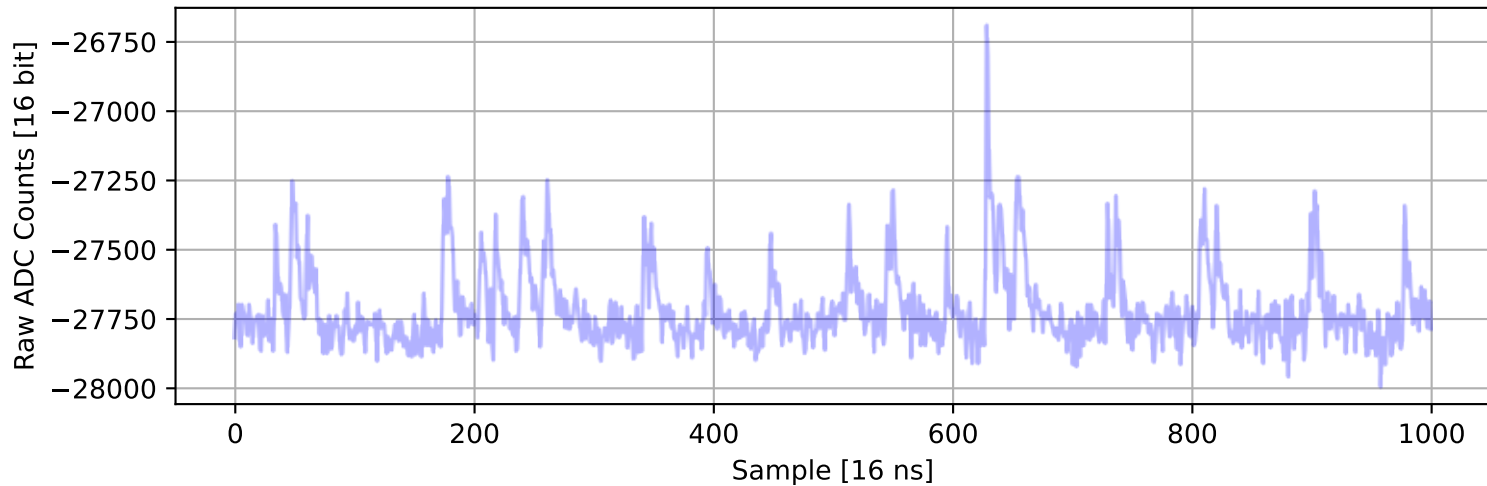
ADC 0, Channel 59, Event 32: Max Signal Waveform



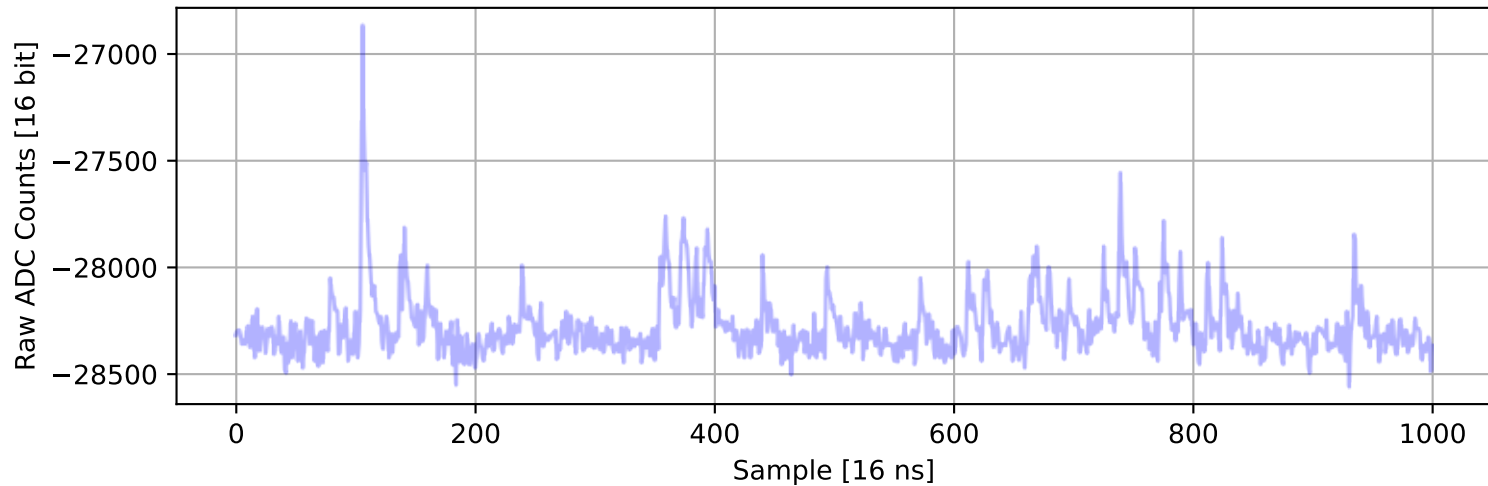
ADC 0, Channel 60, Event 22: Max Signal Waveform



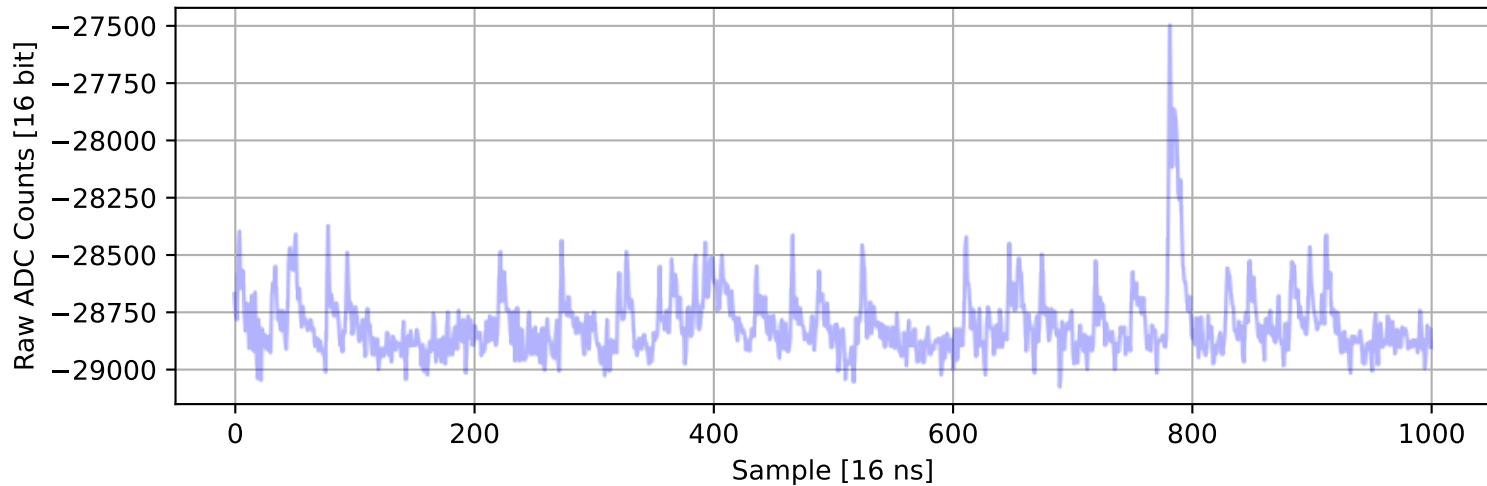
ADC 0, Channel 61, Event 61: Max Signal Waveform



ADC 0, Channel 62, Event 50: Max Signal Waveform

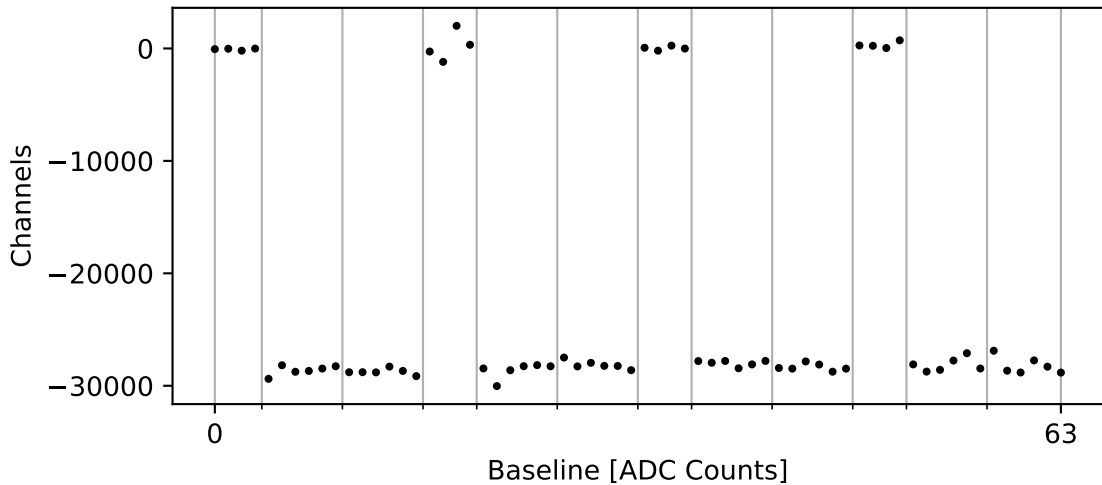


ADC 0, Channel 63, Event 81: Max Signal Waveform





ADC 0: Baseline Distribution



ADC 0: Max Signal Distribution

