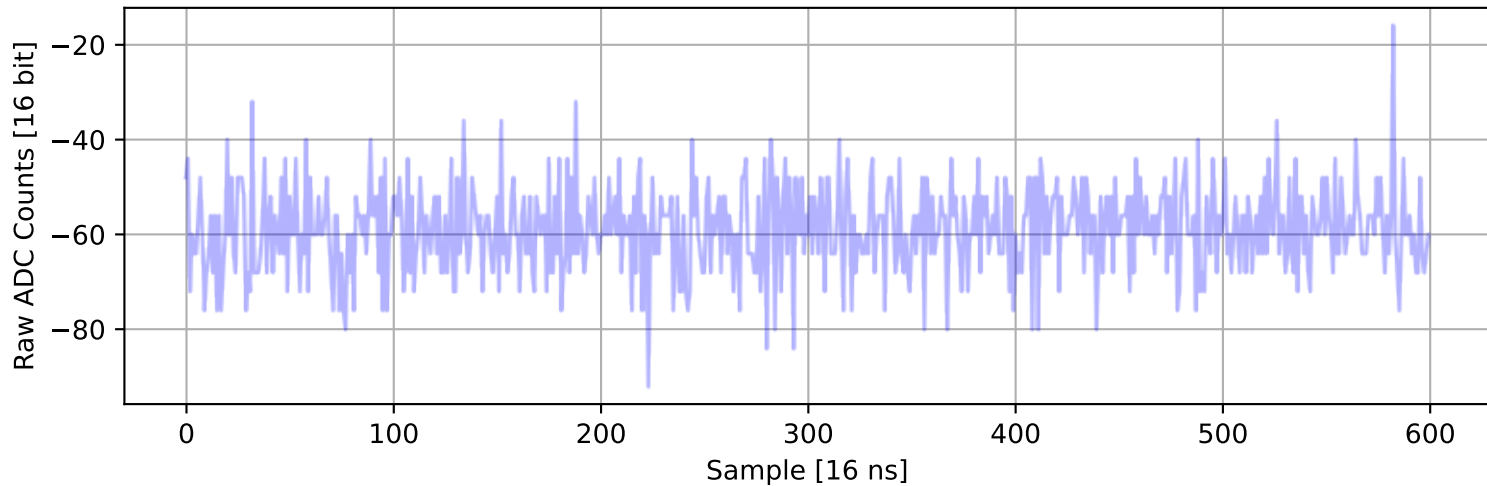
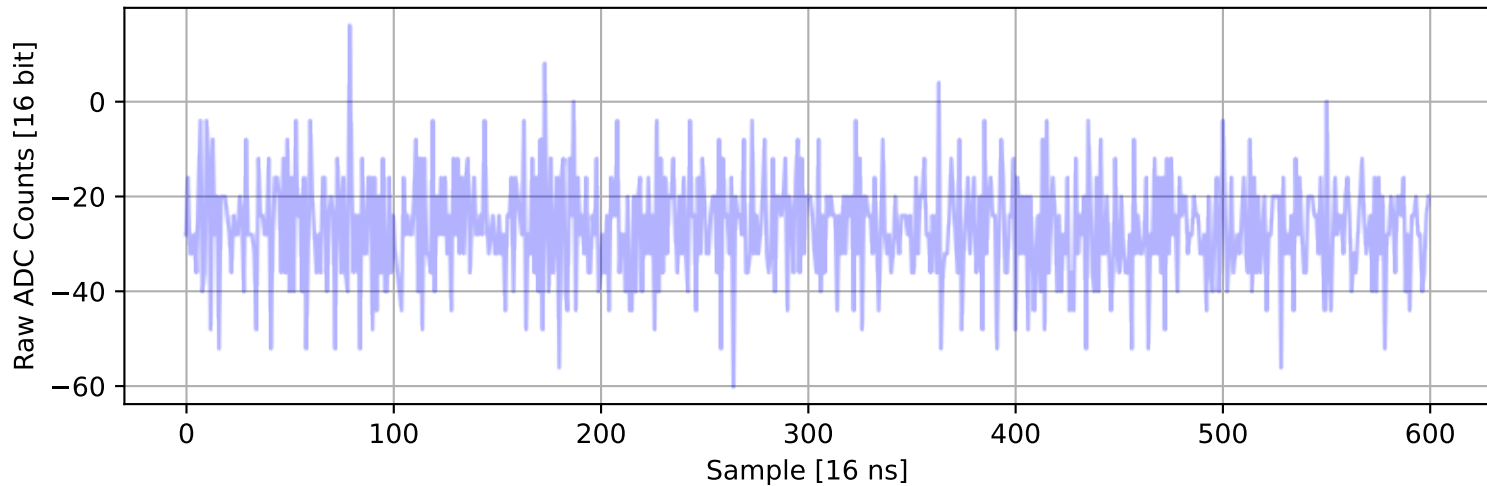


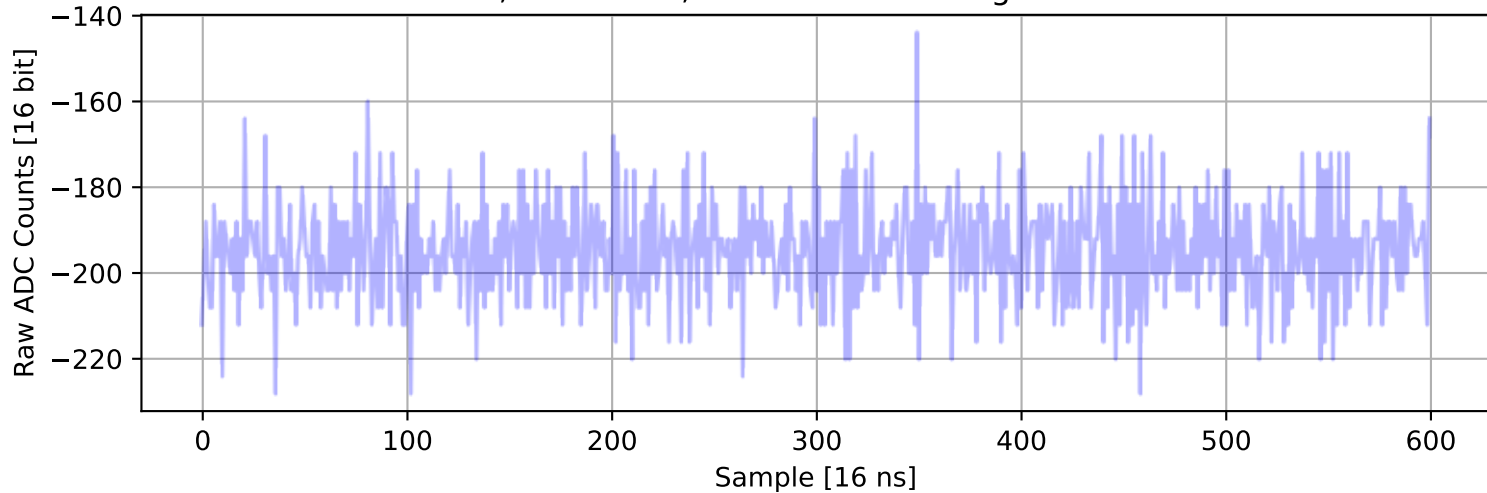
ADC 0, Channel 0, Event 176: Max Signal Waveform



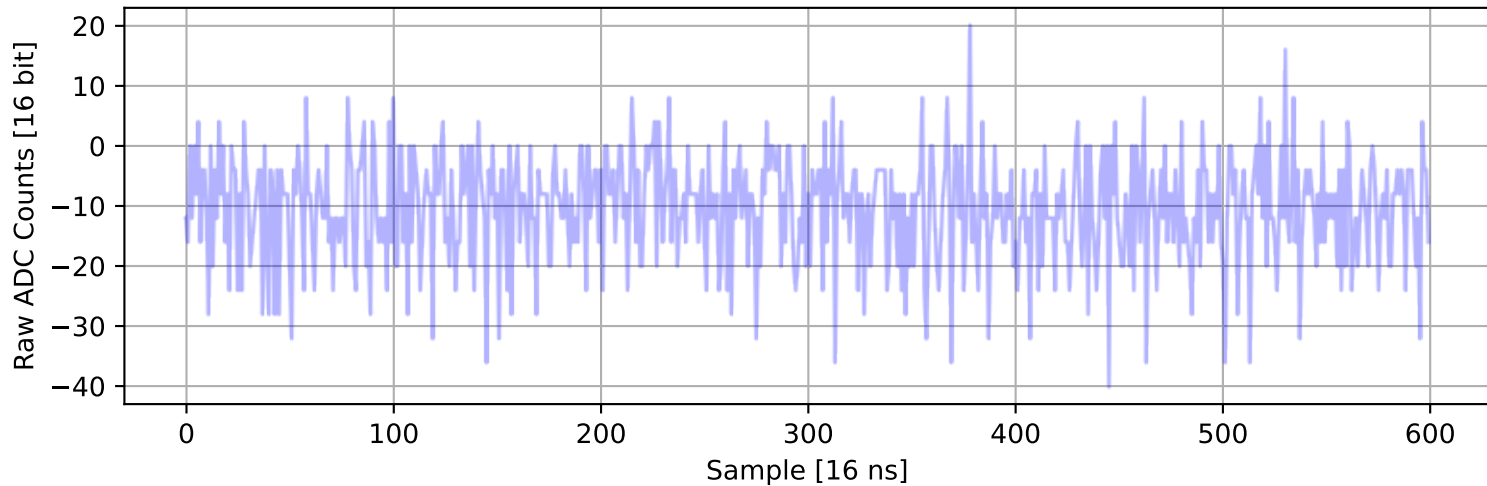
ADC 0, Channel 1, Event 47: Max Signal Waveform



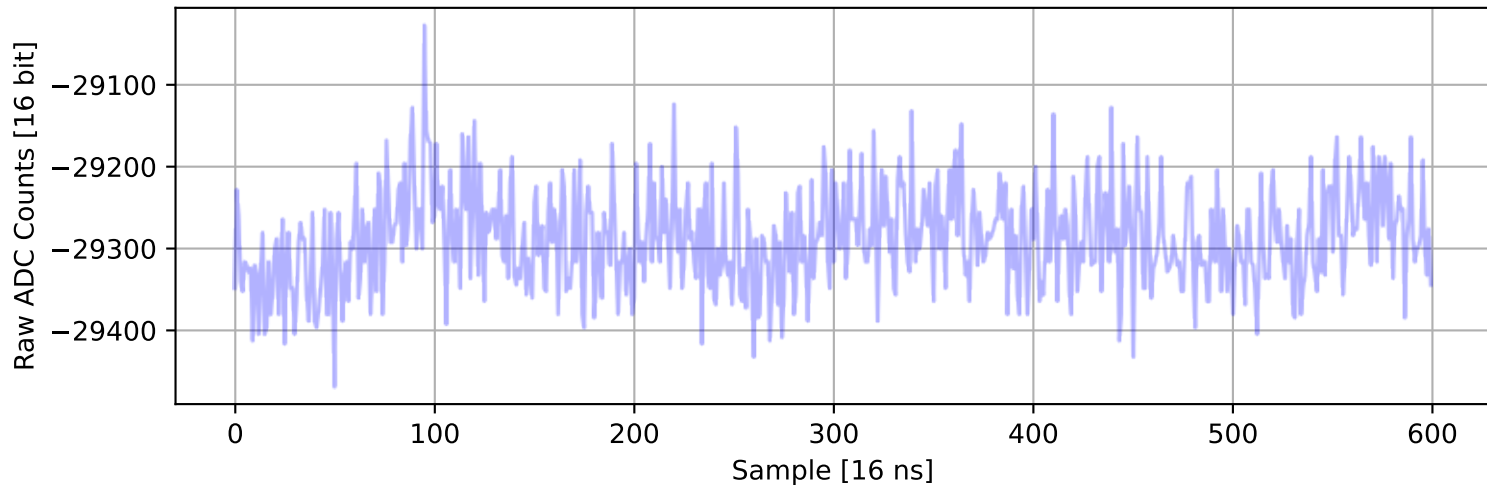
ADC 0, Channel 2, Event 135: Max Signal Waveform



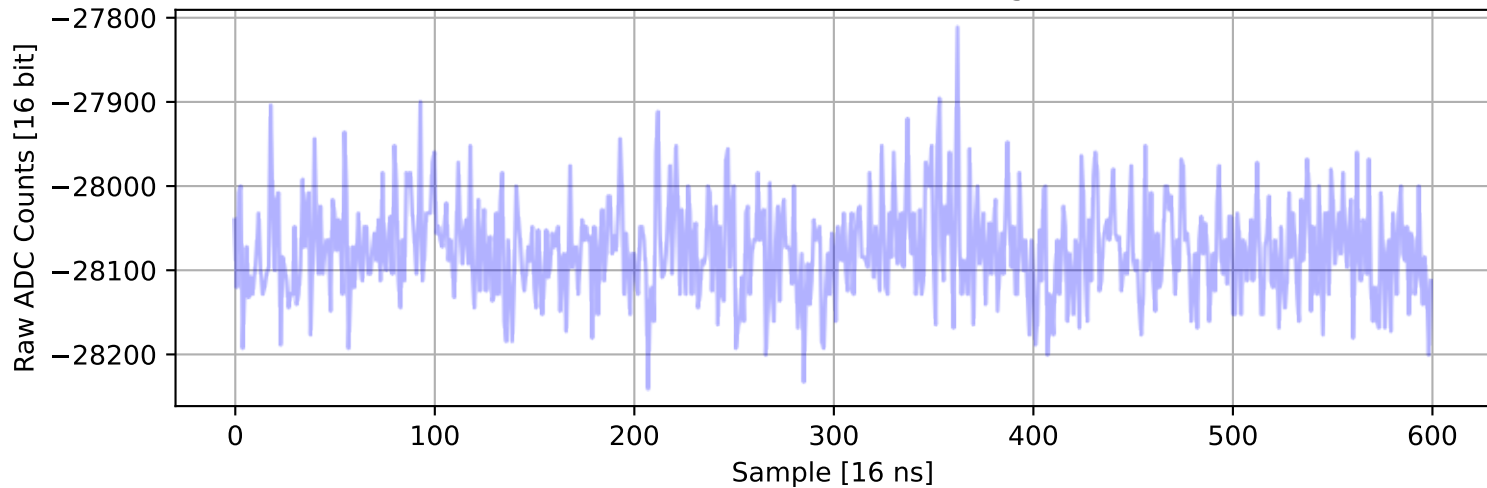
ADC 0, Channel 3, Event 61: Max Signal Waveform



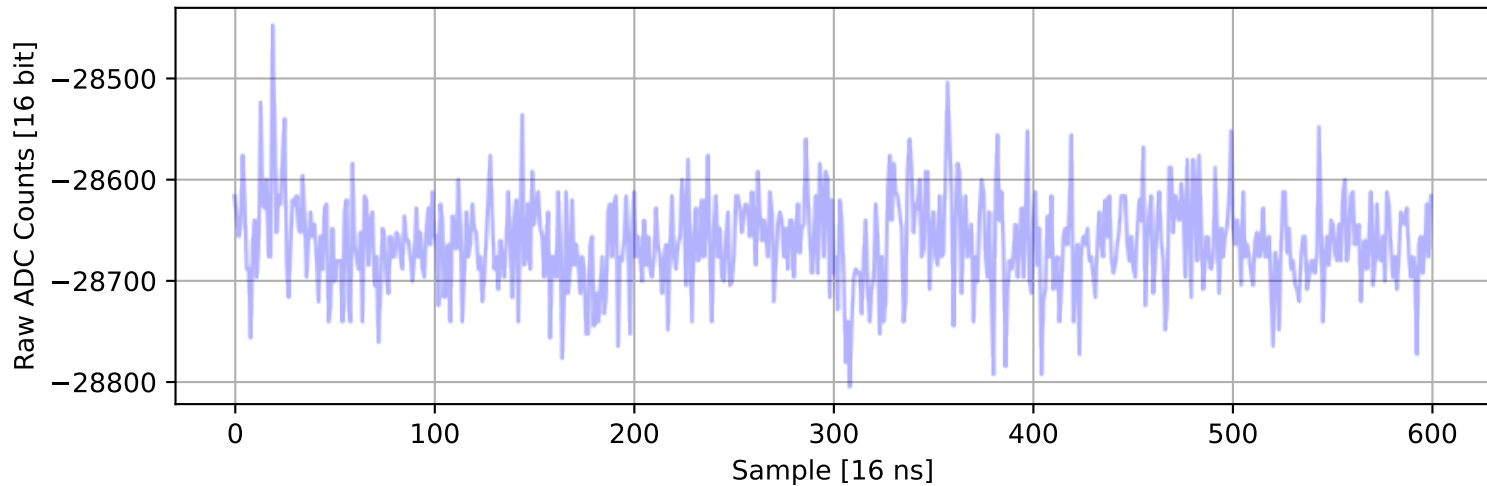
ADC 0, Channel 4, Event 36: Max Signal Waveform



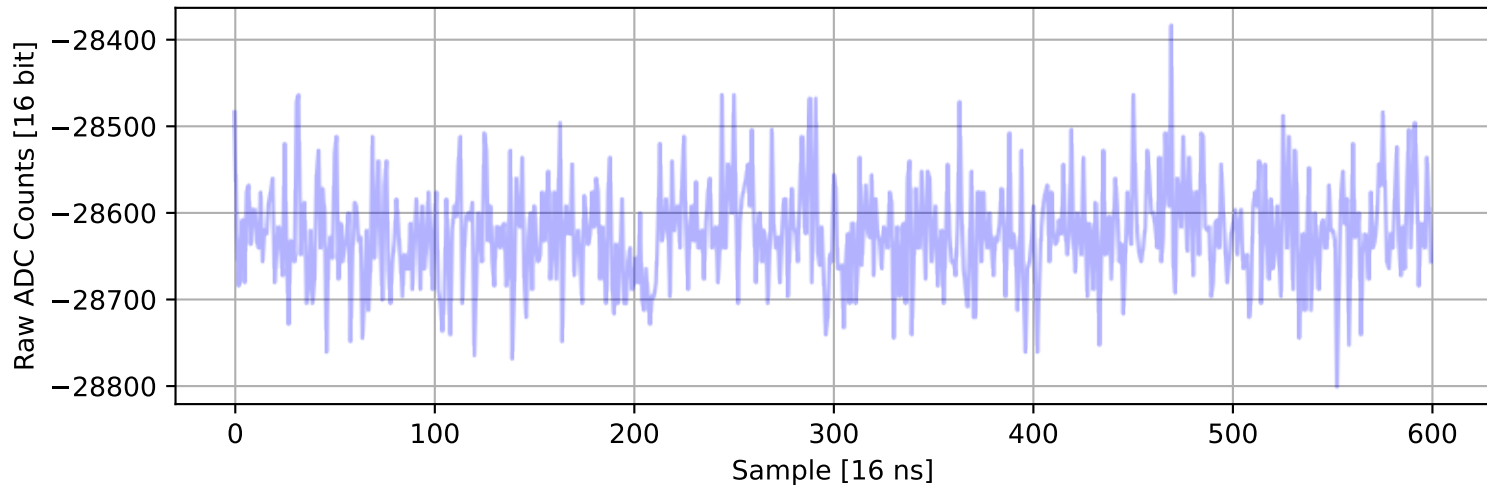
ADC 0, Channel 5, Event 107: Max Signal Waveform



ADC 0, Channel 6, Event 2: Max Signal Waveform

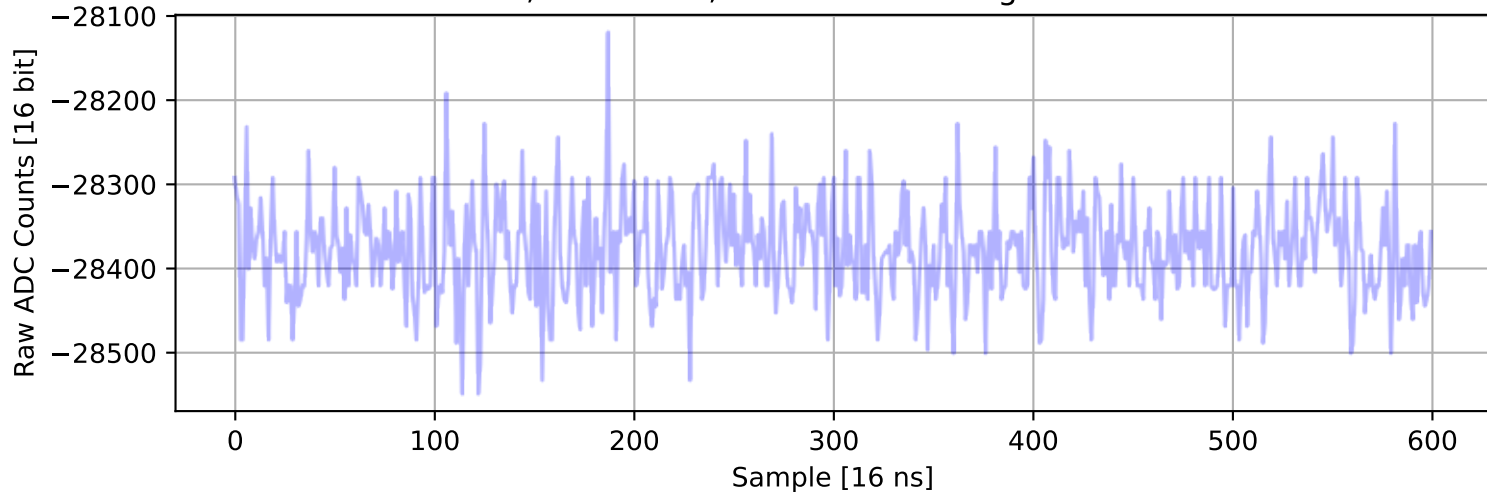


ADC 0, Channel 7, Event 22: Max Signal Waveform

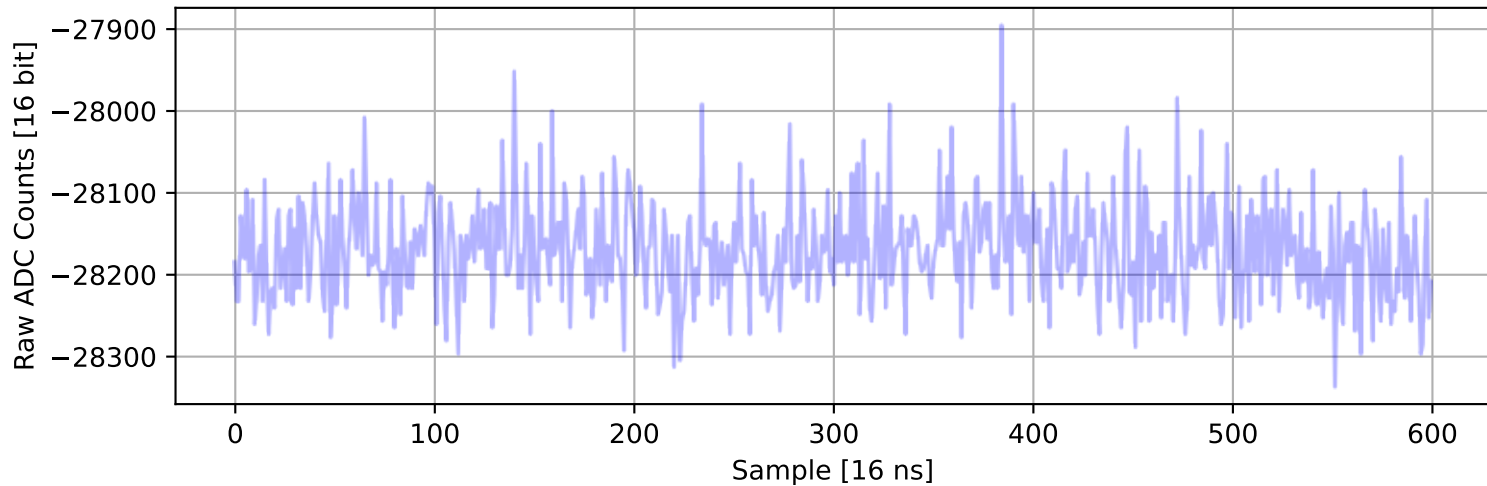




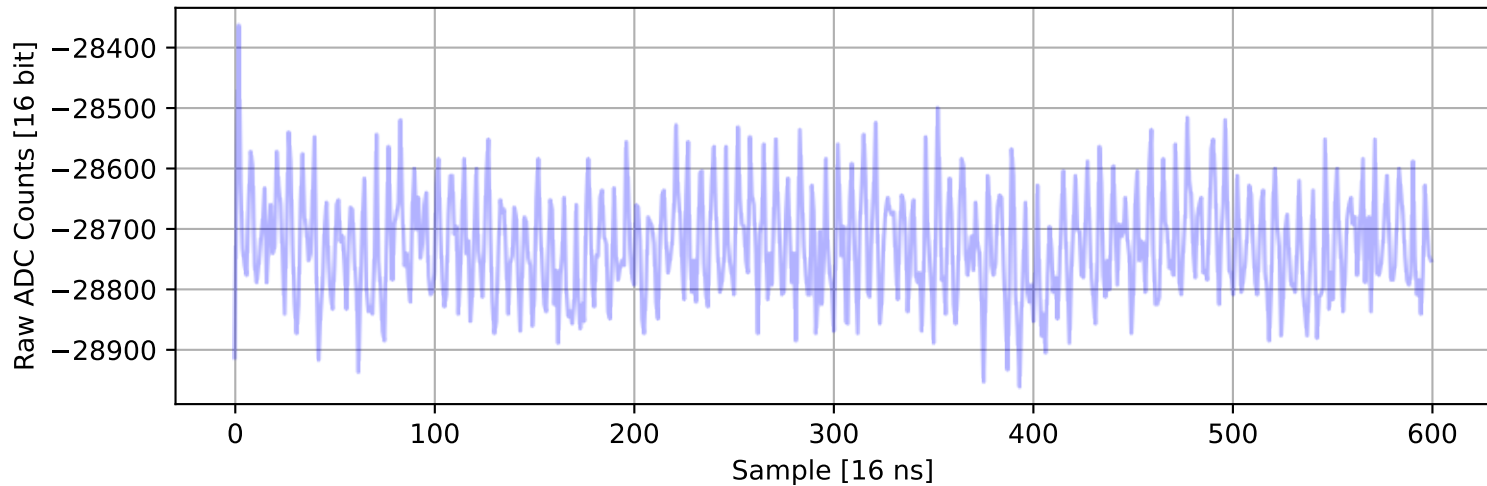
ADC 0, Channel 8, Event 95: Max Signal Waveform



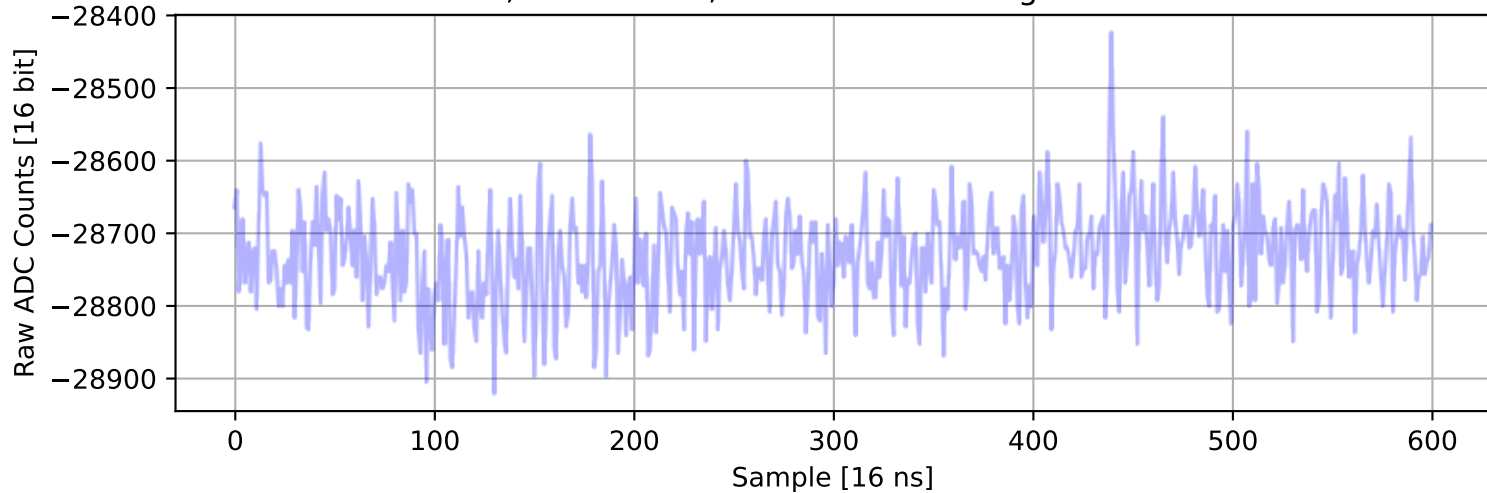
ADC 0, Channel 9, Event 181: Max Signal Waveform



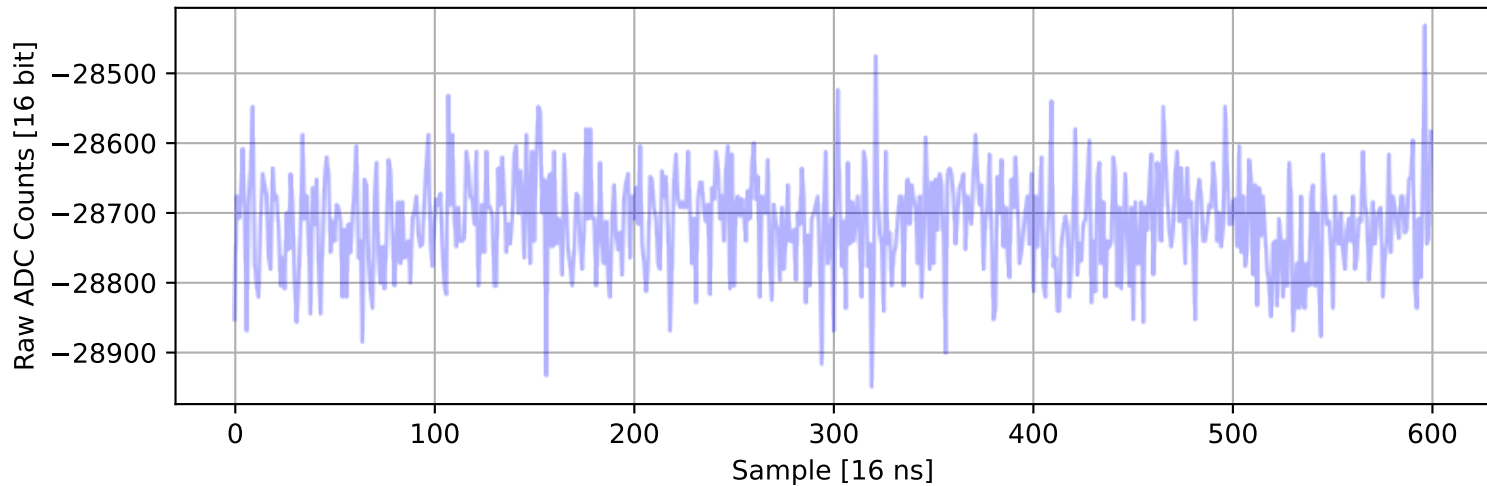
ADC 0, Channel 10, Event 158: Max Signal Waveform



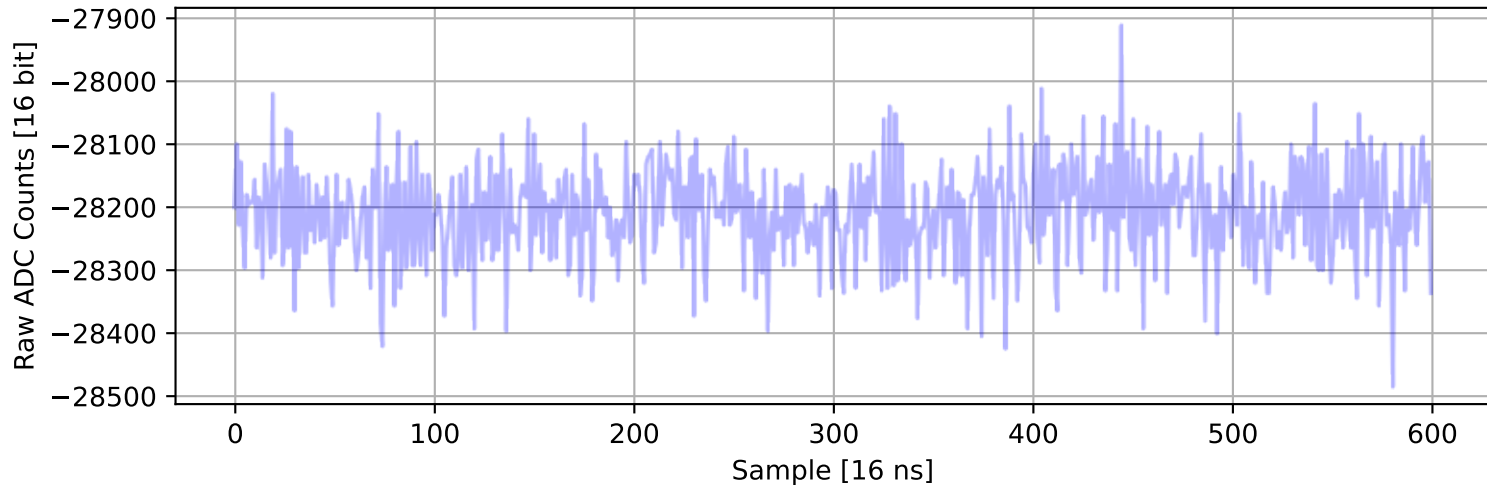
ADC 0, Channel 11, Event 105: Max Signal Waveform



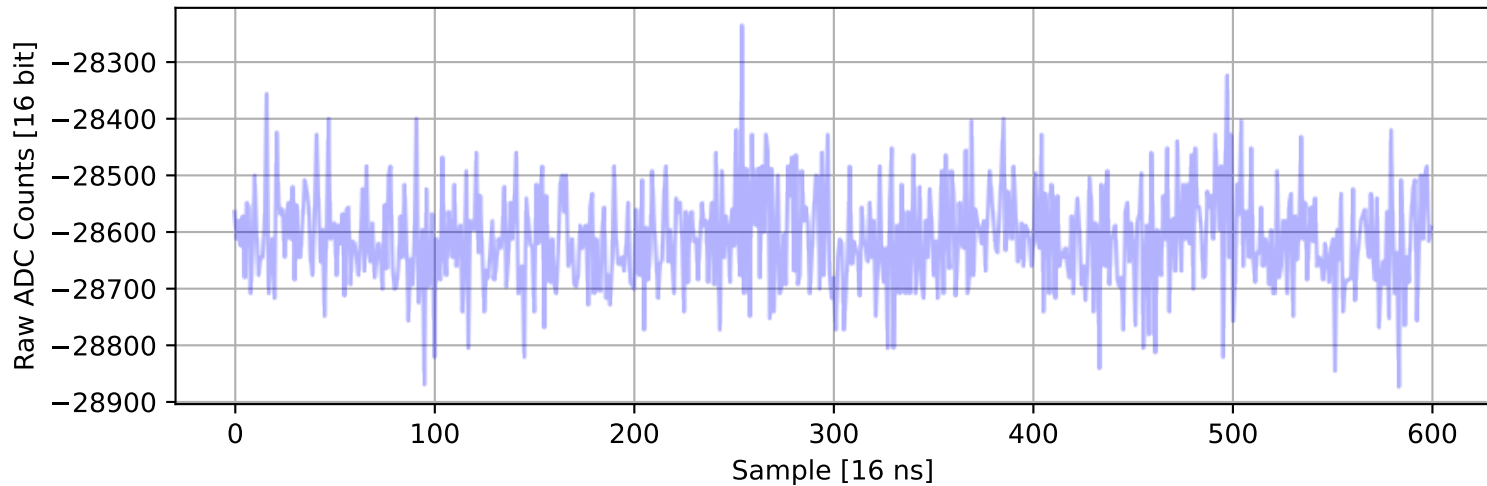
ADC 0, Channel 12, Event 33: Max Signal Waveform



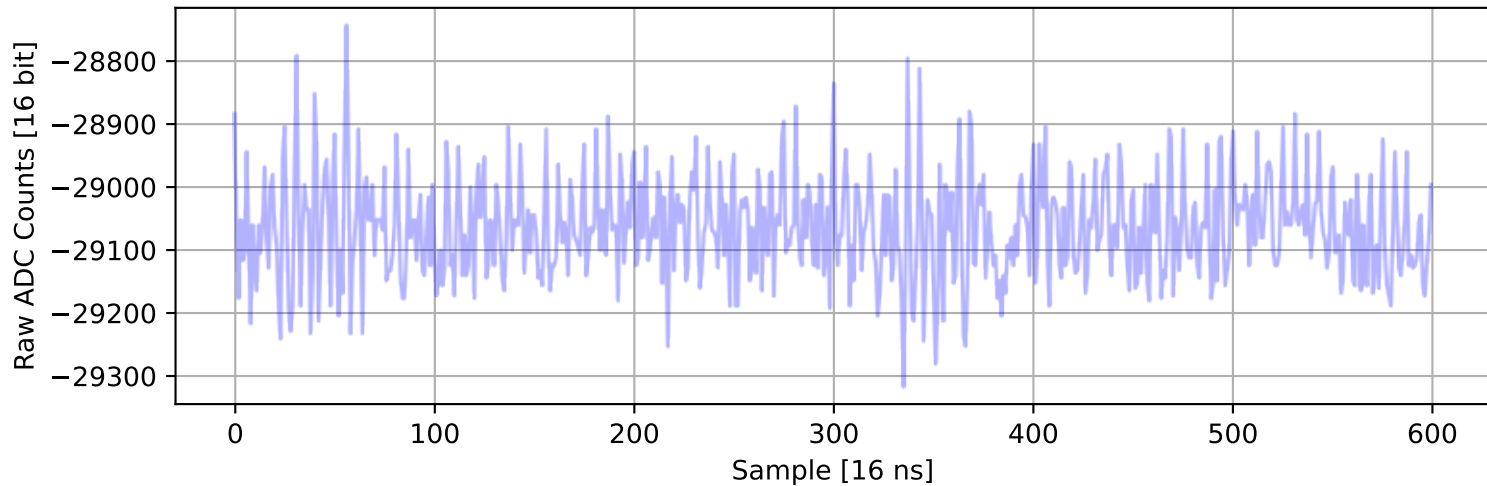
ADC 0, Channel 13, Event 152: Max Signal Waveform



ADC 0, Channel 14, Event 160: Max Signal Waveform

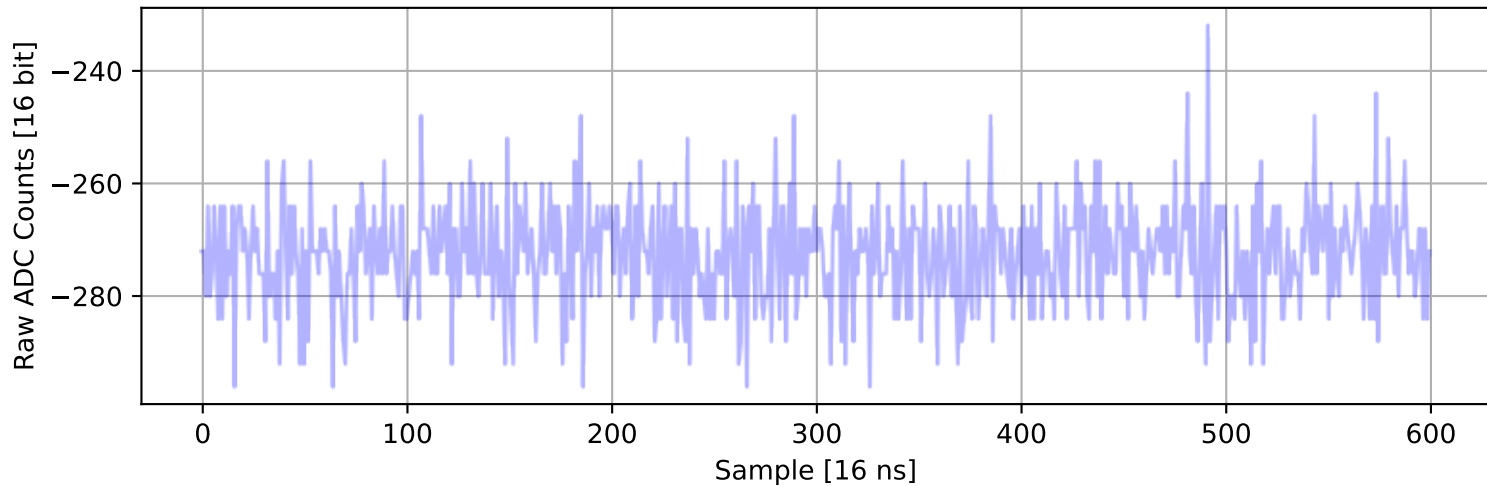


ADC 0, Channel 15, Event 168: Max Signal Waveform

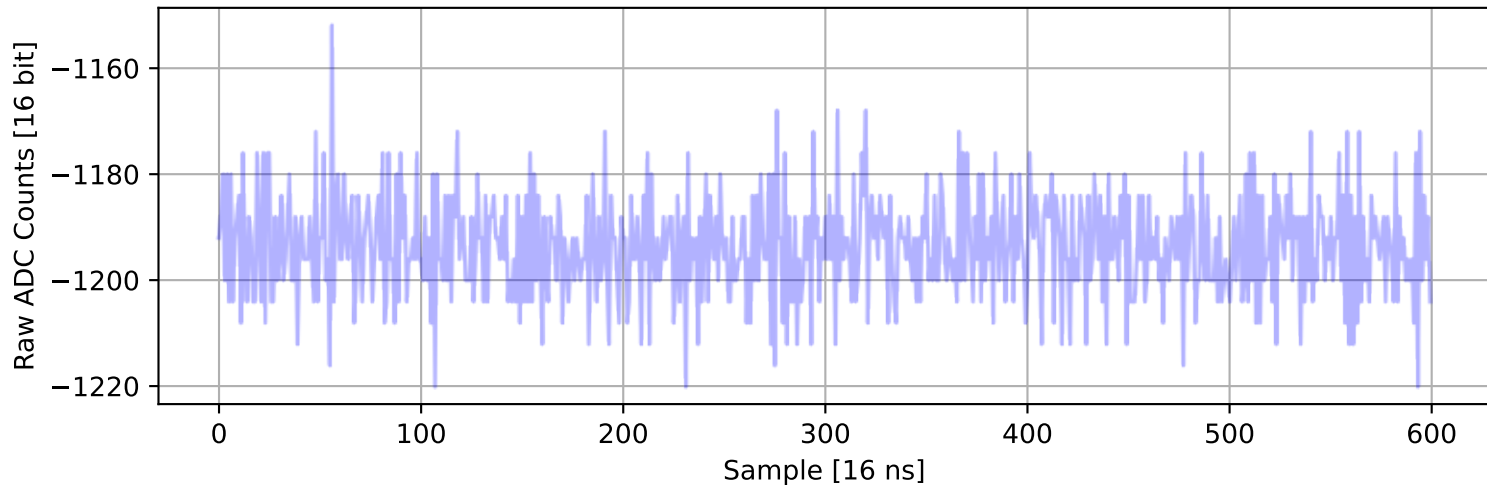




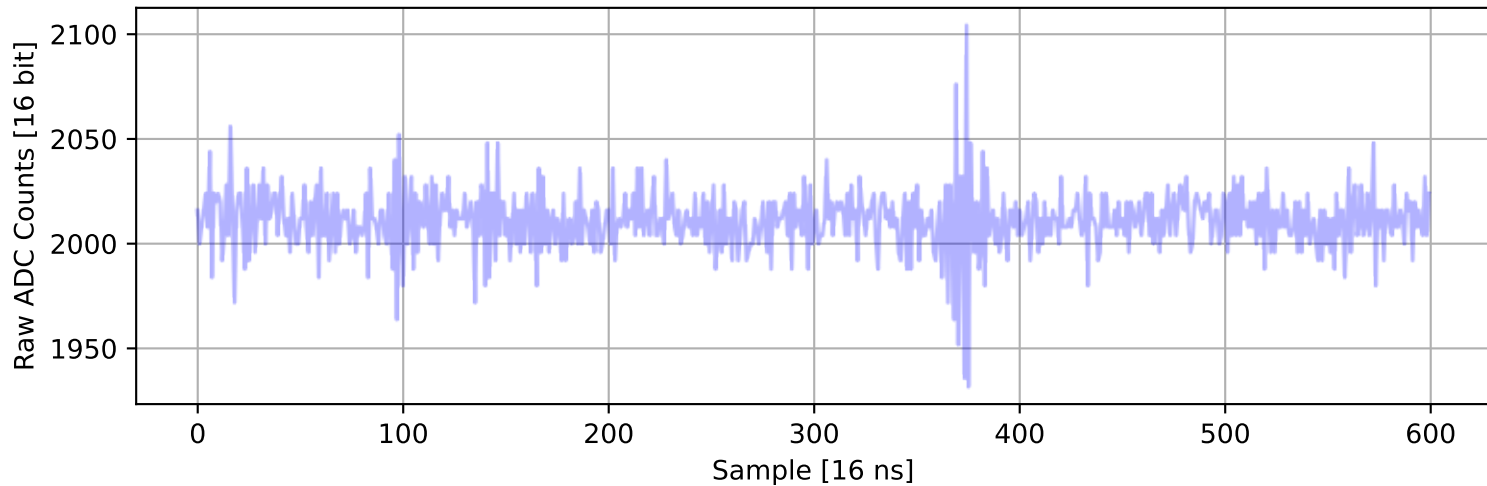
ADC 0, Channel 16, Event 59: Max Signal Waveform



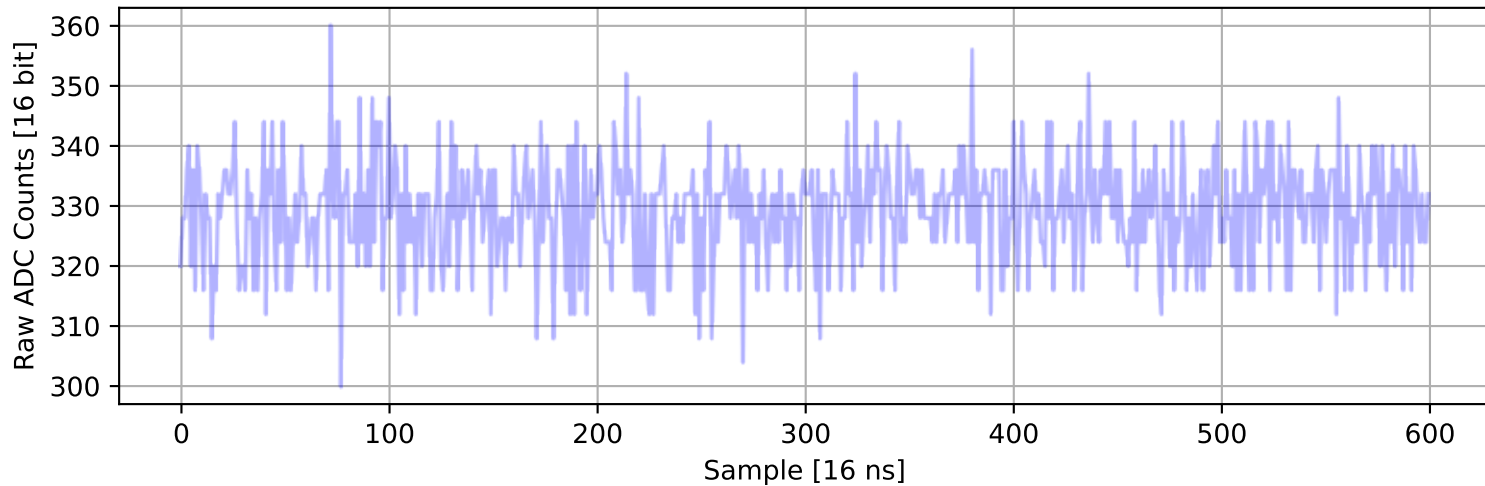
ADC 0, Channel 17, Event 13: Max Signal Waveform



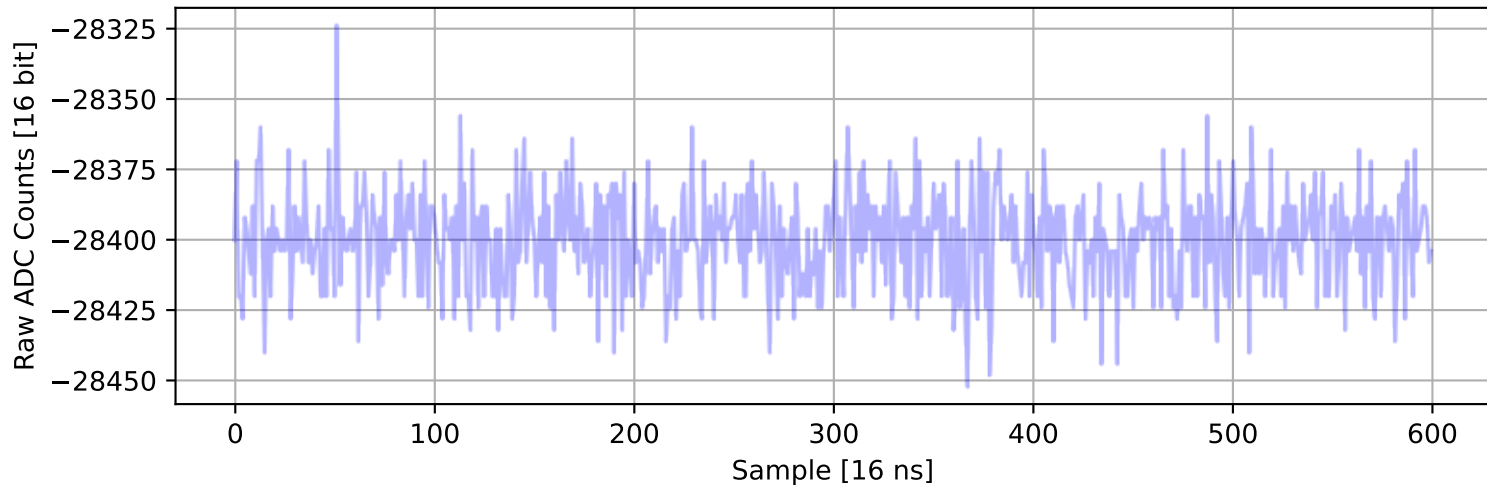
ADC 0, Channel 18, Event 29: Max Signal Waveform



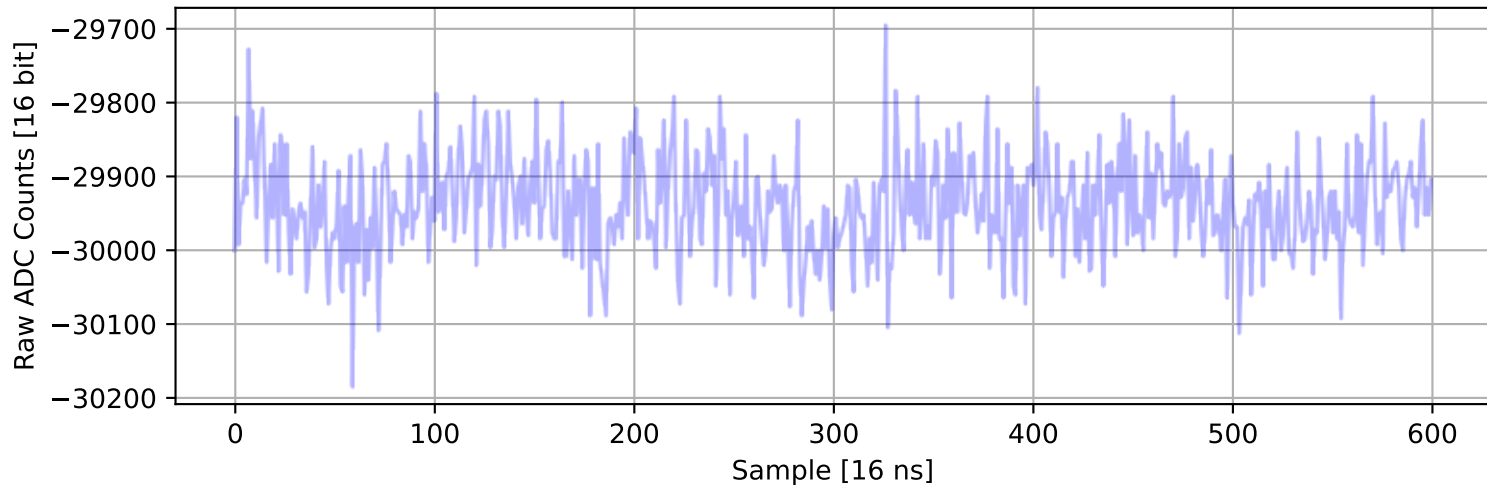
ADC 0, Channel 19, Event 13: Max Signal Waveform



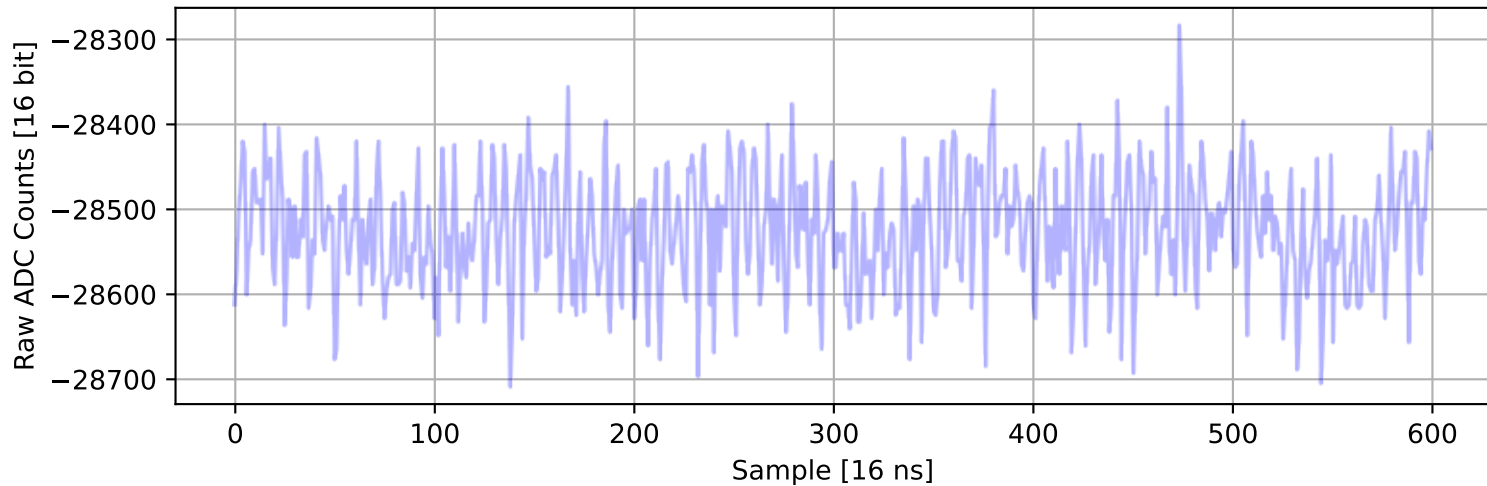
ADC 0, Channel 20, Event 32: Max Signal Waveform



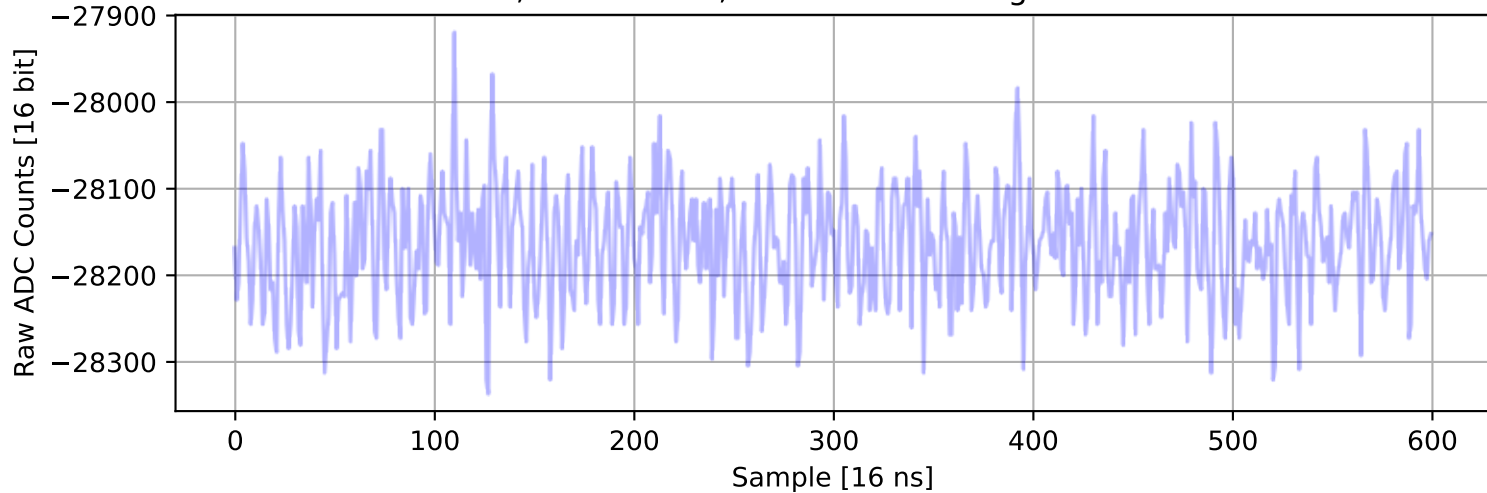
ADC 0, Channel 21, Event 81: Max Signal Waveform



ADC 0, Channel 22, Event 43: Max Signal Waveform

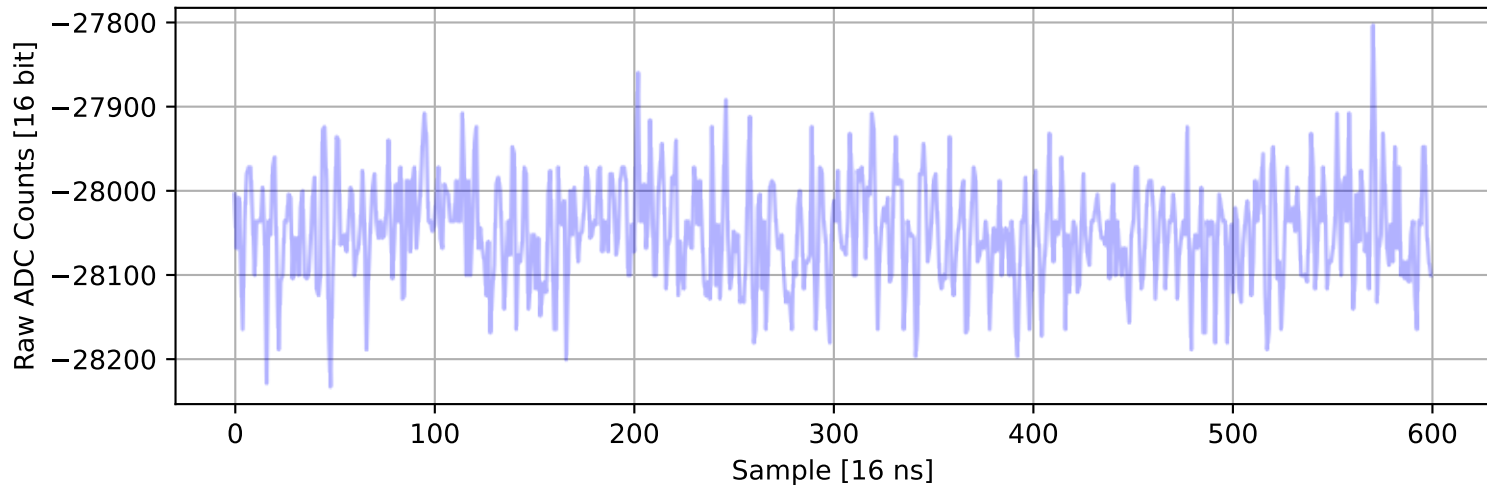


ADC 0, Channel 23, Event 15: Max Signal Waveform

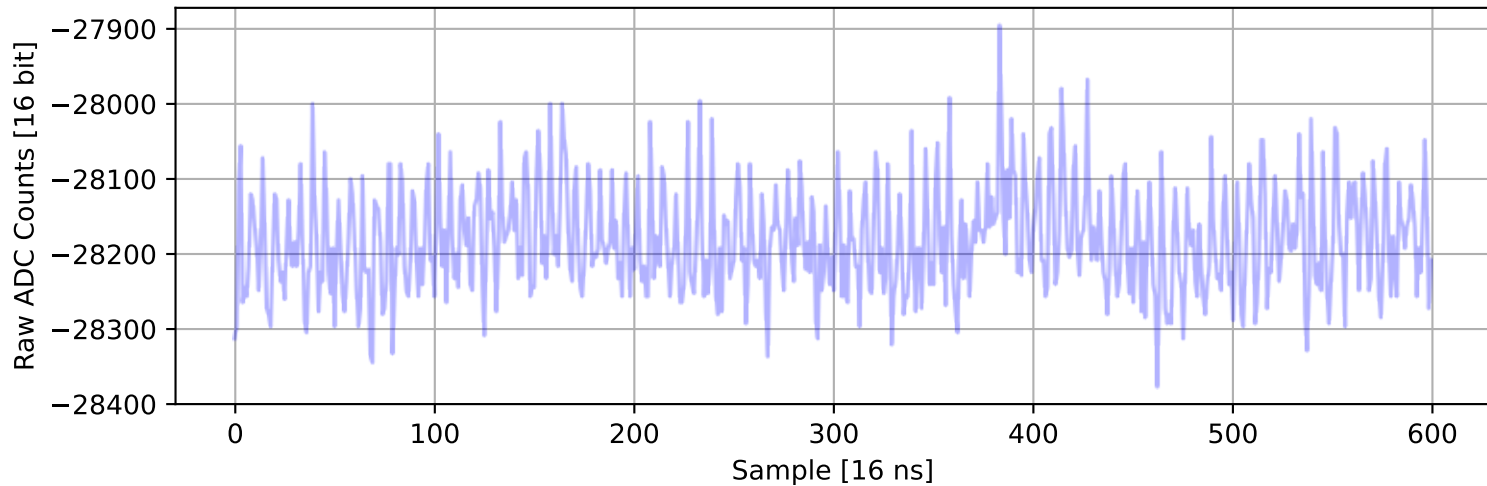




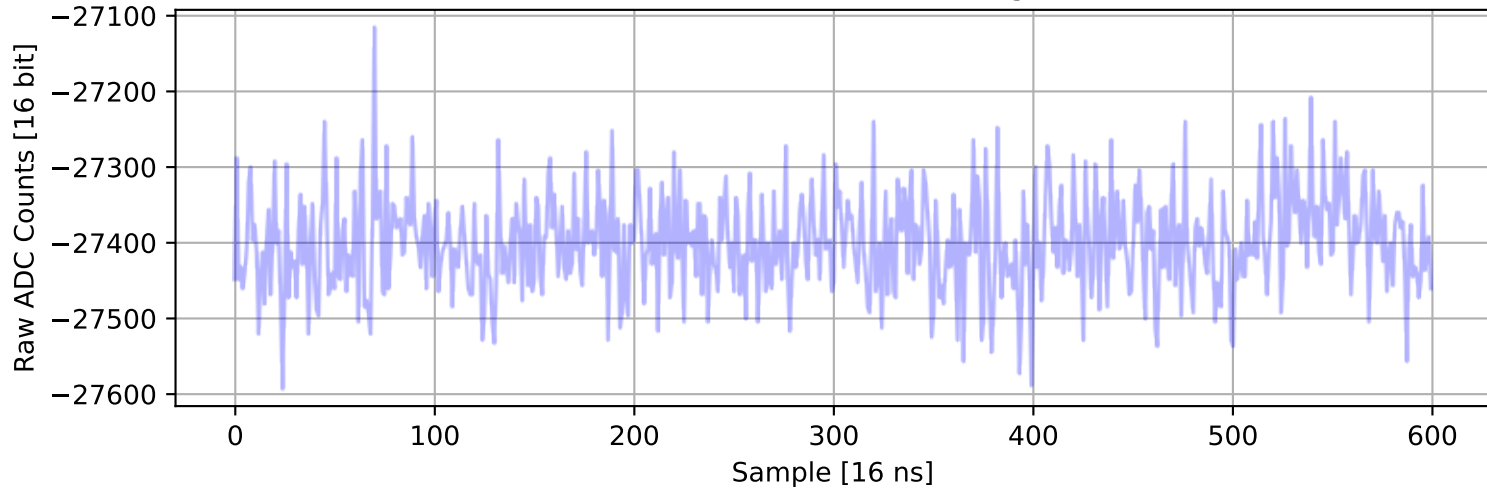
ADC 0, Channel 24, Event 157: Max Signal Waveform



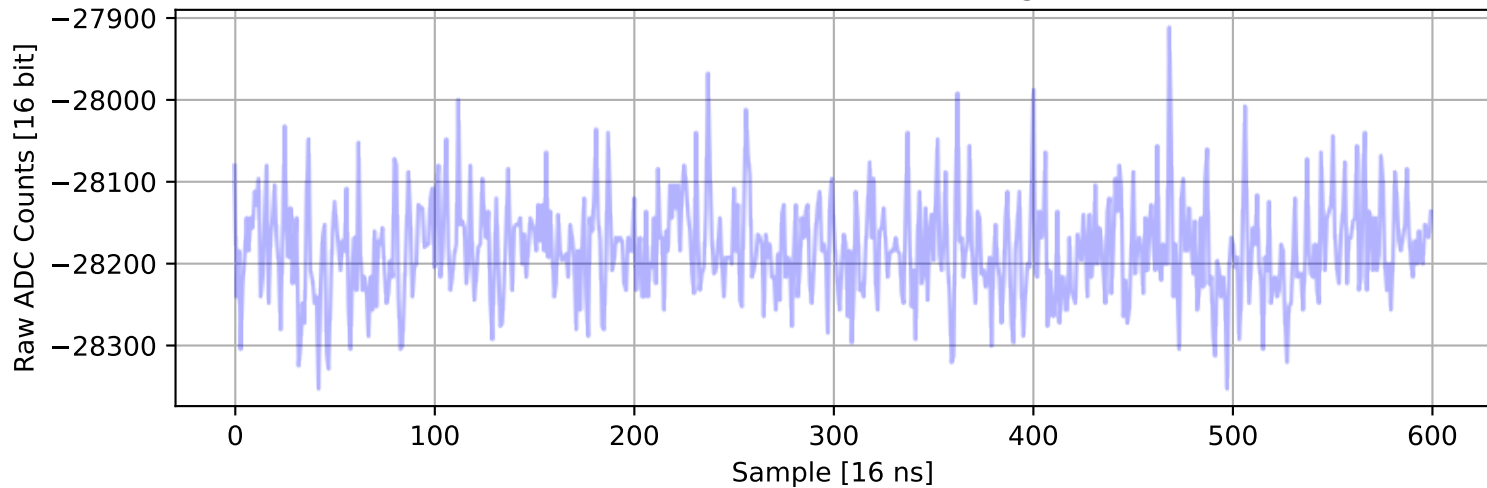
ADC 0, Channel 25, Event 26: Max Signal Waveform



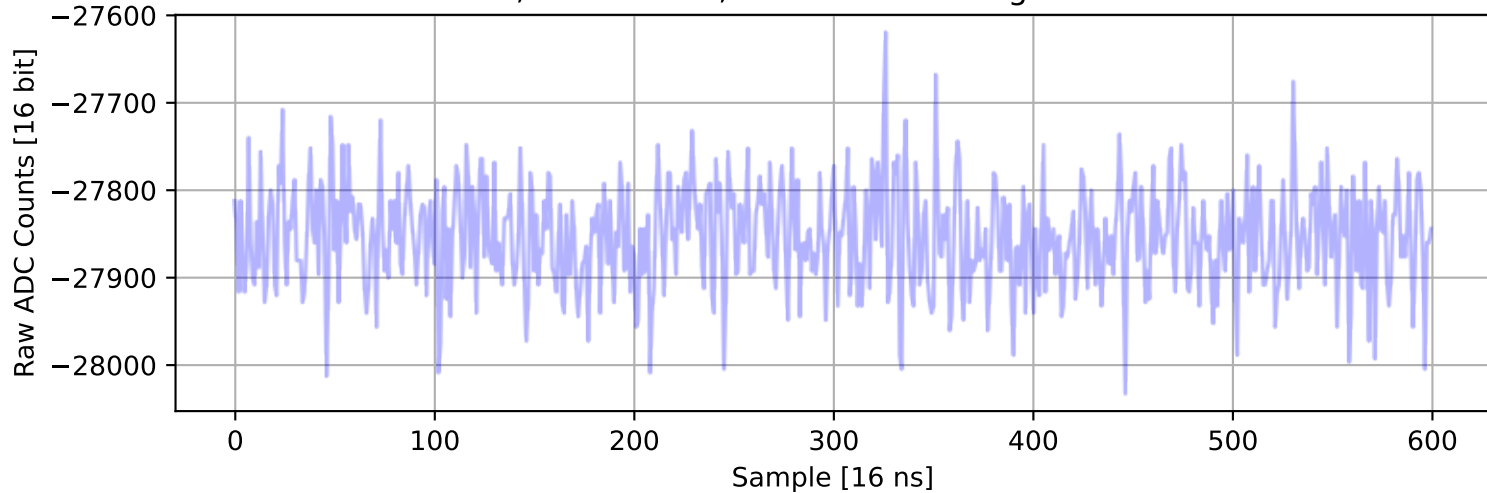
ADC 0, Channel 26, Event 23: Max Signal Waveform



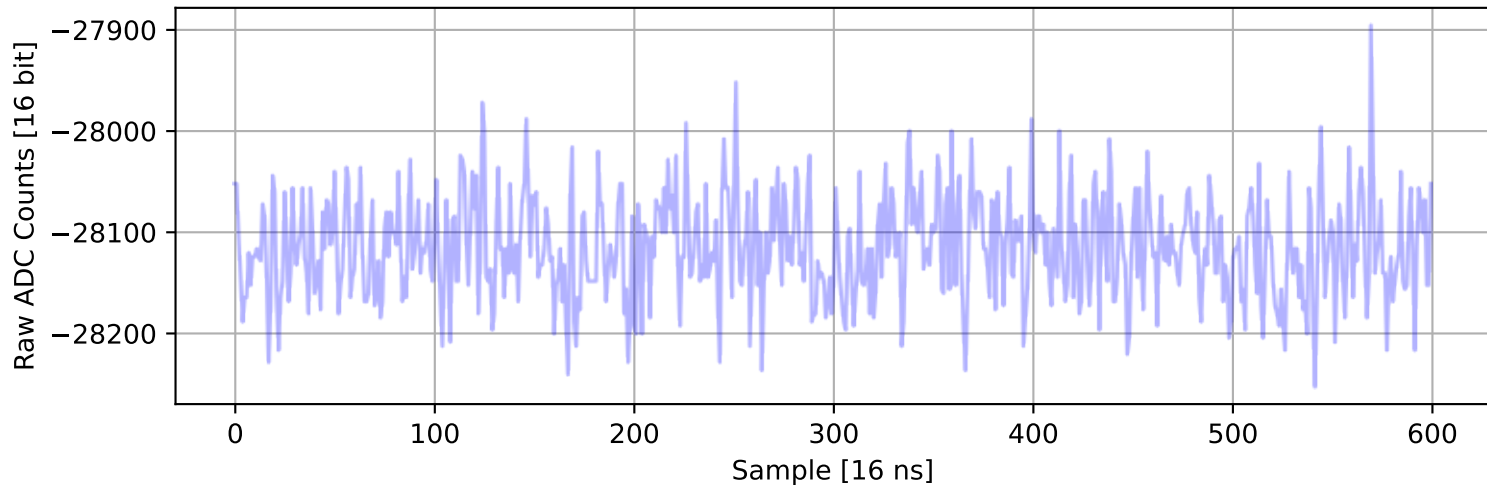
ADC 0, Channel 27, Event 169: Max Signal Waveform



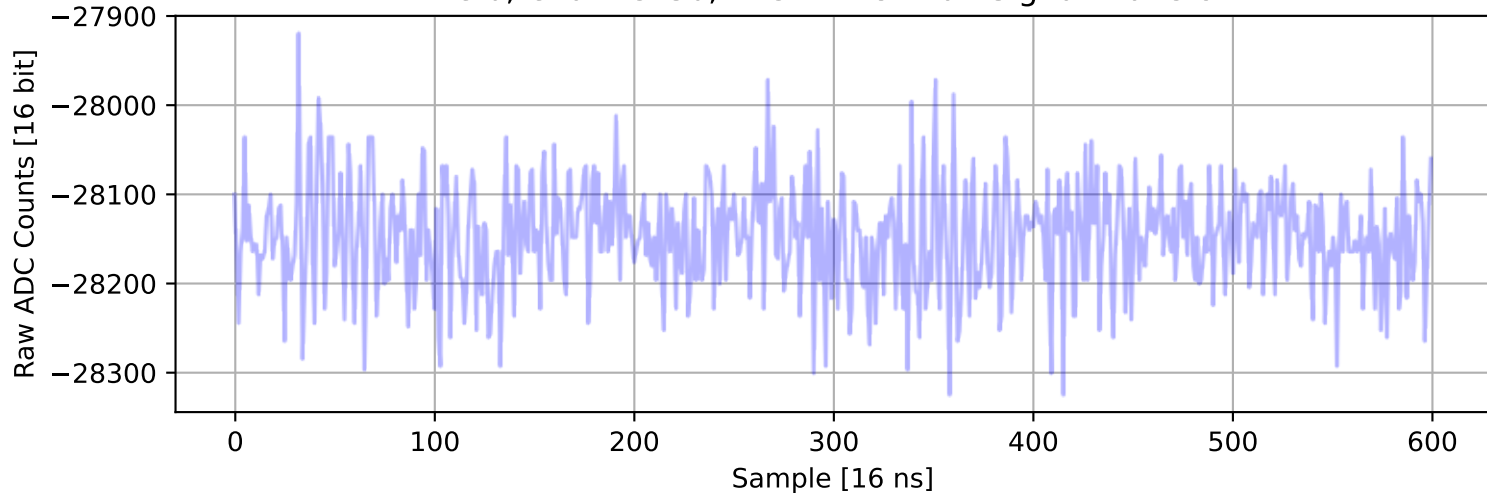
ADC 0, Channel 28, Event 27: Max Signal Waveform



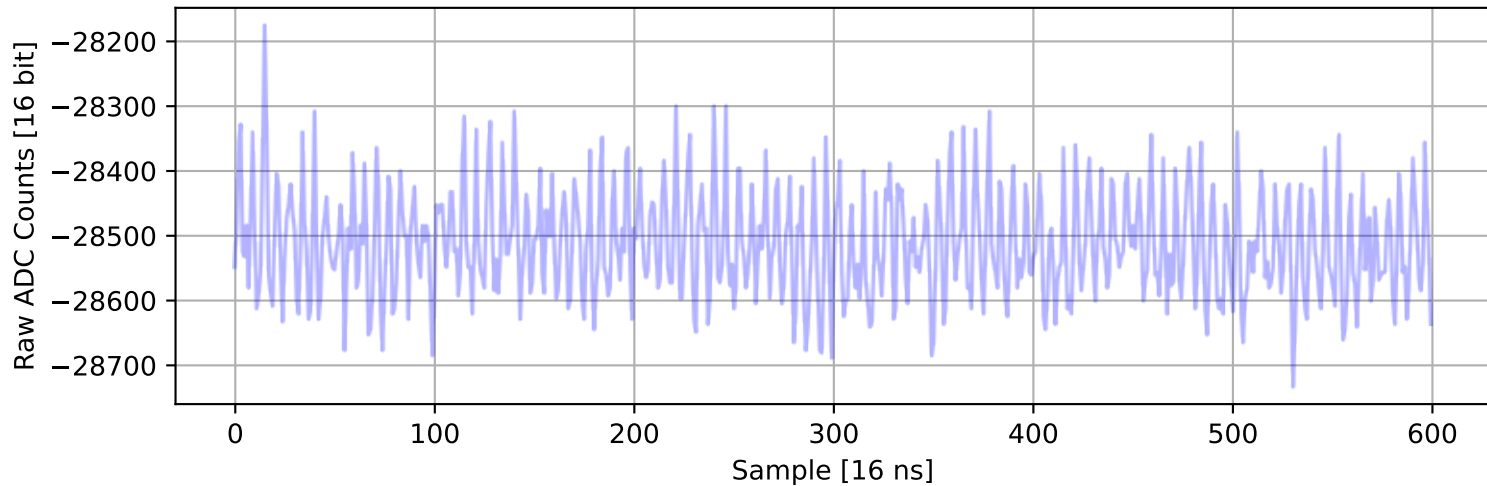
ADC 0, Channel 29, Event 92: Max Signal Waveform



ADC 0, Channel 30, Event 118: Max Signal Waveform

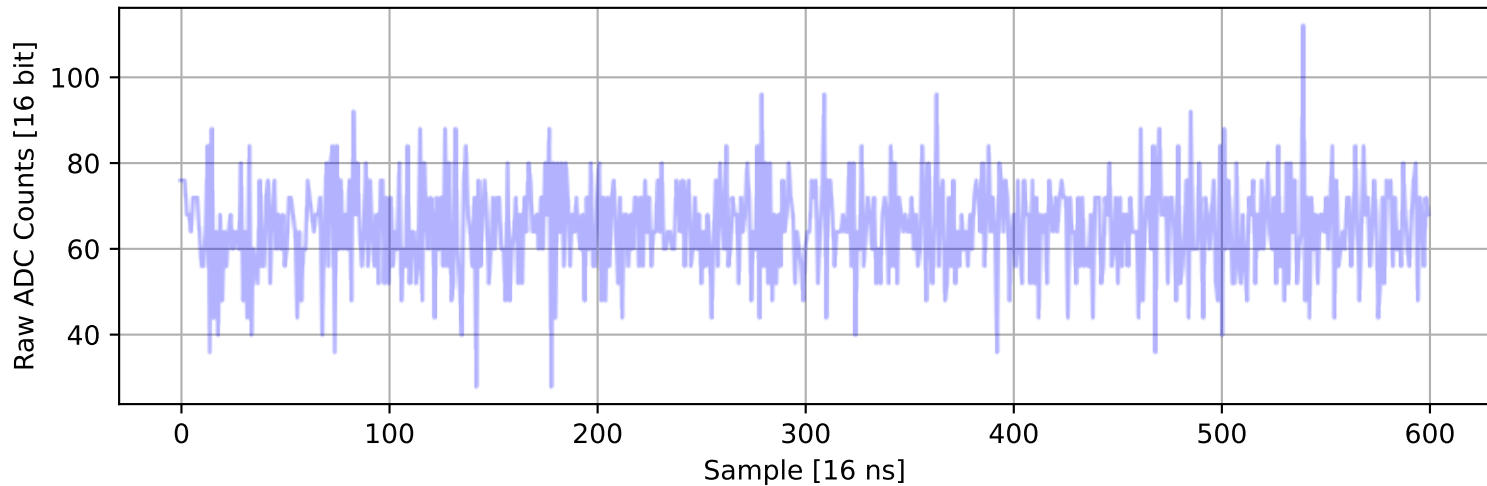


ADC 0, Channel 31, Event 147: Max Signal Waveform

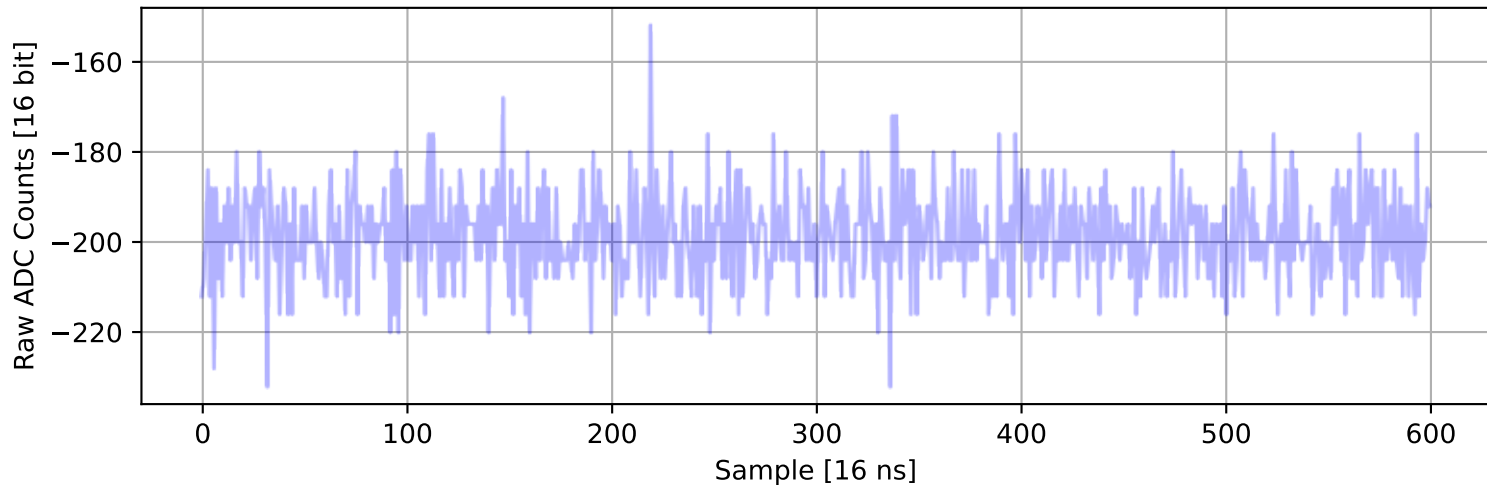




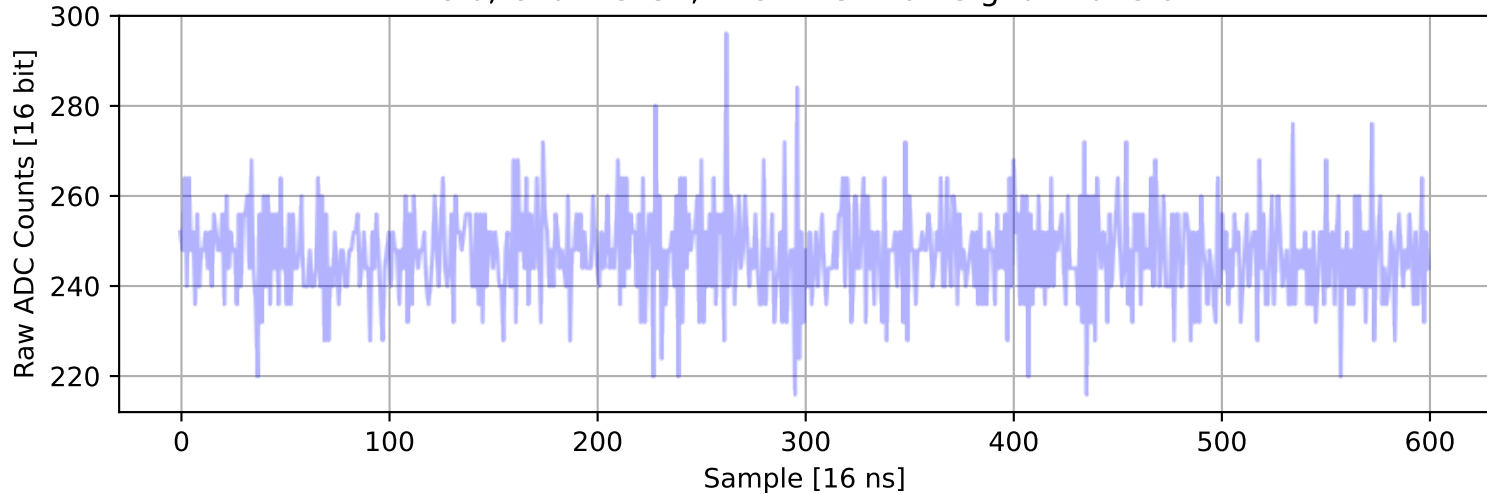
ADC 0, Channel 32, Event 100: Max Signal Waveform



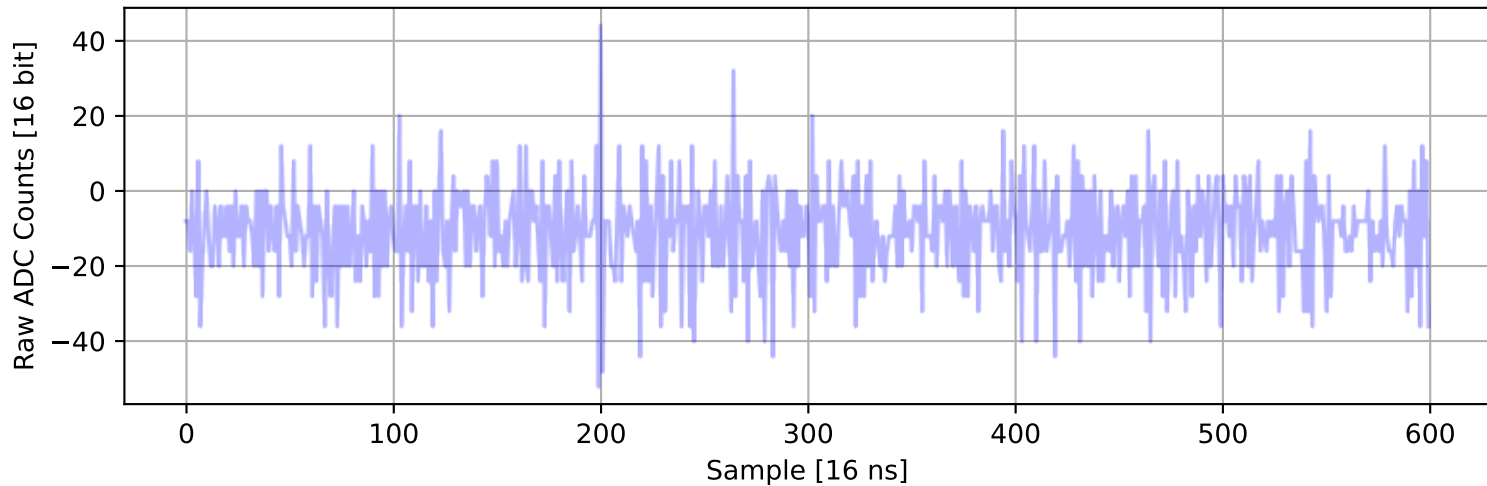
ADC 0, Channel 33, Event 90: Max Signal Waveform



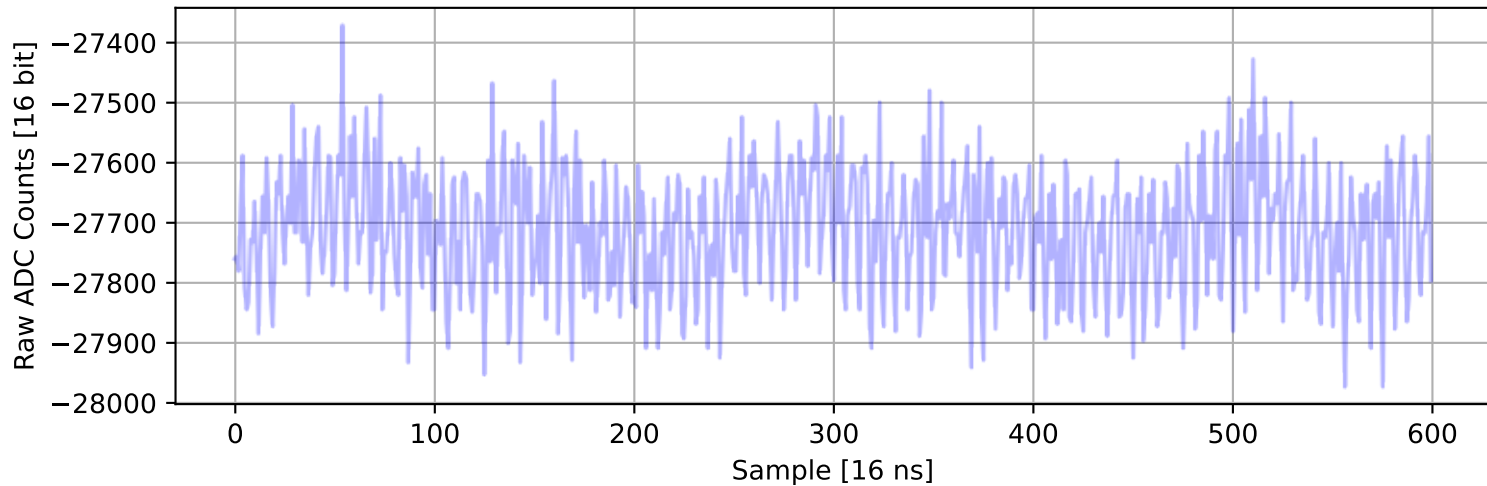
ADC 0, Channel 34, Event 23: Max Signal Waveform



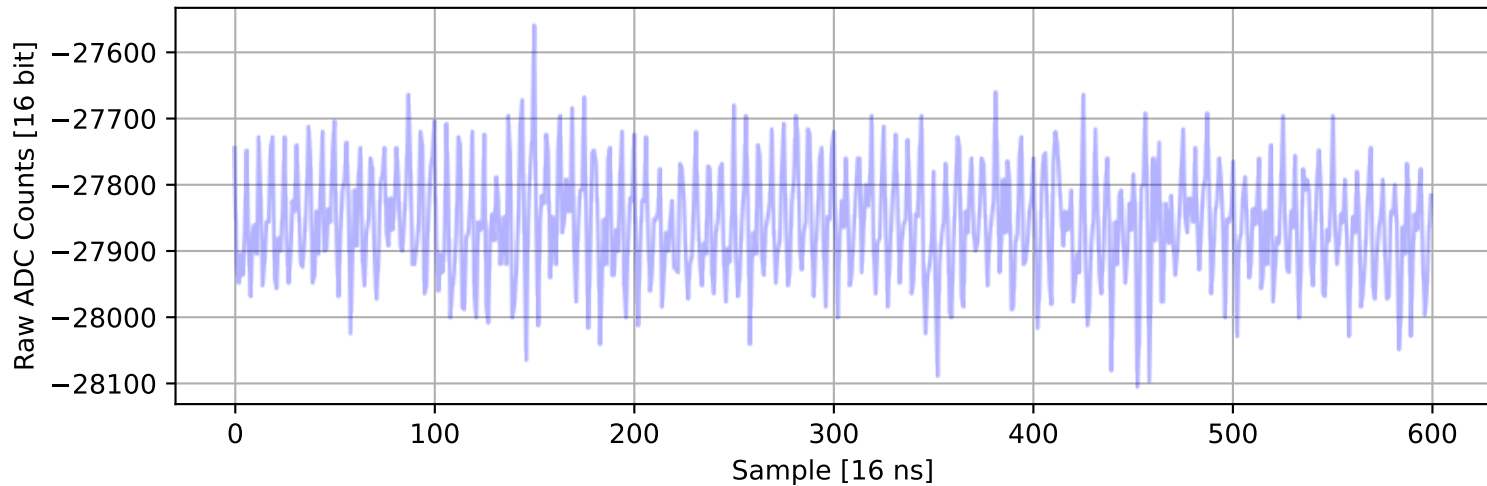
ADC 0, Channel 35, Event 29: Max Signal Waveform



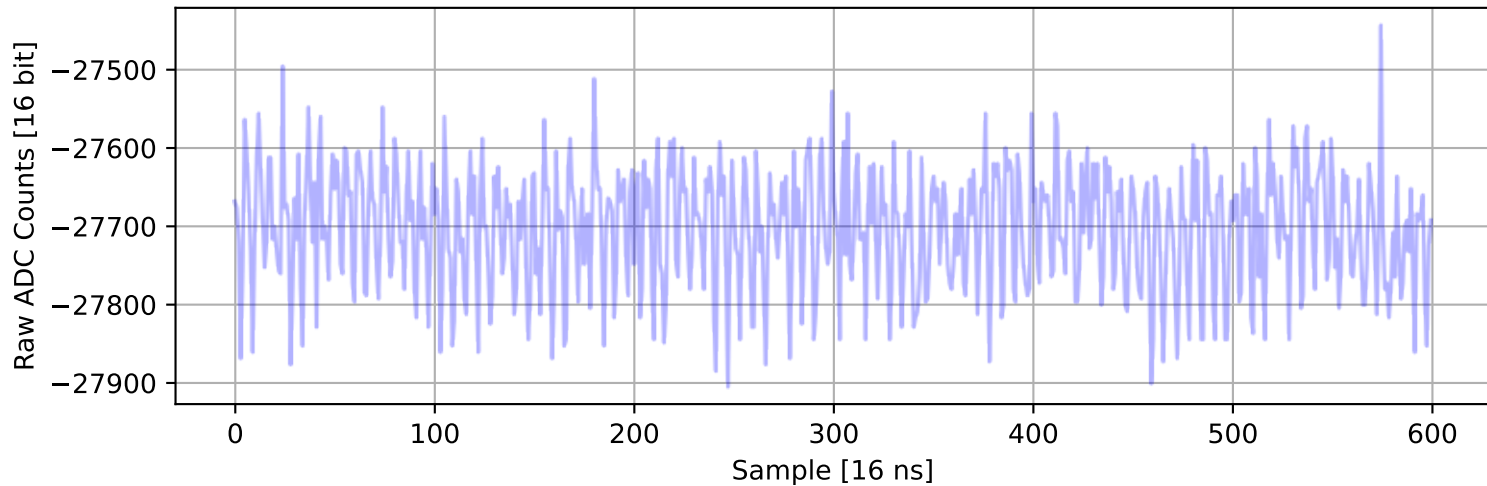
ADC 0, Channel 36, Event 3: Max Signal Waveform



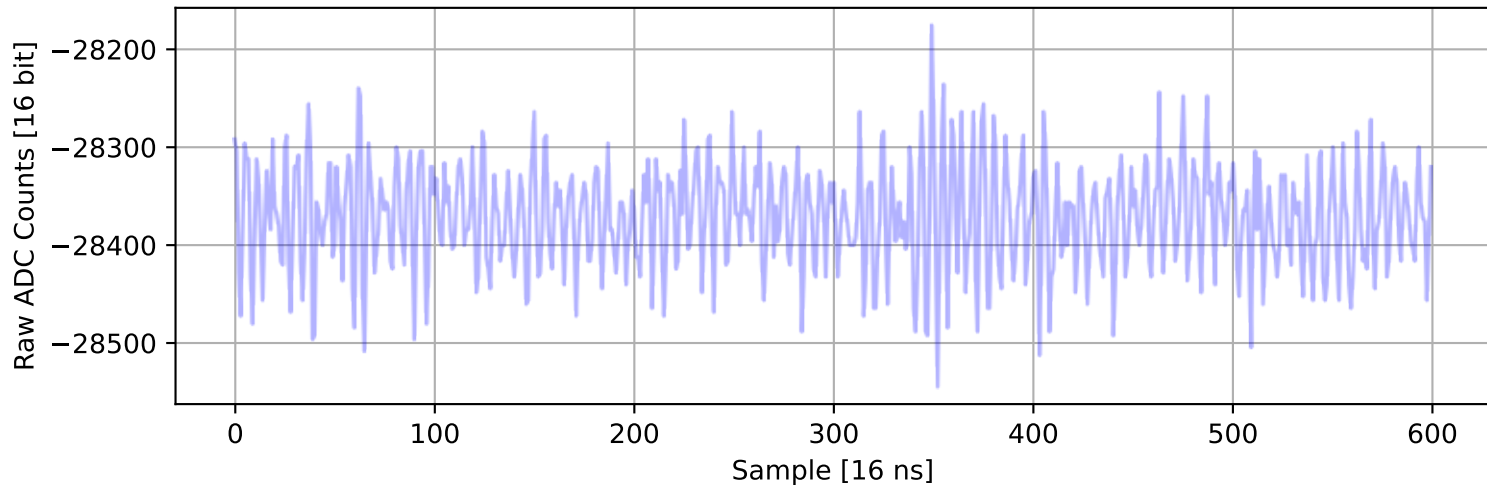
ADC 0, Channel 37, Event 57: Max Signal Waveform



ADC 0, Channel 38, Event 142: Max Signal Waveform

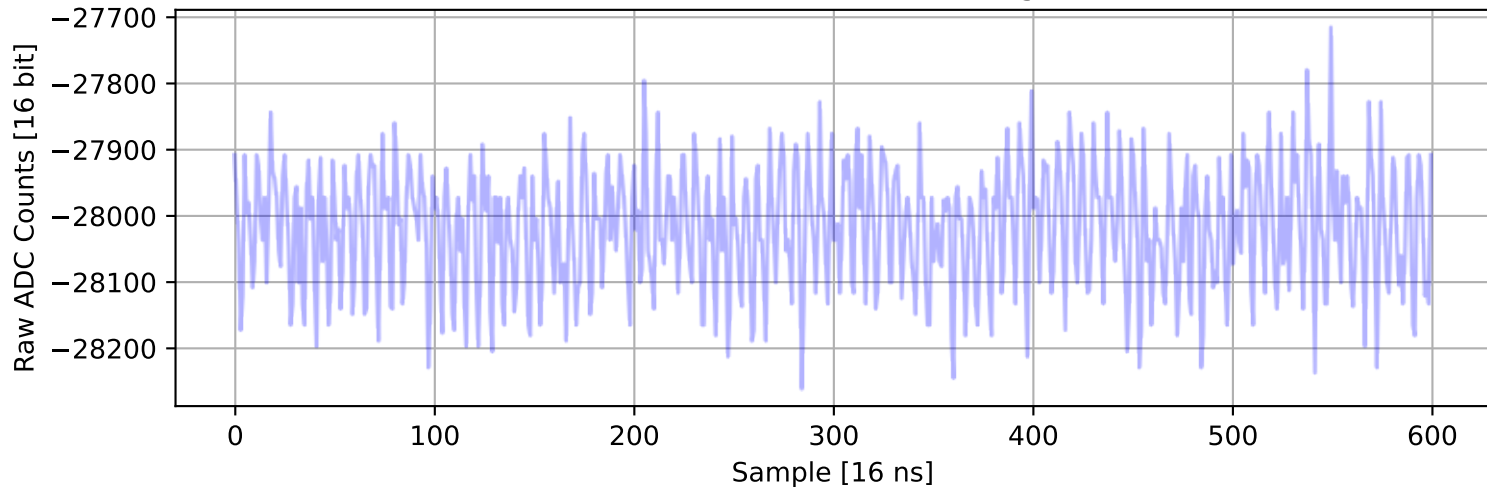


ADC 0, Channel 39, Event 110: Max Signal Waveform

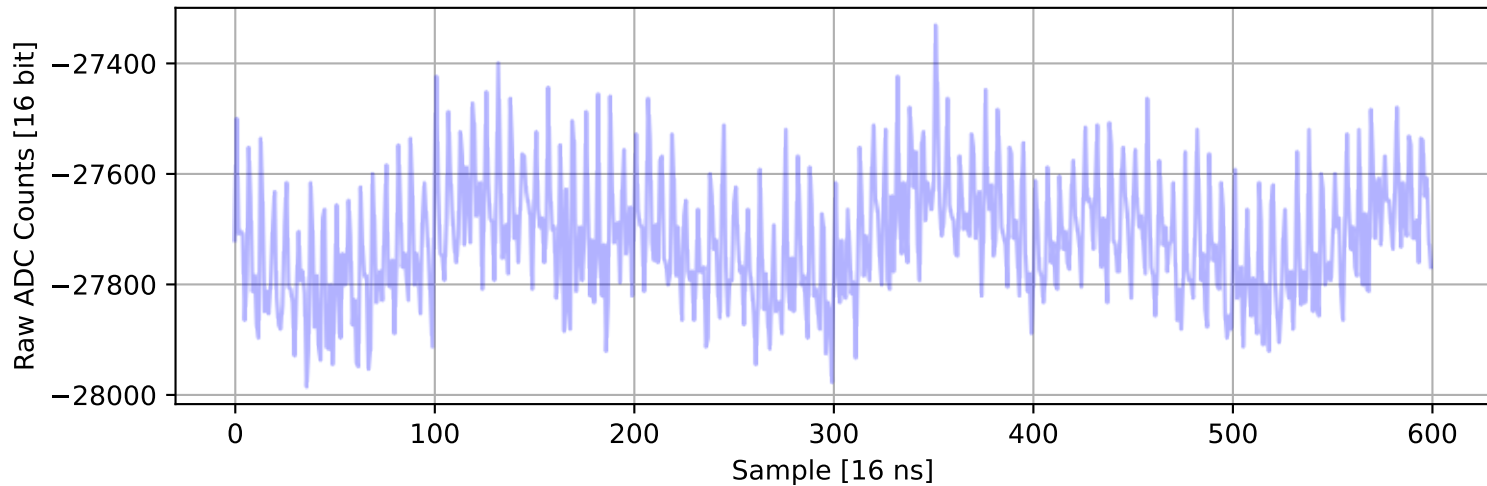




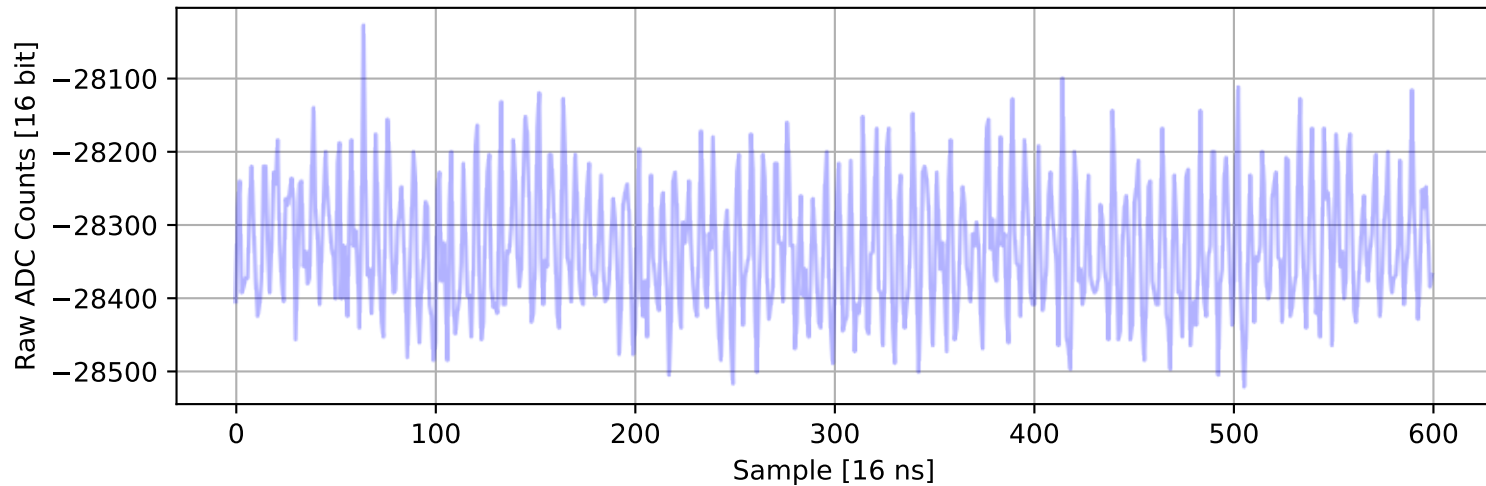
ADC 0, Channel 40, Event 103: Max Signal Waveform



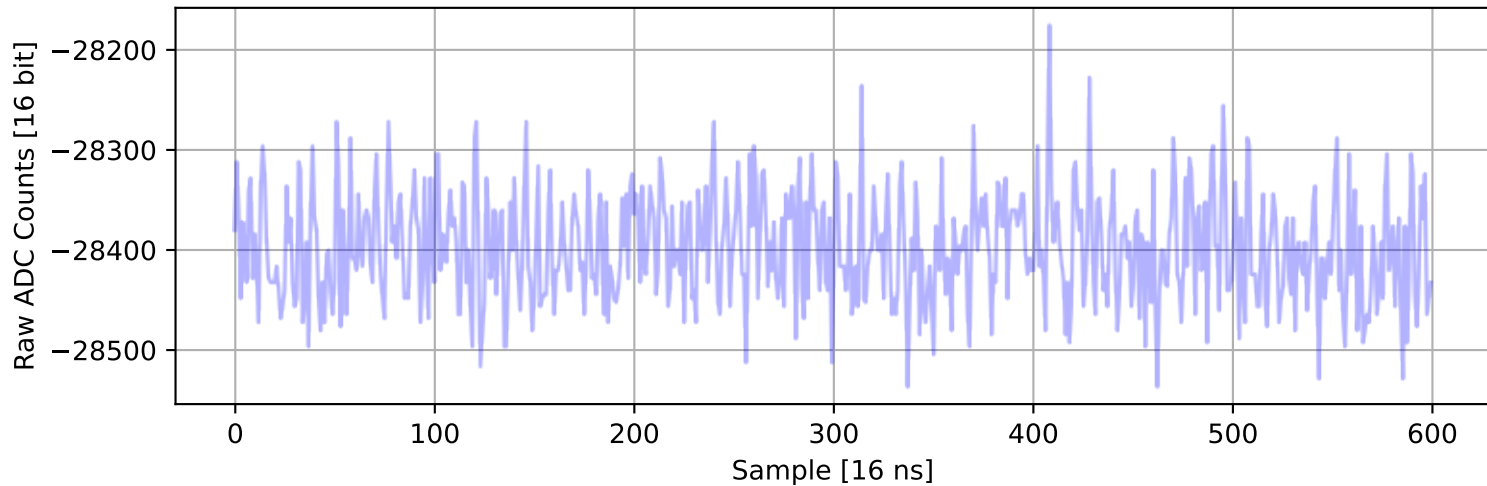
ADC 0, Channel 41, Event 186: Max Signal Waveform



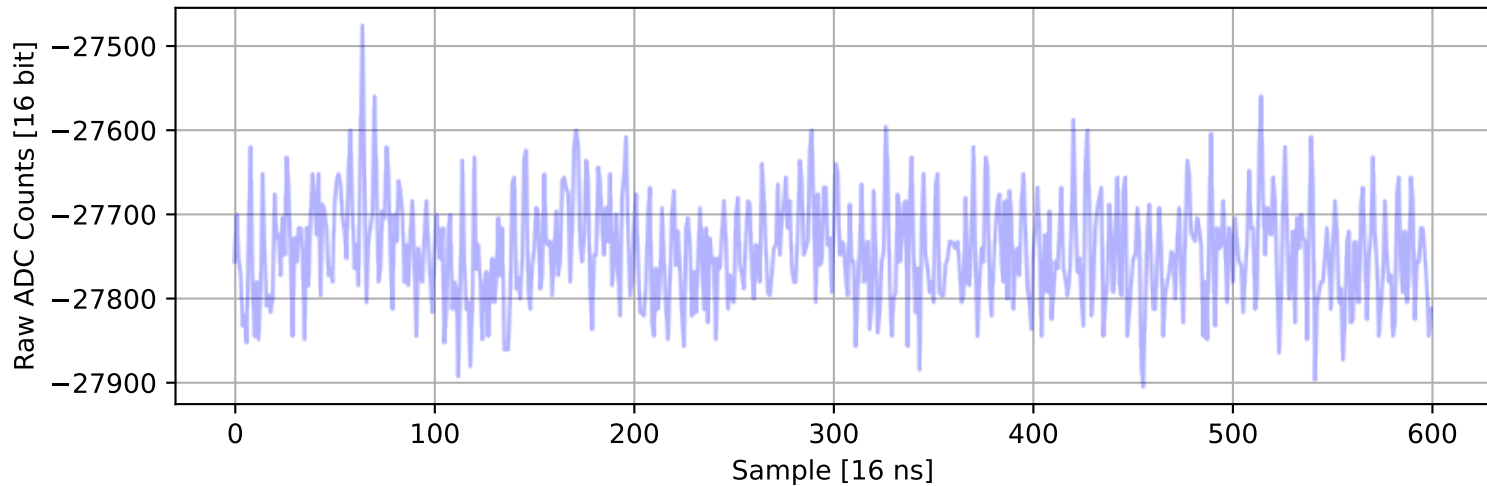
ADC 0, Channel 42, Event 47: Max Signal Waveform



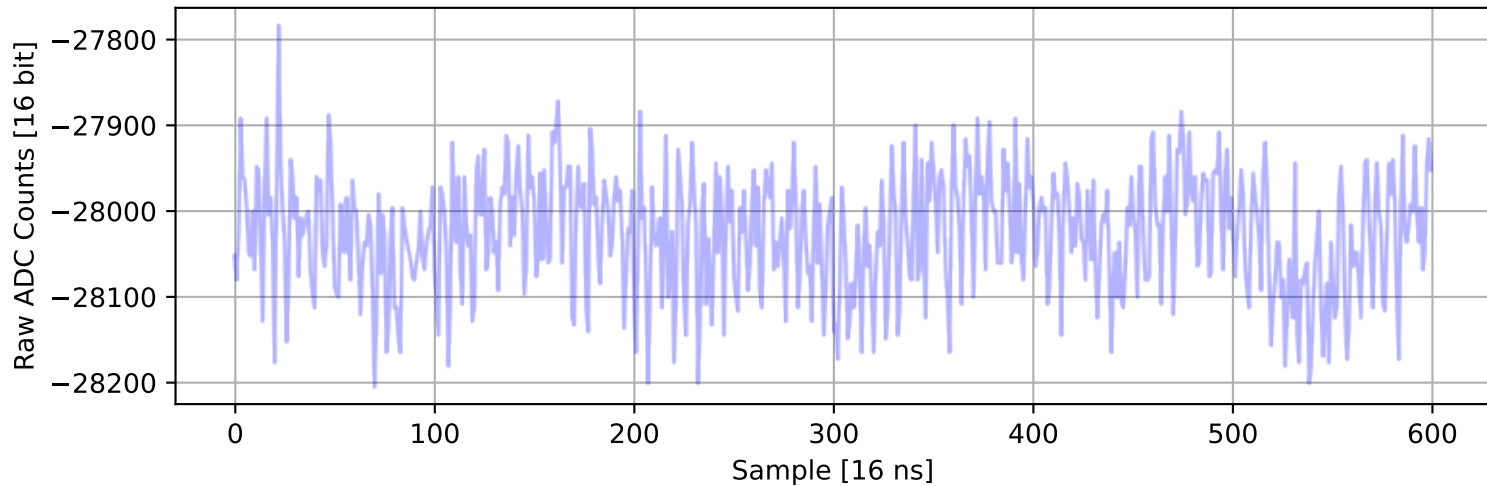
ADC 0, Channel 43, Event 120: Max Signal Waveform



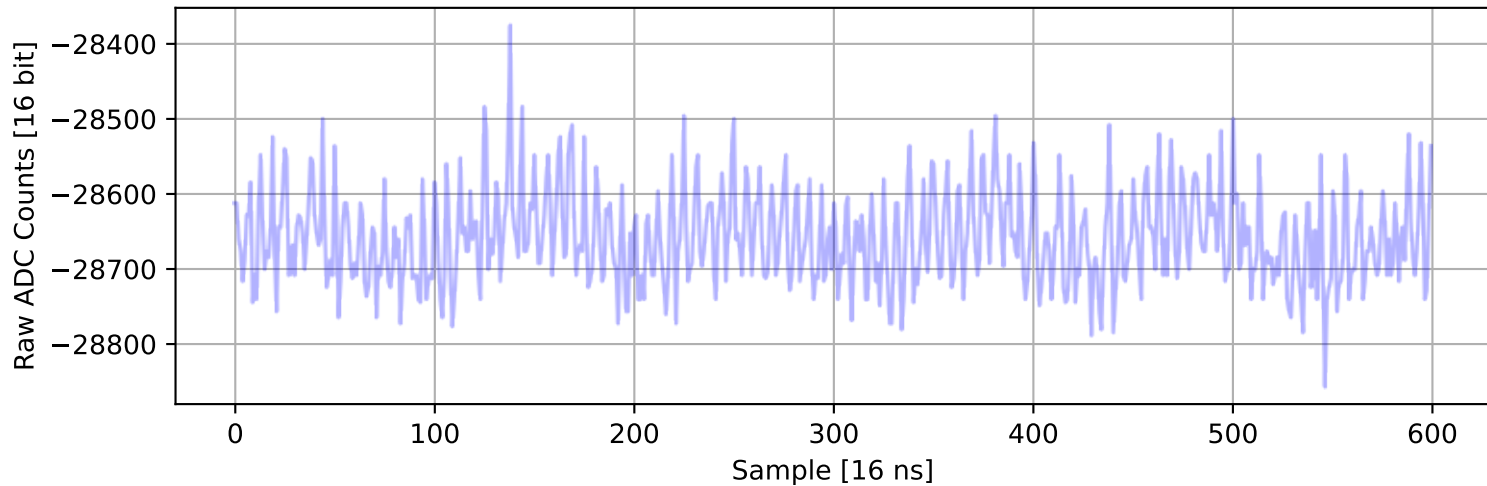
ADC 0, Channel 44, Event 3: Max Signal Waveform



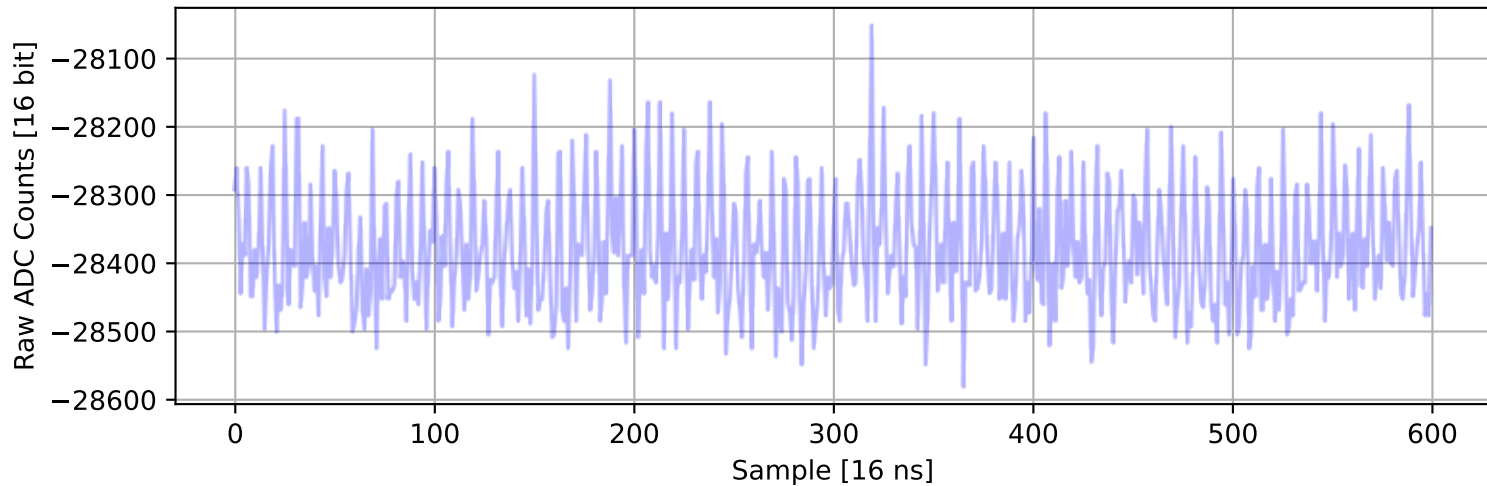
ADC 0, Channel 45, Event 33: Max Signal Waveform



ADC 0, Channel 46, Event 186: Max Signal Waveform

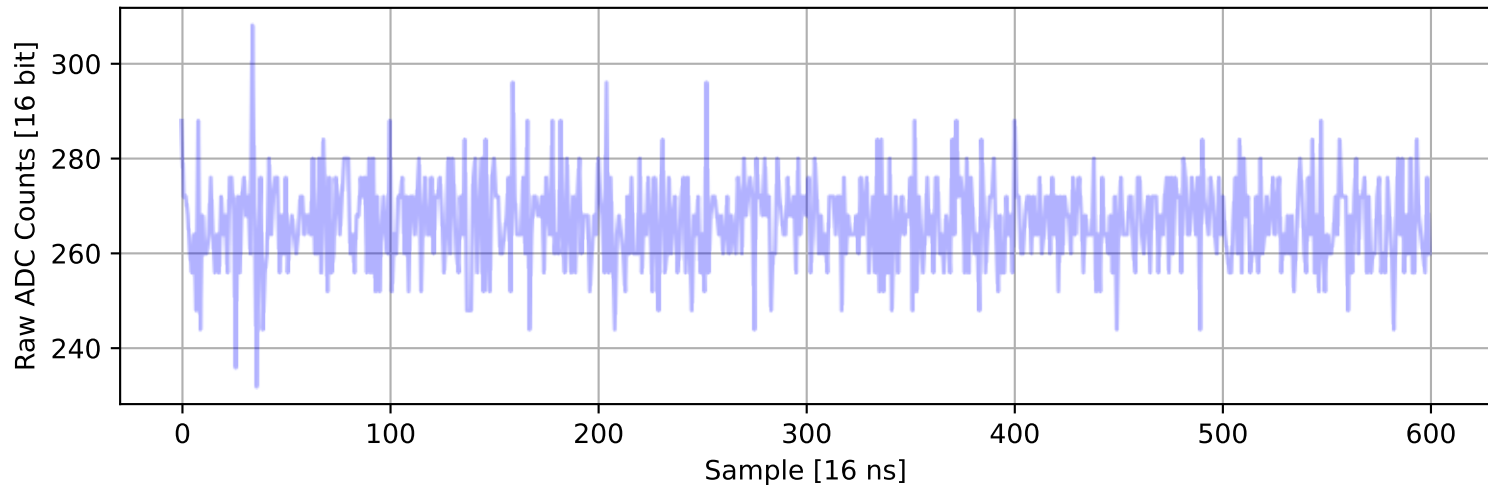


ADC 0, Channel 47, Event 19: Max Signal Waveform

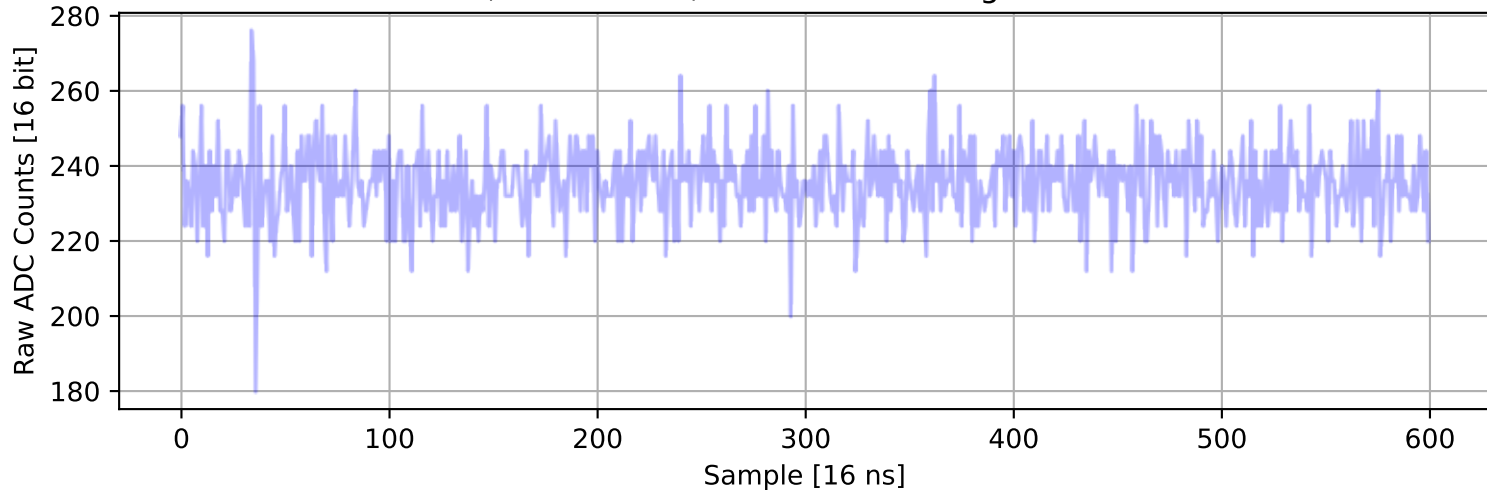




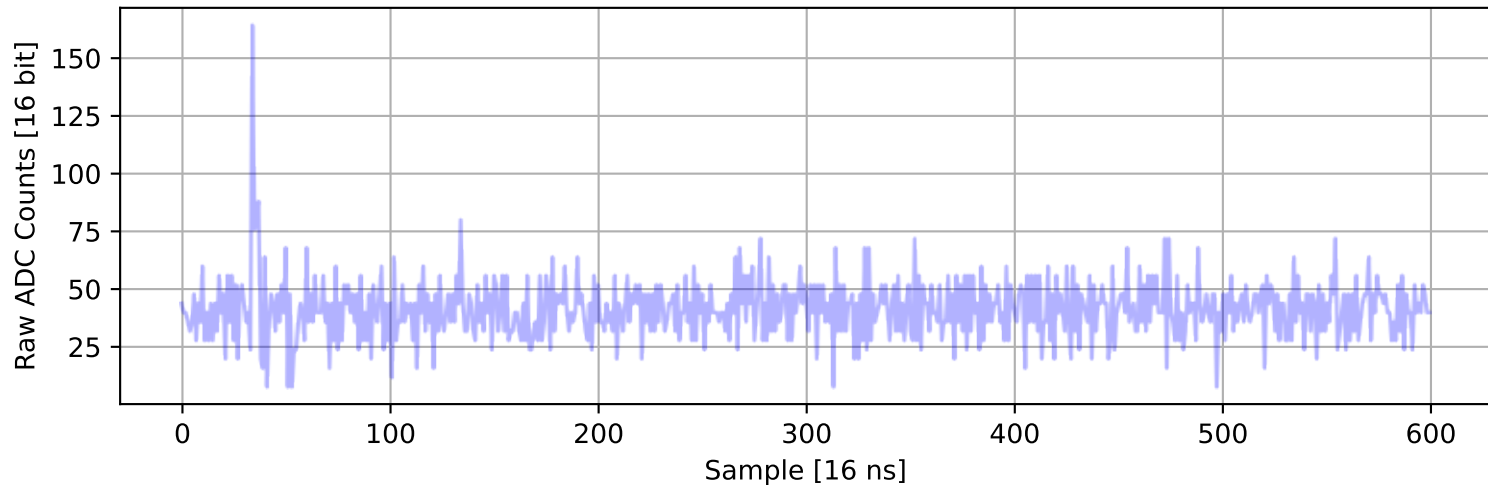
ADC 0, Channel 48, Event 41: Max Signal Waveform



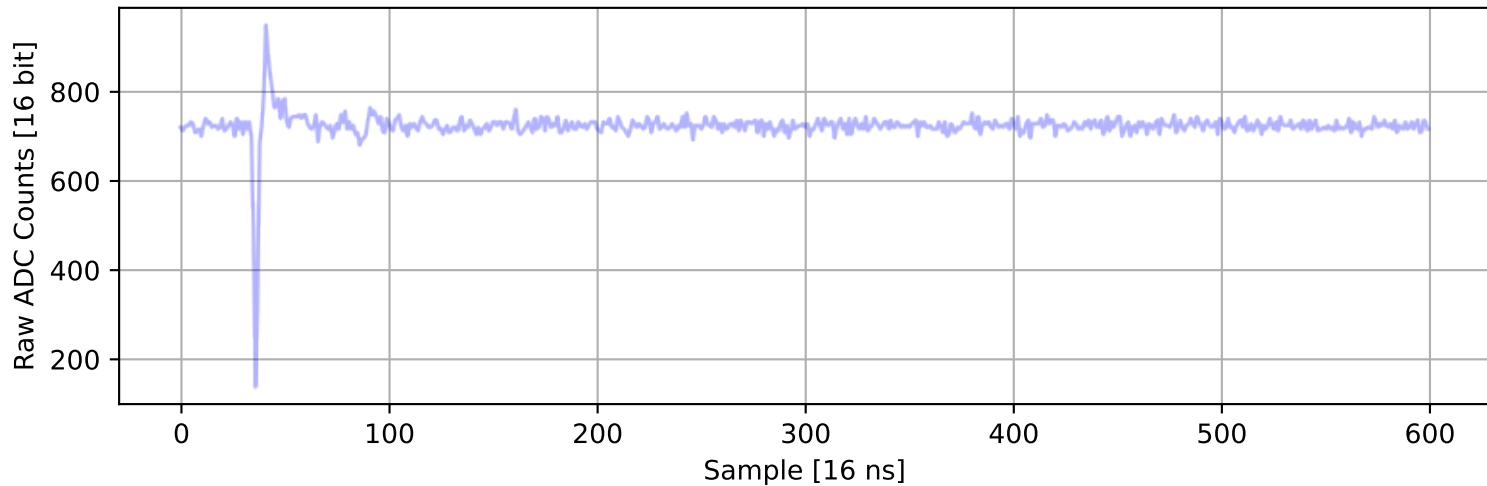
ADC 0, Channel 49, Event 41: Max Signal Waveform



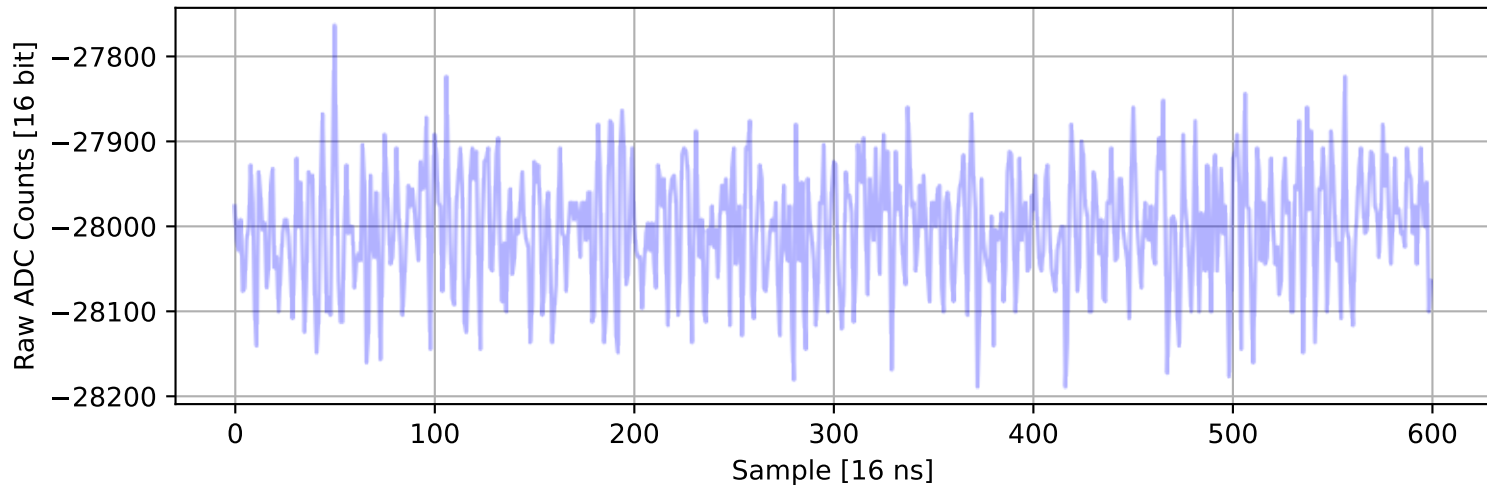
ADC 0, Channel 50, Event 41: Max Signal Waveform



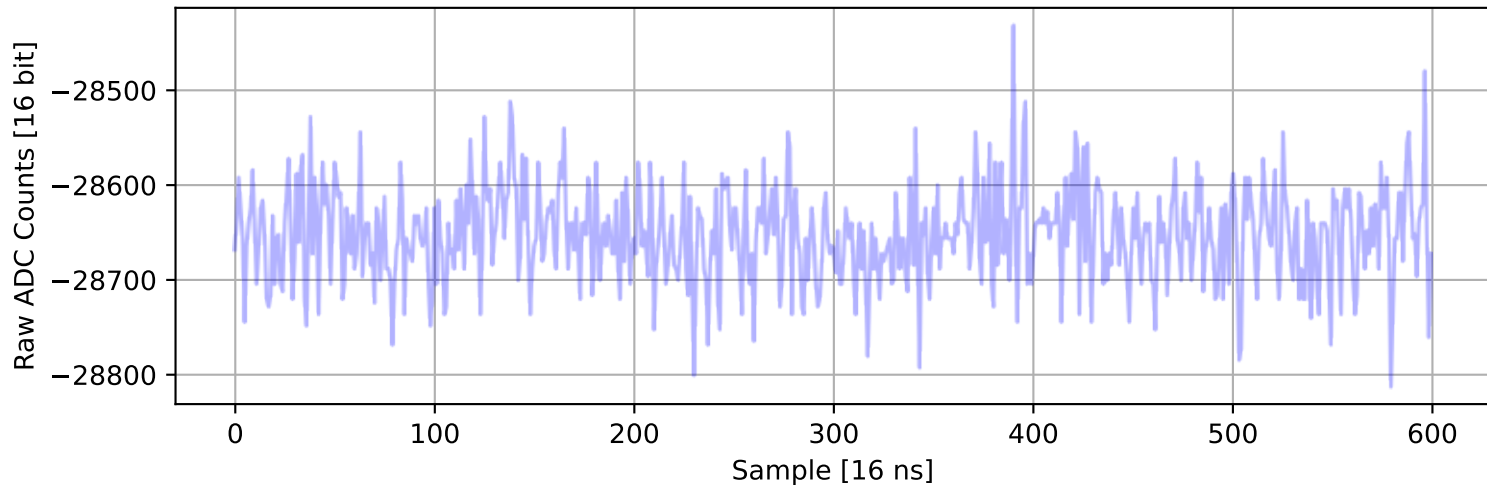
ADC 0, Channel 51, Event 148: Max Signal Waveform



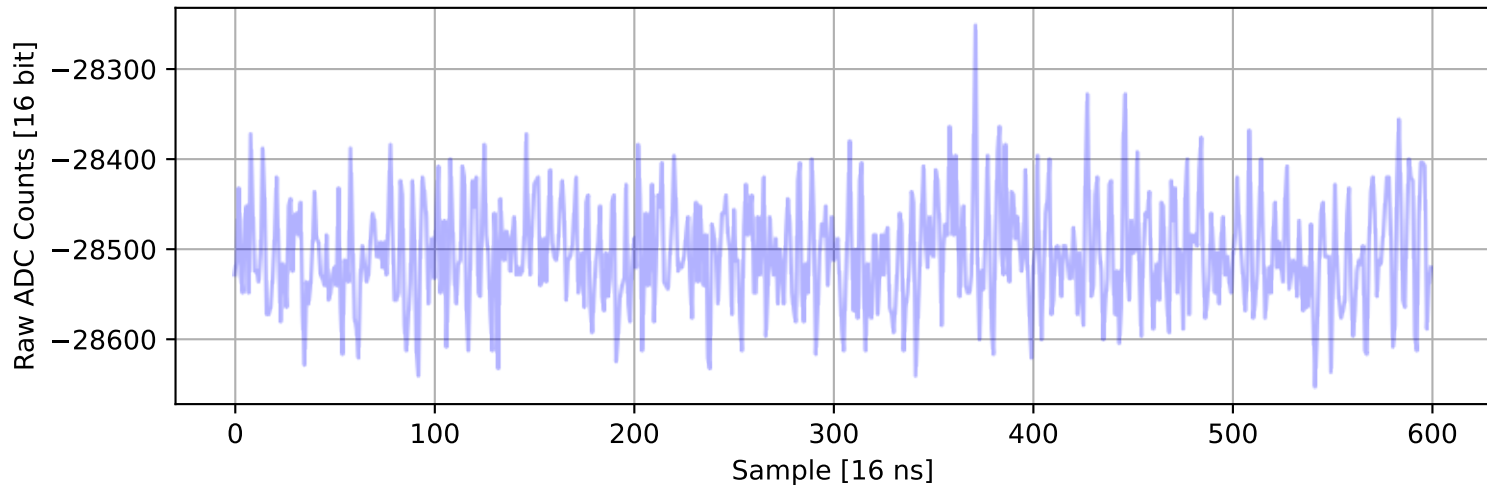
ADC 0, Channel 52, Event 138: Max Signal Waveform



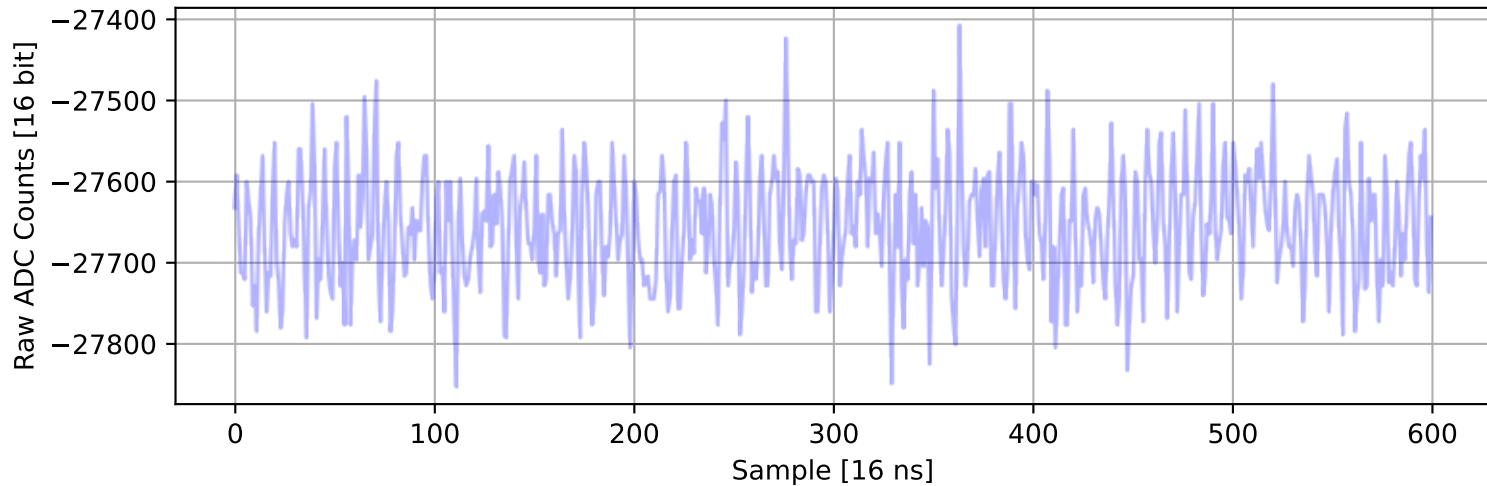
ADC 0, Channel 53, Event 0: Max Signal Waveform



ADC 0, Channel 54, Event 22: Max Signal Waveform

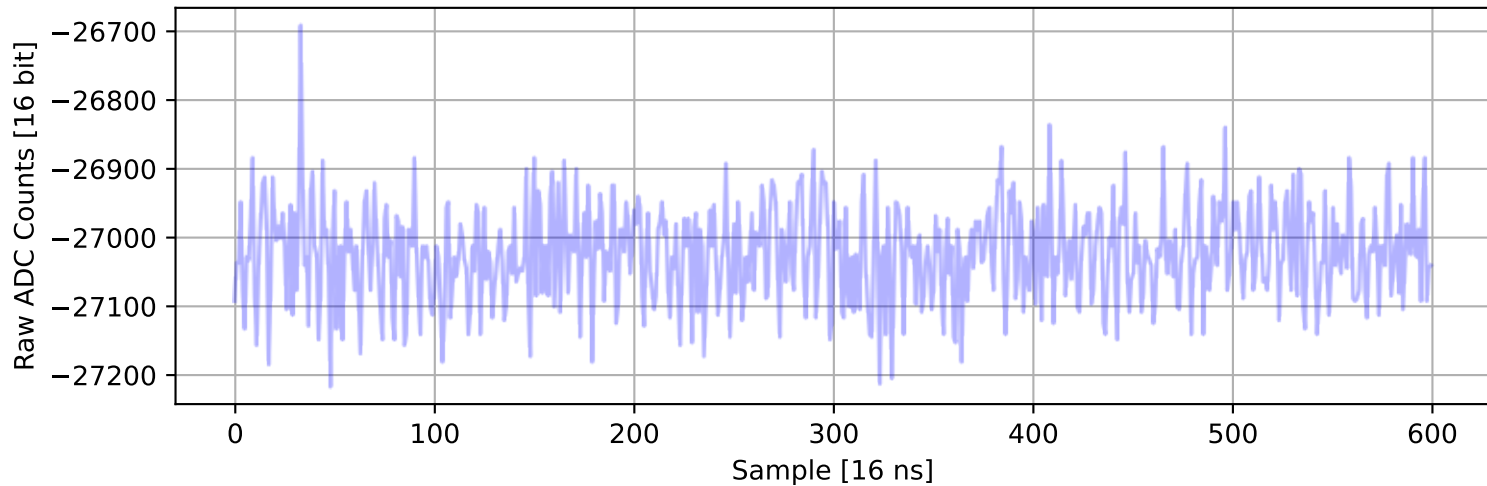


ADC 0, Channel 55, Event 118: Max Signal Waveform

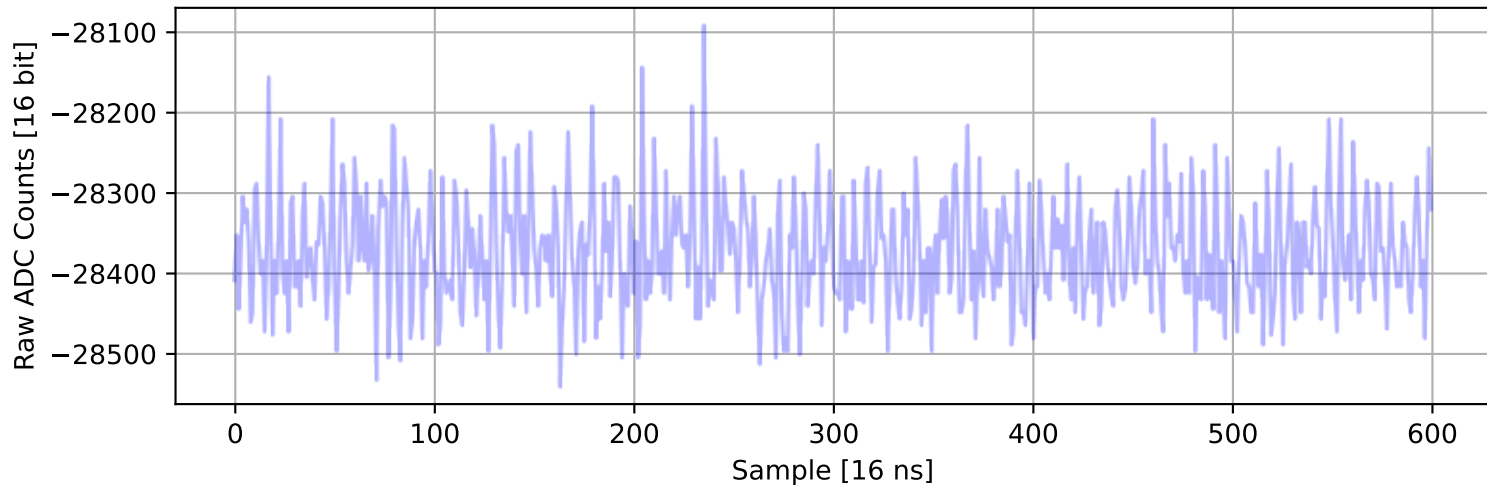




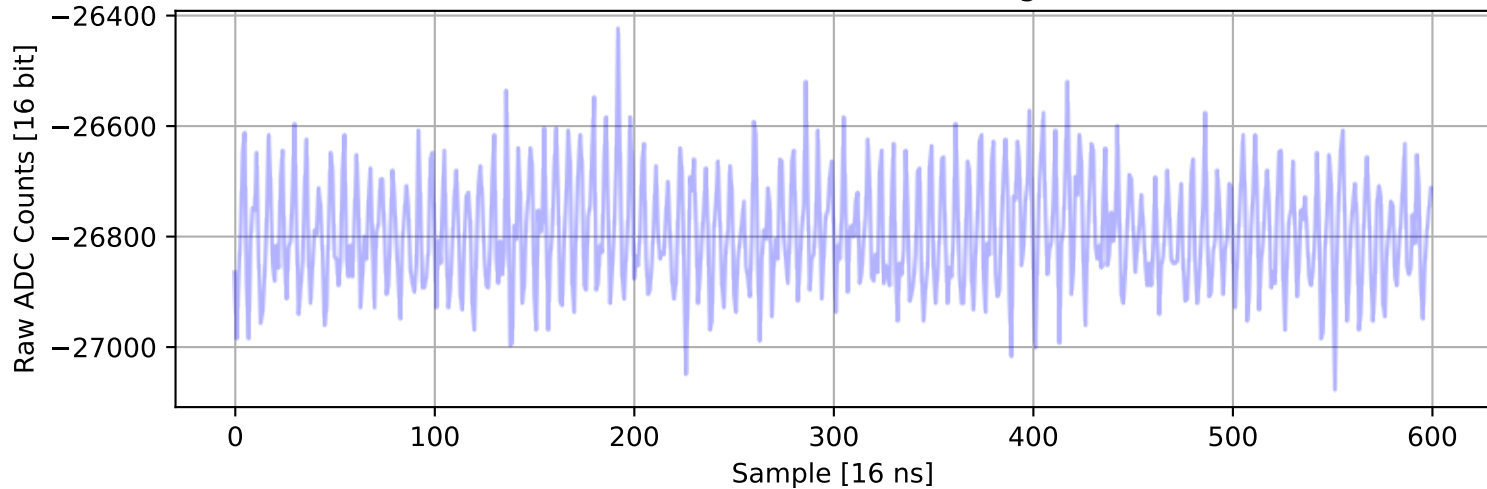
ADC 0, Channel 56, Event 122: Max Signal Waveform



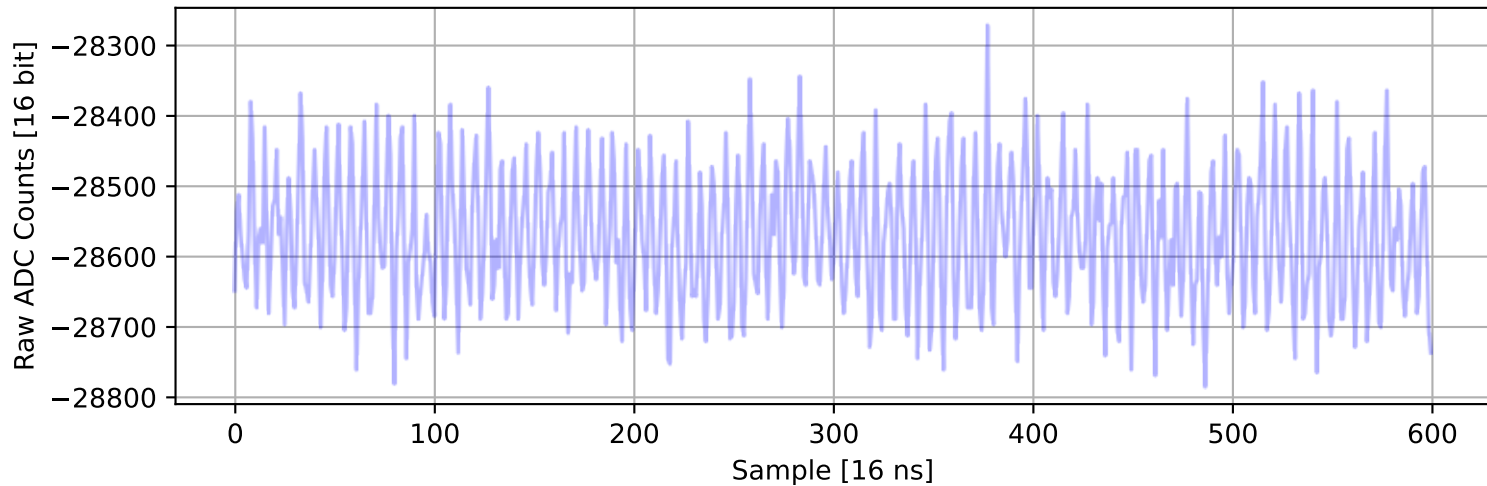
ADC 0, Channel 57, Event 75: Max Signal Waveform



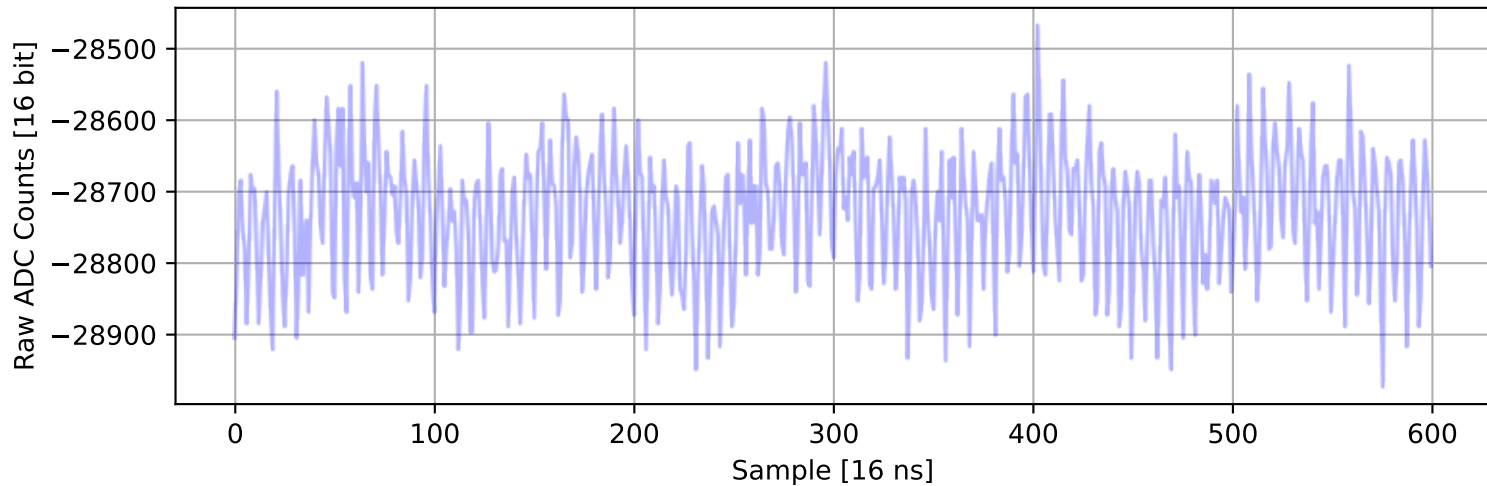
ADC 0, Channel 58, Event 103: Max Signal Waveform



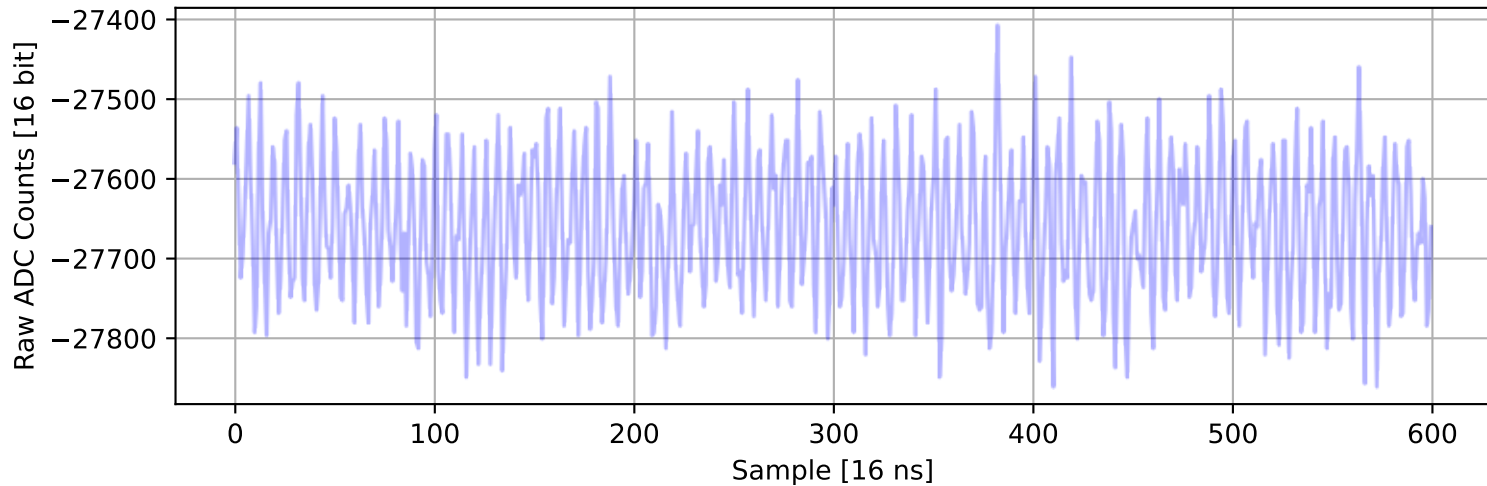
ADC 0, Channel 59, Event 72: Max Signal Waveform



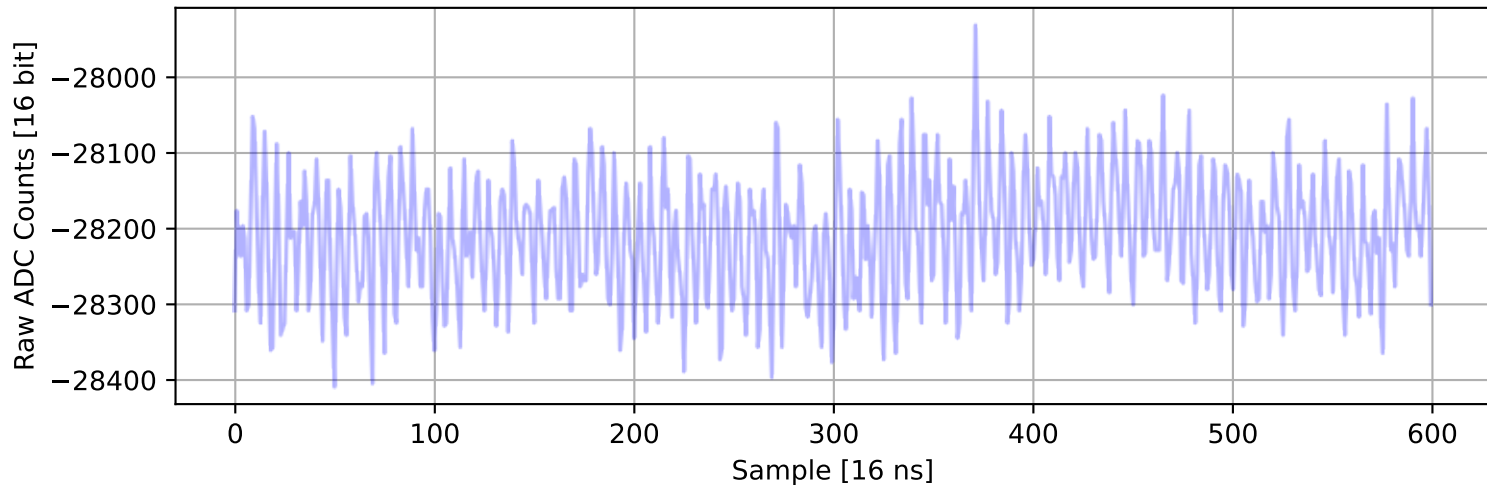
ADC 0, Channel 60, Event 162: Max Signal Waveform



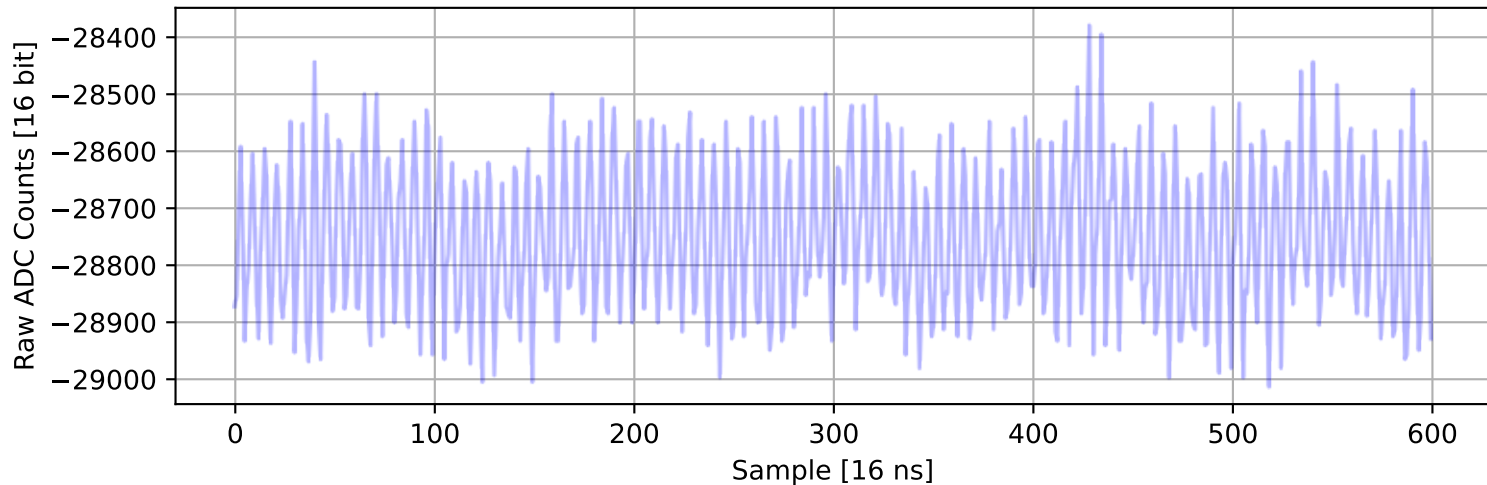
ADC 0, Channel 61, Event 14: Max Signal Waveform



ADC 0, Channel 62, Event 20: Max Signal Waveform

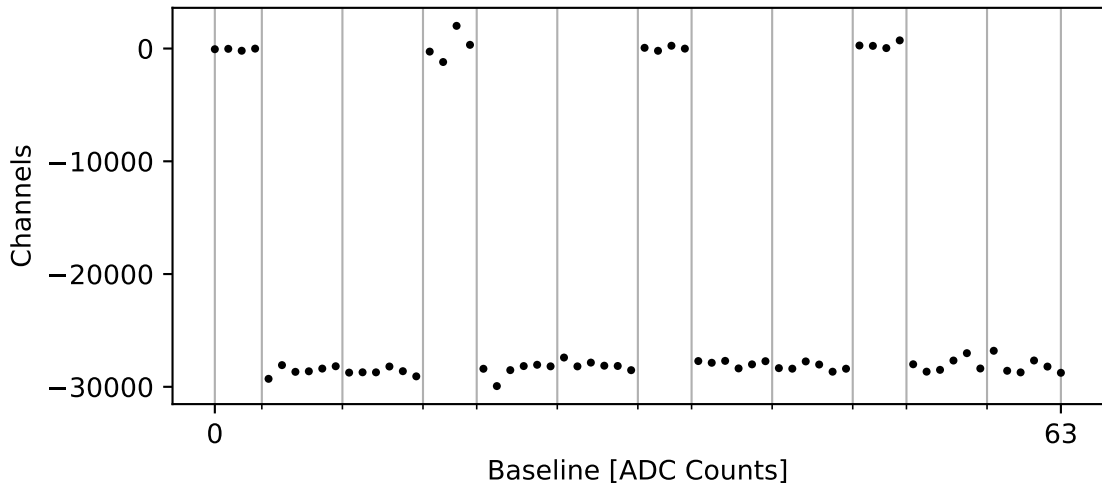


ADC 0, Channel 63, Event 173: Max Signal Waveform





ADC 0: Baseline Distribution



ADC 0: Max Signal Distribution

